

7. Schematic Diagram

7-1 Overall Block Diagram.....

7-2

7-2 FRONT.....

7-3

7-3 MAIN-1.....

7-4

7-4 MAIN-2.....

7-5

7-5 CD.....

7-6

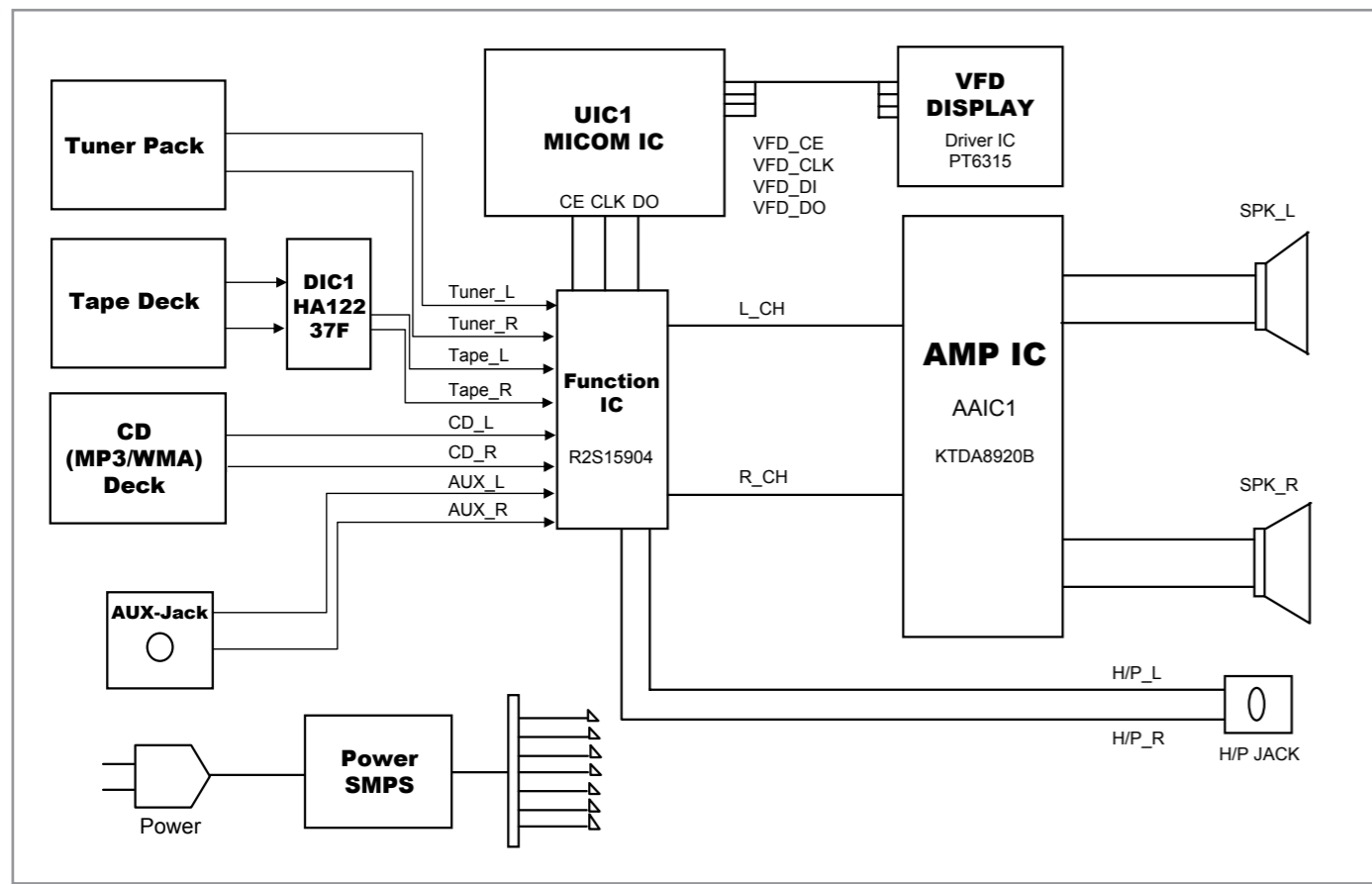
7-6 SMPS (MAX-A54).....

7-7

7-7 SMPS (MAX-A55).....

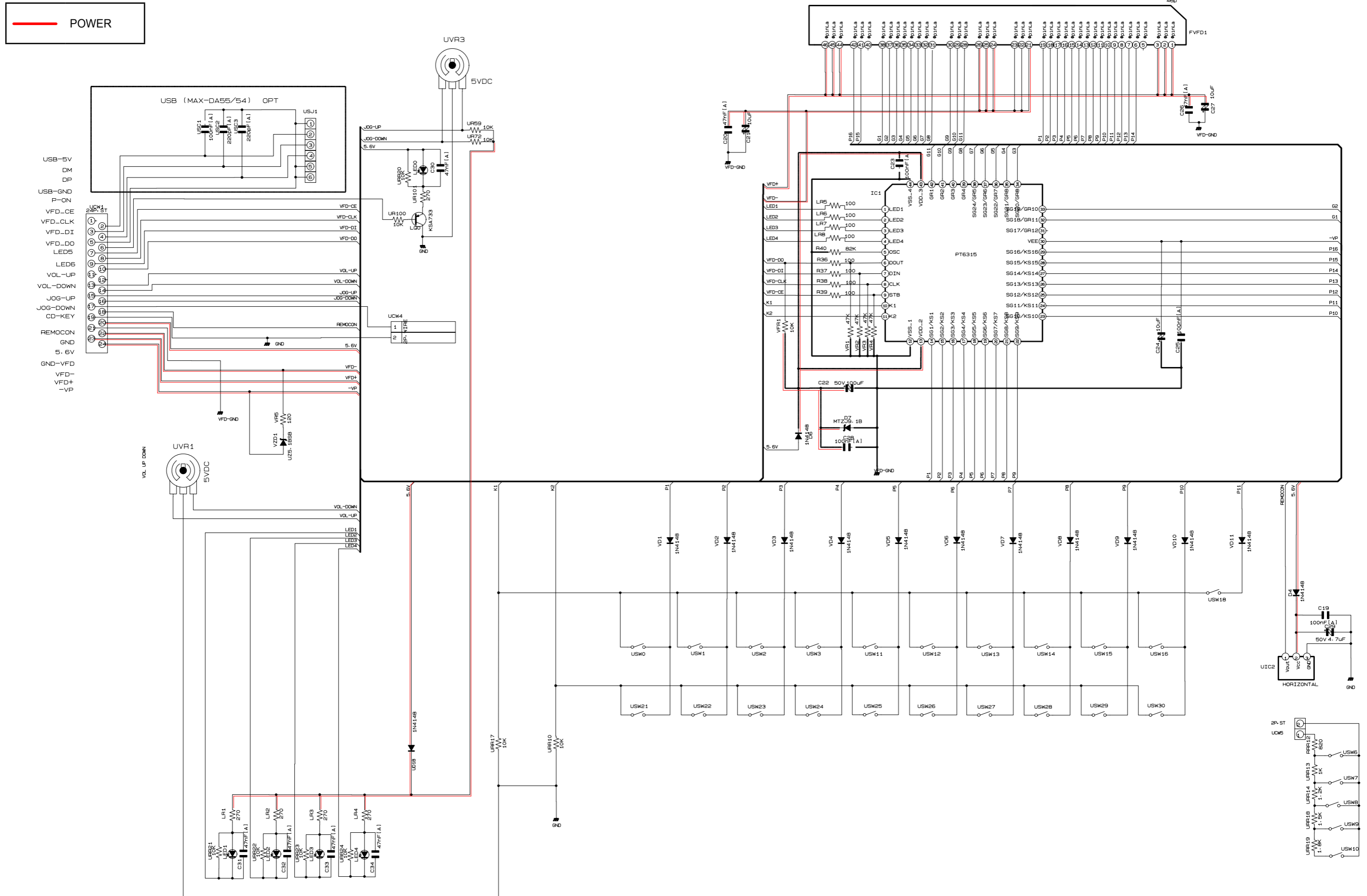
7-8

7-1 Overall Block Diagram

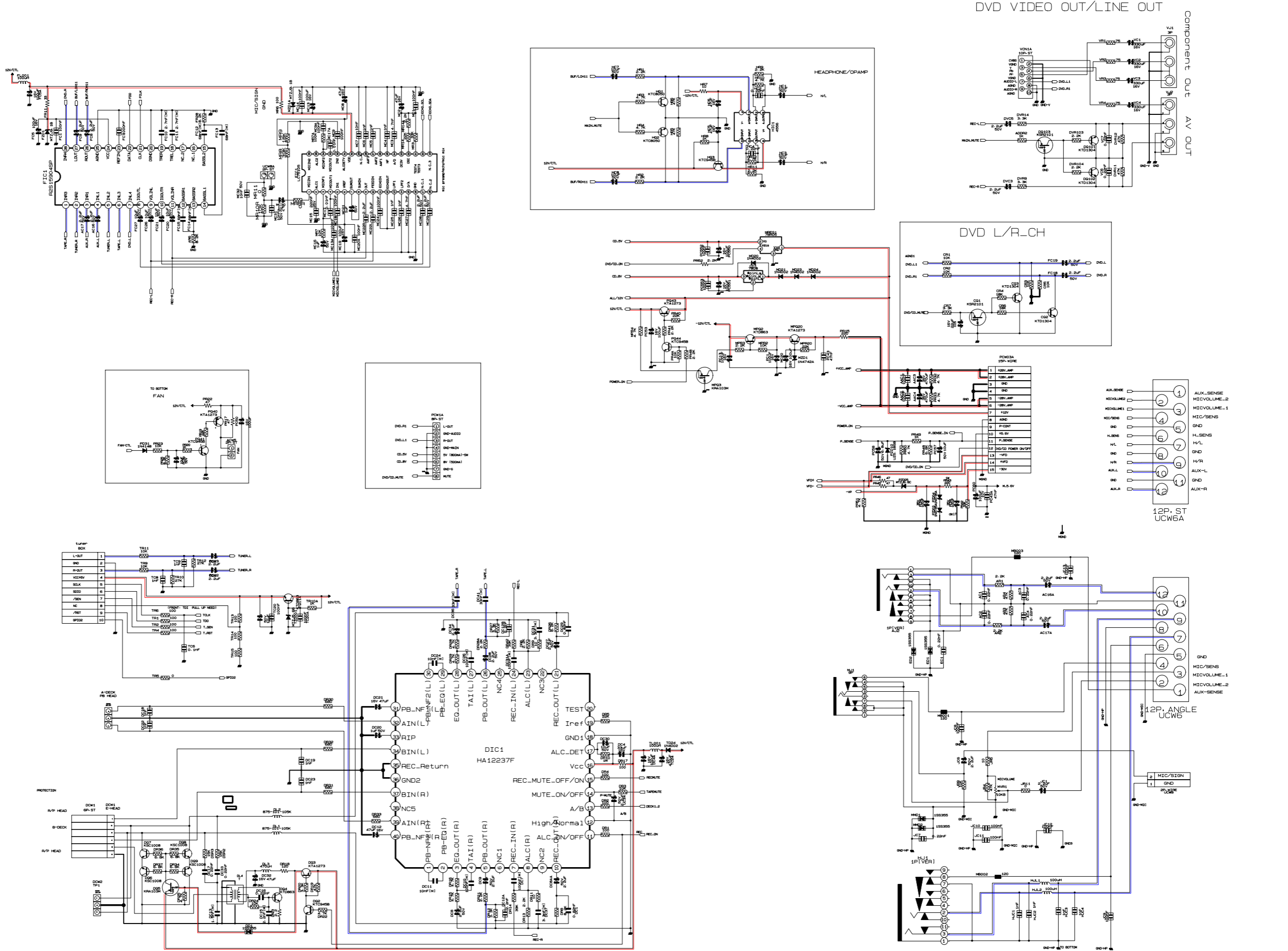


- Main System MICOM control Function IC (R2S15904), Tuner pack, Tape deck IC (DIC1HA12237F), CD deck. It gets all information from R2S15904. They have a communication among the with HOST ↔ SLAVE interface. And Main System MICOM gets the information from User's remote control. So whenever it gets the information, it displays it on the VFD.
- The CD Deck and Tape Deck transfer the audio data to R2S15904.
- AUX Jack and Tuner pack are each connected to R2S15904. Data from AUX and tuner transfer to R2S15904. Then, after processing, the data will send the date to AMP IC.

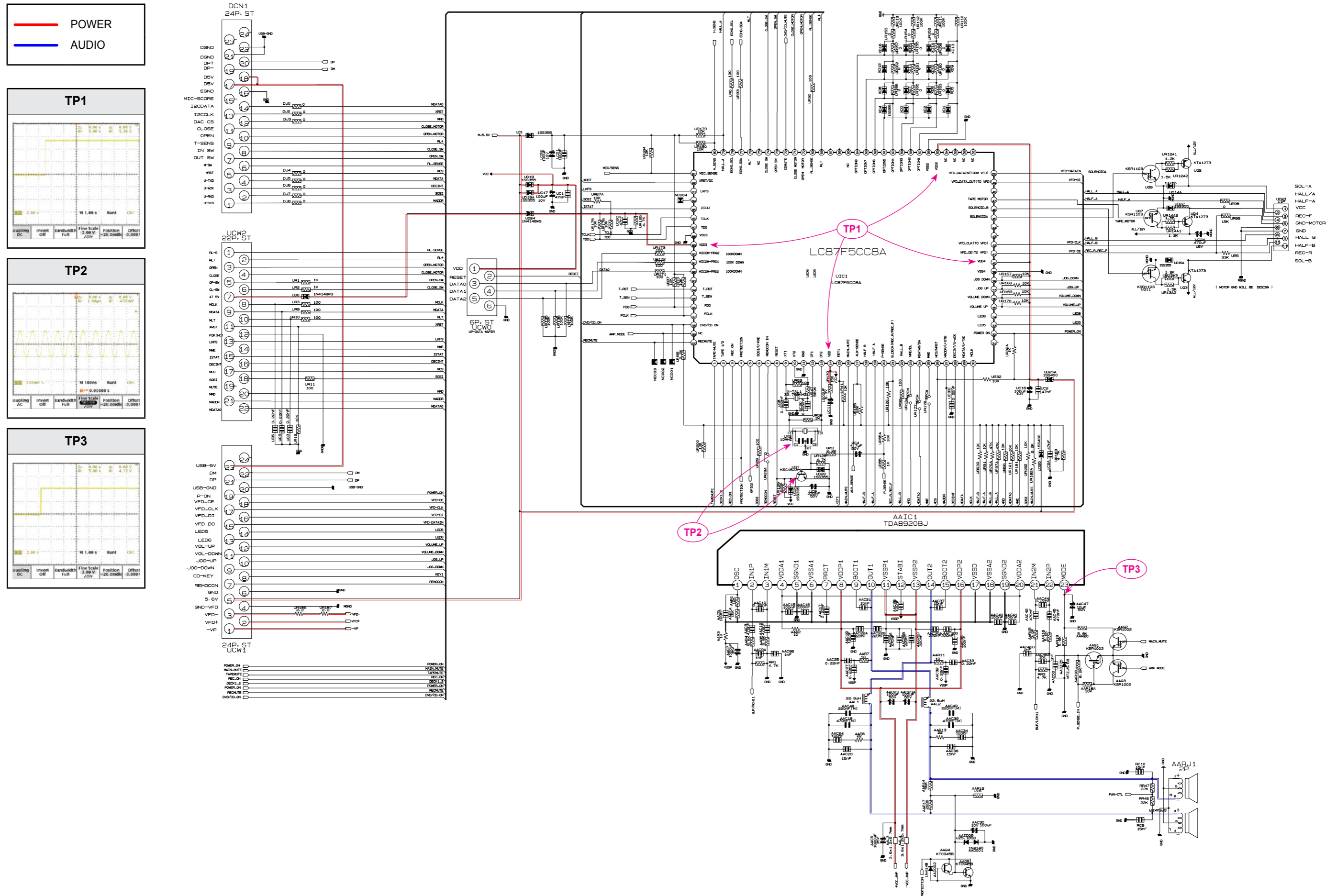
7-2 FRONT



7-3 MAIN-1

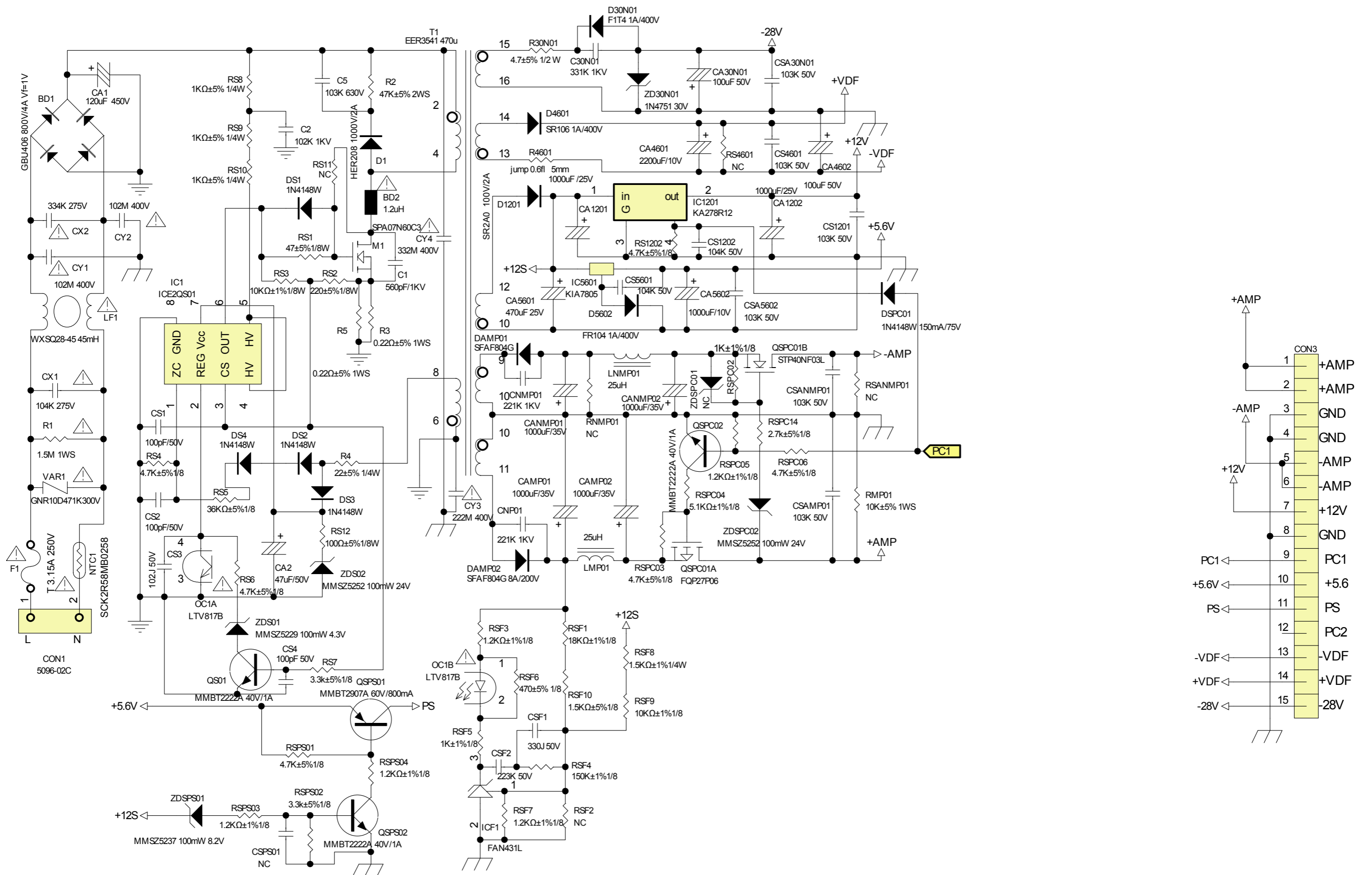


7-4 MAIN-2





7-6 SMPS (MAX-A54)



7-7 SMPS (MAX-A55)

