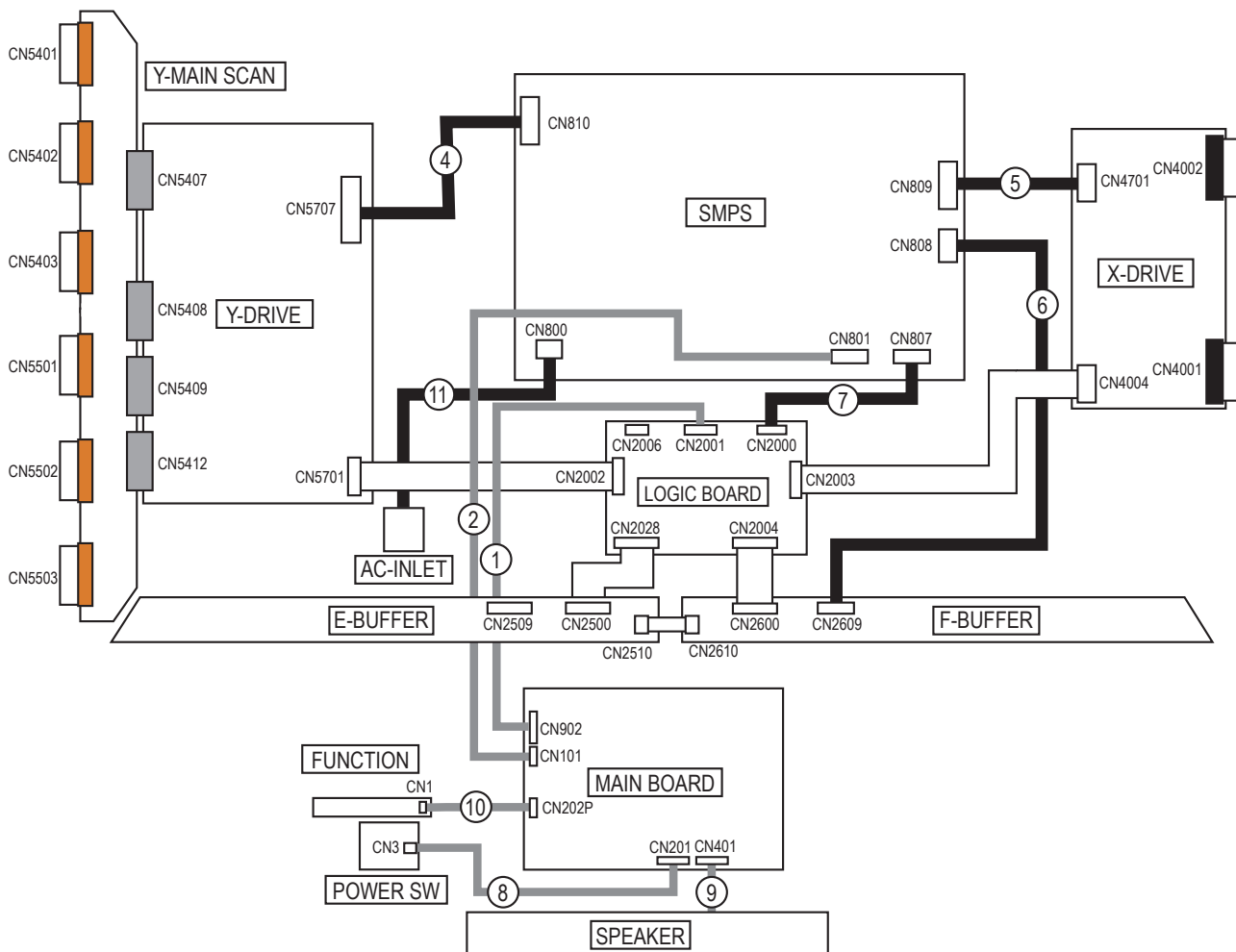


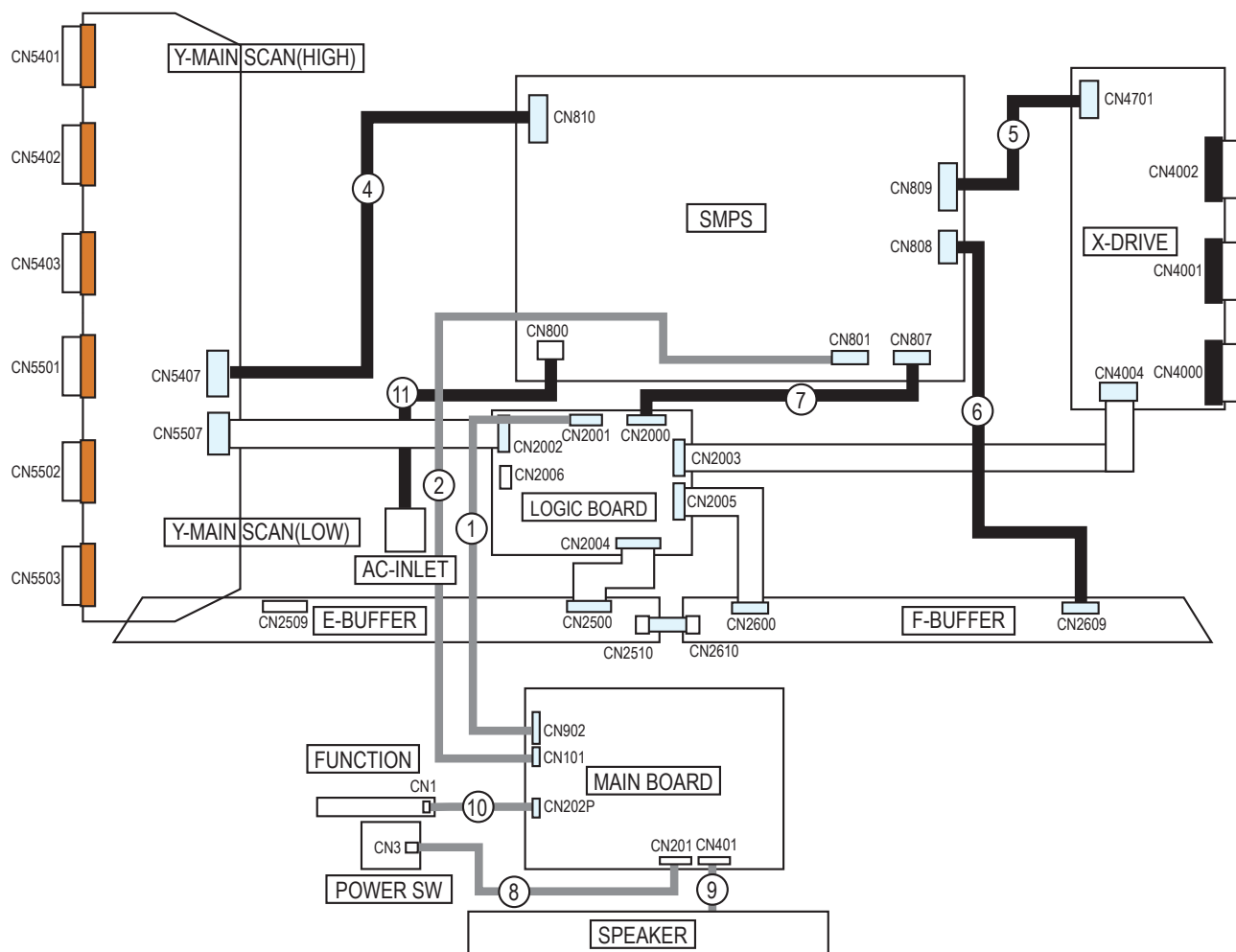
## 6. Wiring Diagram

### 6-1 Overall Wiring




<42" Overall Wiring>



## &lt;50" Overall Wiring&gt;



※ The code number of cable(Lead-connector) can be changed, see "5. Exploded View & Part List."

Use	① LVDS	⑧ POWER CABLE	⑪ AC INLET
Code	42" : BN96-07766M 50" : BN96-07766N	42" : BN39-00802C 50" : BN39-00802S	2901-001548
Photo			

## 6-1-1 Pin Connection

① CN902(MAIN B'D) ↔ CN2001(LOGIC B'D)			
Pin No.	Signal	Pin No.	Signal
1	RxIN1b-	16	GND
2	RxIN1b+	17	RxIN3-
3	RxIN0b-	18	RxIN3+
4	RxIN0b+	19	GND
5	RxIN0-	20	I2C_READY
6	RxIN0+	21	GND
7	GND	22	3D_SYNC
8	RxIN1-	23	GND
9	RxIN1+	24	UART Tx
10	GND	25	GND
11	RxIN2-	26	UART Rx
12	RxIN2+	27	Start_OPT
13	GND	28	SCL
14	RxCLKIN-	29	GND
15	RxCLKIN+	30	SDA

② CN101(MAIN B'D) ↔ CN801(MAIN SMPS)			
Pin No.	Signal	Pin No.	Signal
1	PS_ON	13	D5.3V
2	VS_ON	14	D5.3V
3	STBY	15	D5.3V
4	GND_STBY	16	D5.3V
5	GND_15Vamp	17	GND_15V
6	GND_15Vamp	18	GND_15V
7	15Vamp	19	D15V
8	15Vamp	20	GND_15V
9	GND_5.3V	21	D15V
10	GND_5.3V	22	D15V
11	GND_5.3V	23	AC_DET
12	GND_5.3V	24	NC[Vt]

④ CN810(SMPS) ↔ CN5707_42"(Y B'D) CN5407_50"(Y B'D)	
Pin No.	Signal
1	Vs
2	Vs
3	GND
4	Vg
5	GND
6	Va

⑤ CN809(SMPS) ↔ CN4701(X B'D)	
Pin No.	Signal
1	Vs
2	Vs
3	GND
4	GND
5	Vg

⑦ CN807(SMPS) ↔ CN2000(LOGIC B'D)	
Pin No.	Signal
1	D5.3V
2	D5.3V
3	GND
4	GND
5	Vg

⑧ CN201(MAIN B'D) ↔ POWER&IR	
Pin No.	Signal
1	IR
2	GND
3	A5V
4	LED_STB
5	BUZZER
6	KEY_INPUT1
7	KEY_INPUT2
8	GND
9	NC
10	NC

⑨ CN401(MAIN B'D) ↔ SPEAKER	
Pin No.	Signal
1	R+_OUT
2	R-_OUT
3	L+_OUT
4	L-_OUT

⑪ CN800(SMPS) ↔ AC INLET	
Pin No.	Signal
1	AC Neutral
2	N/C
3	AC Live

**6-1-2 Connector role**

42" Loc. No.	50" Loc. No.	Description
CN5401	CN5401	Horizontal Y-scan line(1~128) of Module and Y-Main Scan Connect
CN5402	CN5402	Horizontal Y-scan line(129~256) of Module and Y-Main Scan Connect
CN5403	CN5403	Horizontal Y-scan line(256~384) of Module and Y-Main Scan Connect
-	CN5512	Y-Main Scan(High) and Y-Main Scan(Low) Connect
CN5501	CN5501	Horizontal Y-scan line(384~512) of Module and Y-Main Scan Connect
CN5502	CN5502	Horizontal Y-scan line(512~640) of Module and Y-Main Scan Connect
CN5503	CN5503	Horizontal Y-scan line(640~768) of Module and Y-Main Scan Connect
CN5407	CN5407	Upper Y-Drive and Y-Main Scan Connect
CN5507	CN5507	Lower Y-Drive and Y-Main Scan Connect
CN5707	CN5507	Vs(205V),Vg(15v) Power input connect(6Pin) of Y-Drive
CN5701	CN5701	Y-Drive control signal from Logic Board
CN810	CN810	Vs(205V),Vg(15v) Power input connect(6Pin) of SMPS for Y-Drive
CN809	CN809	Vs(205V),Vg(15v) Power input connect(6Pin) of SMPS for X-Drive
CN808	CN808	Va(63V) ,5.3V Power input connect(3Pin) of SMPS for F-Buffer
CN807	CN807	Power input connect(10pin) for Logic Board
CN801	CN801	Image signal(LVDS) connect(41pin) from Main Board
CN800	CN800	AC Power input connect from AC-inlet
CN4002	CN4002	Horizontal X-scan line of Module and X-scan Connect(first Block)
CN4001	CN4001	Horizontal X-scan line of Module and X-scan Connect(second Block)
-	CN4000	Horizontal X-scan line of Module and X-scan Connect(third Block)
CN2000	CN2000	Power input connect(10pin) of Logic Board from SMPS
CN2001	CN2001	Image signal(LVDS) connect(41pin) of Logic board from Main Board
CN2002	CN2002	Y-Drive control signal of Logic Board
CN2004	CN2005	Address Data(684th~1366th) connect for F-Buffer board
CN2028	CN2004	Address Data(1st~683th) connect for E-Buffer board
CN2500	CN2500	Address Data(1st~683th) connect from Logic Board
CN2510	CN2510	Power input connect from F-Buffer Board
CN2610	CN2610	Power input connect to E-Buffer Board
CN2600	CN2600	Address Data(684th~1366th) connect from Logic board
CN2609	CN2609	Va(63V) ,5.3V Power input connect(3Pin) from SMPS
CN1101	CN1101	Power input connect(24Pin) from SMPS
CN2202	CN2202	Image signal(LVDS) connect(41pin) for Logic board
CN1605	CN1605	Function input(source,ch up/down...) connect on Main board
CN1404	CN1404	Video signal input connect form Side AV ass'y
CN1606	CN1606	Power SW input connect on Main Board
CN1203	CN1203	Speak out connect on Main Board
CN101	CN101	Video signal input connect on Side AV ass'y
CN1	CN1	Function input(source,ch up/down...) connect to Main board
CN3	CN3	Power SW input connect to Main Board