

TOSHIBA

FILE NO. 810-200429

CIRCUIT DIAGRAMS



SPATIALIZER[®]
VIRTUAL SURROUND



DOLBY
DIGITAL

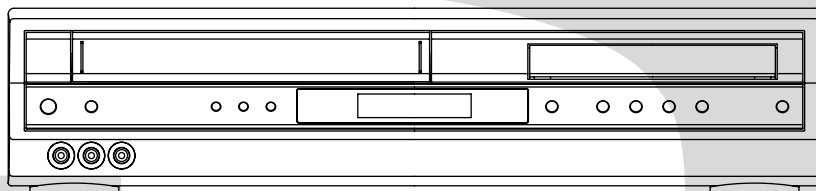


Hi-Fi SQPB

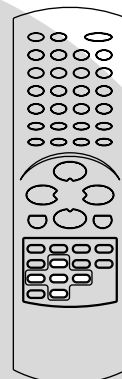
DVD VIDEO PLAYER & VIDEO CASSETTE RECORDER

SD-V392SUA

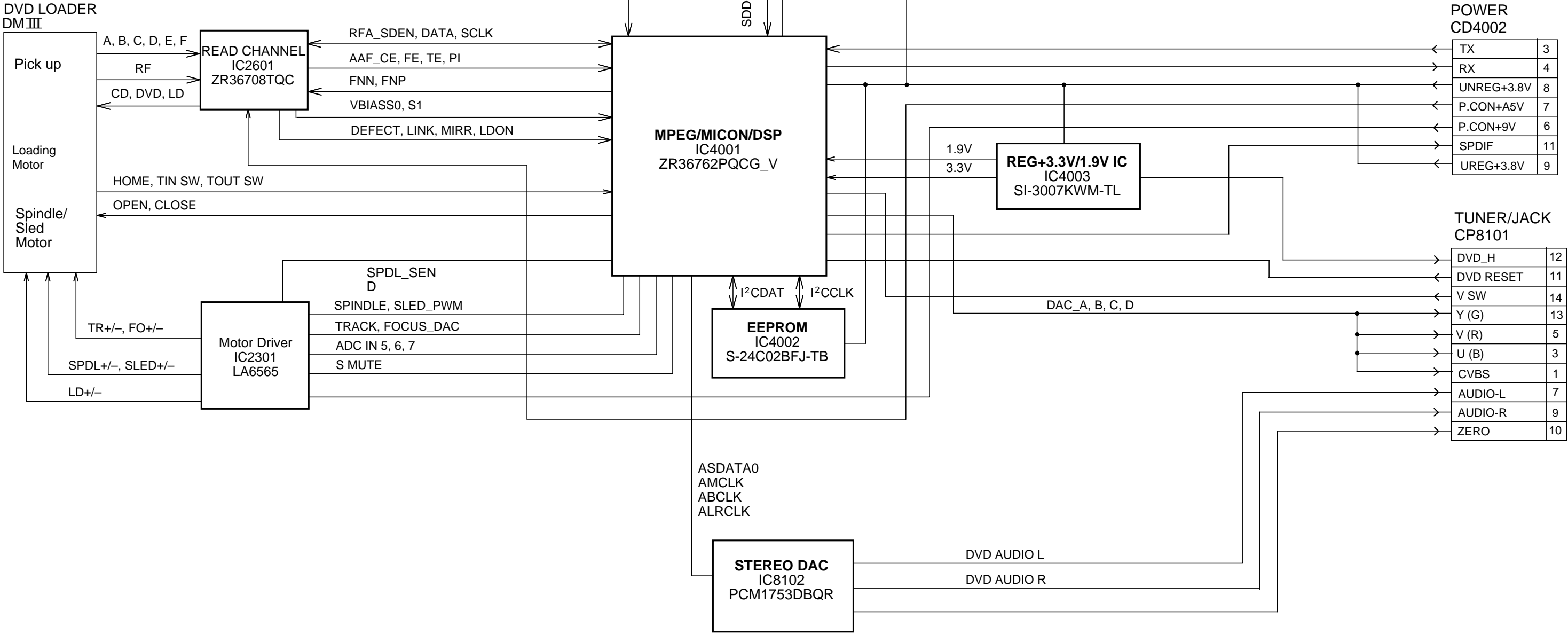
SD-V392SCA



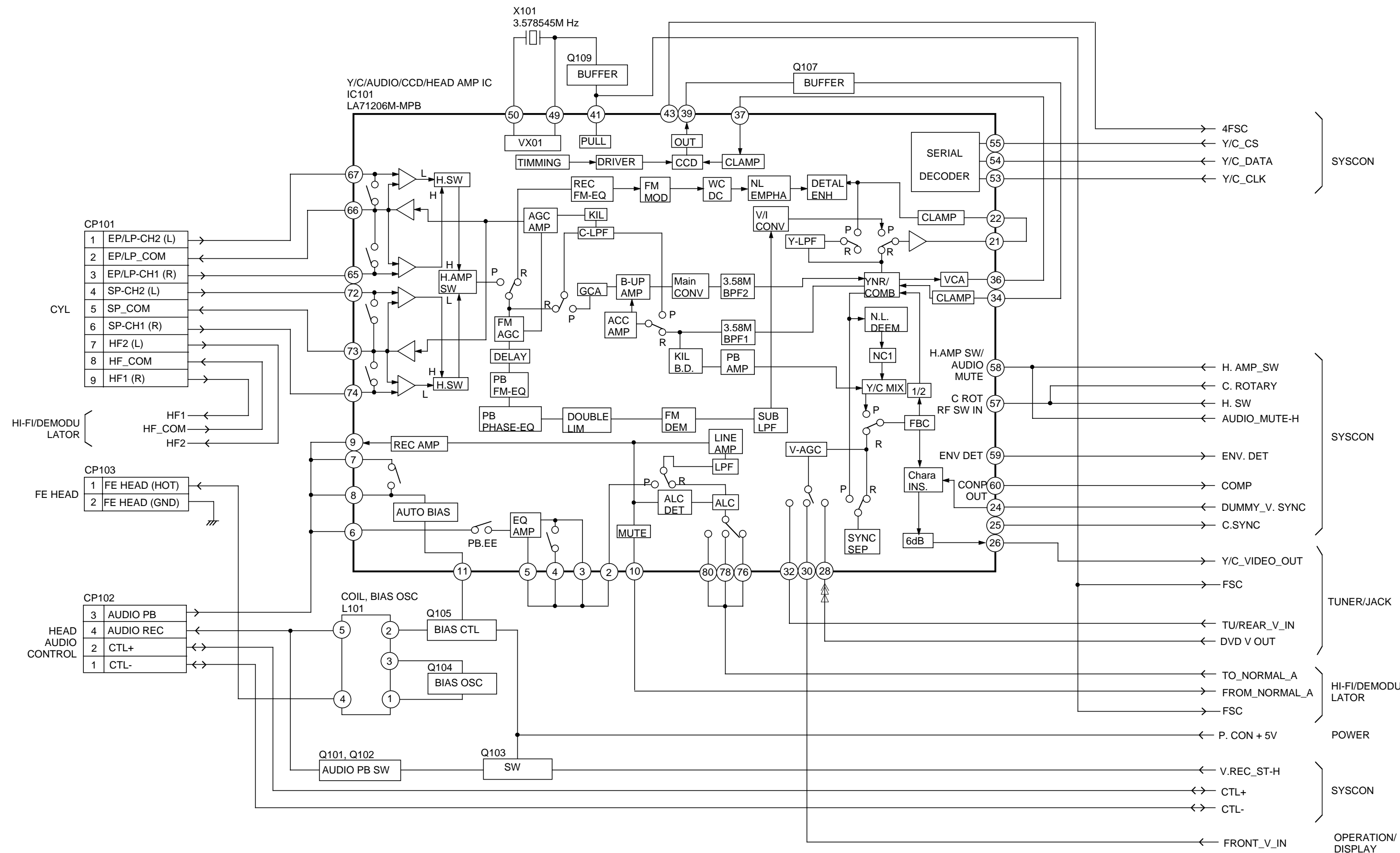
(SD-V392SUA)



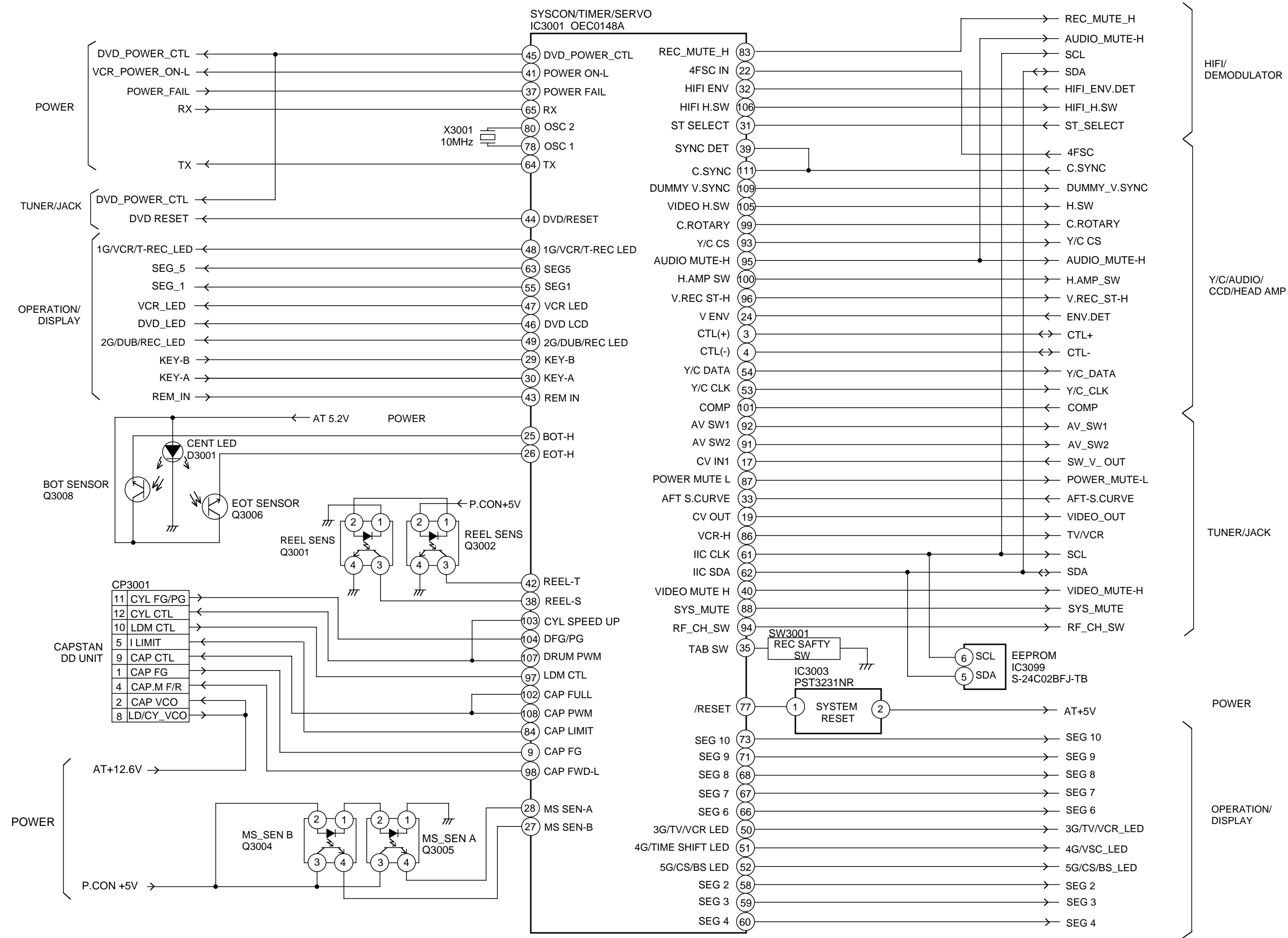
DVD BLOCK DIAGRAM



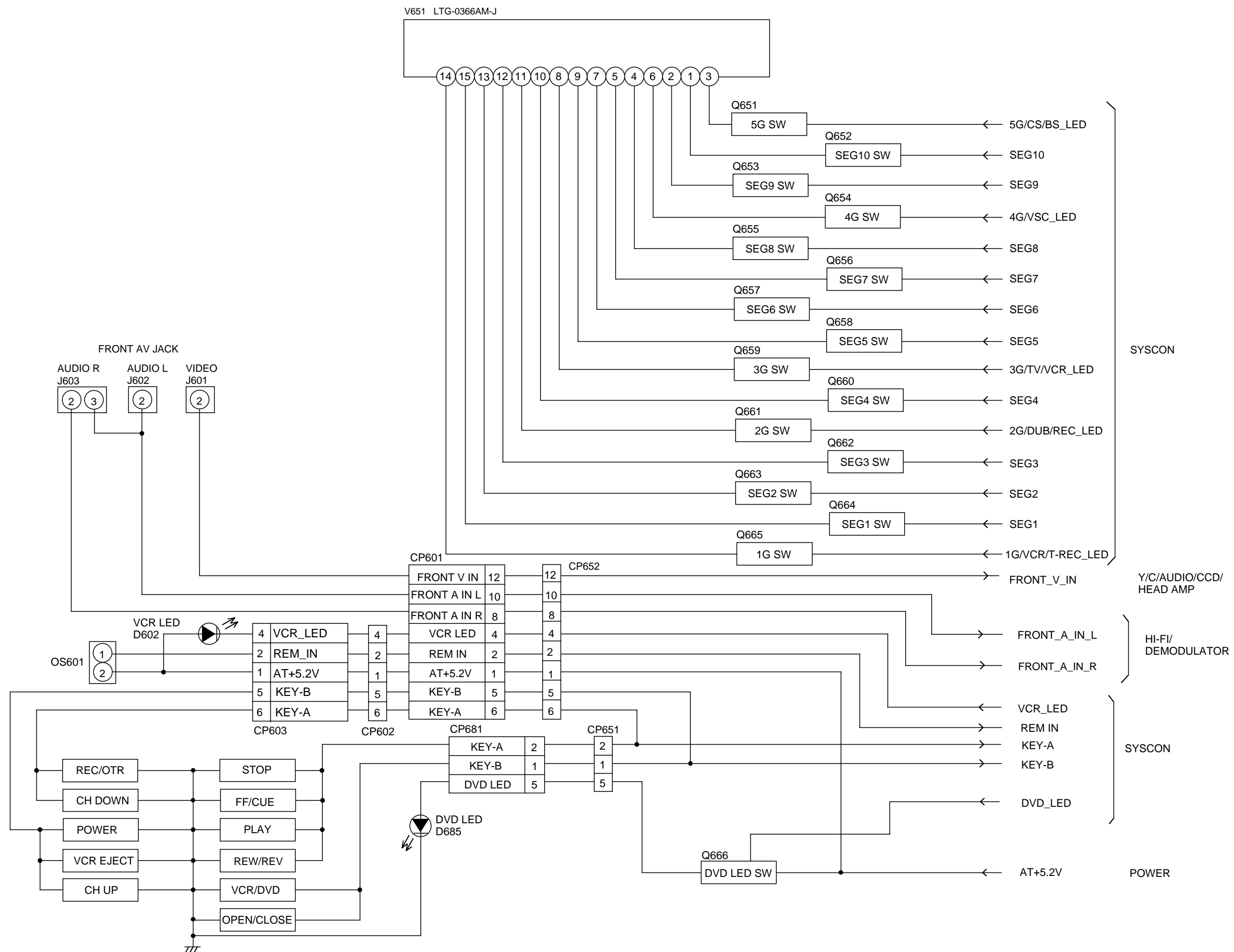
Y/C/AUDIO/CCD/HEAD AMP BLOCK DIAGRAM



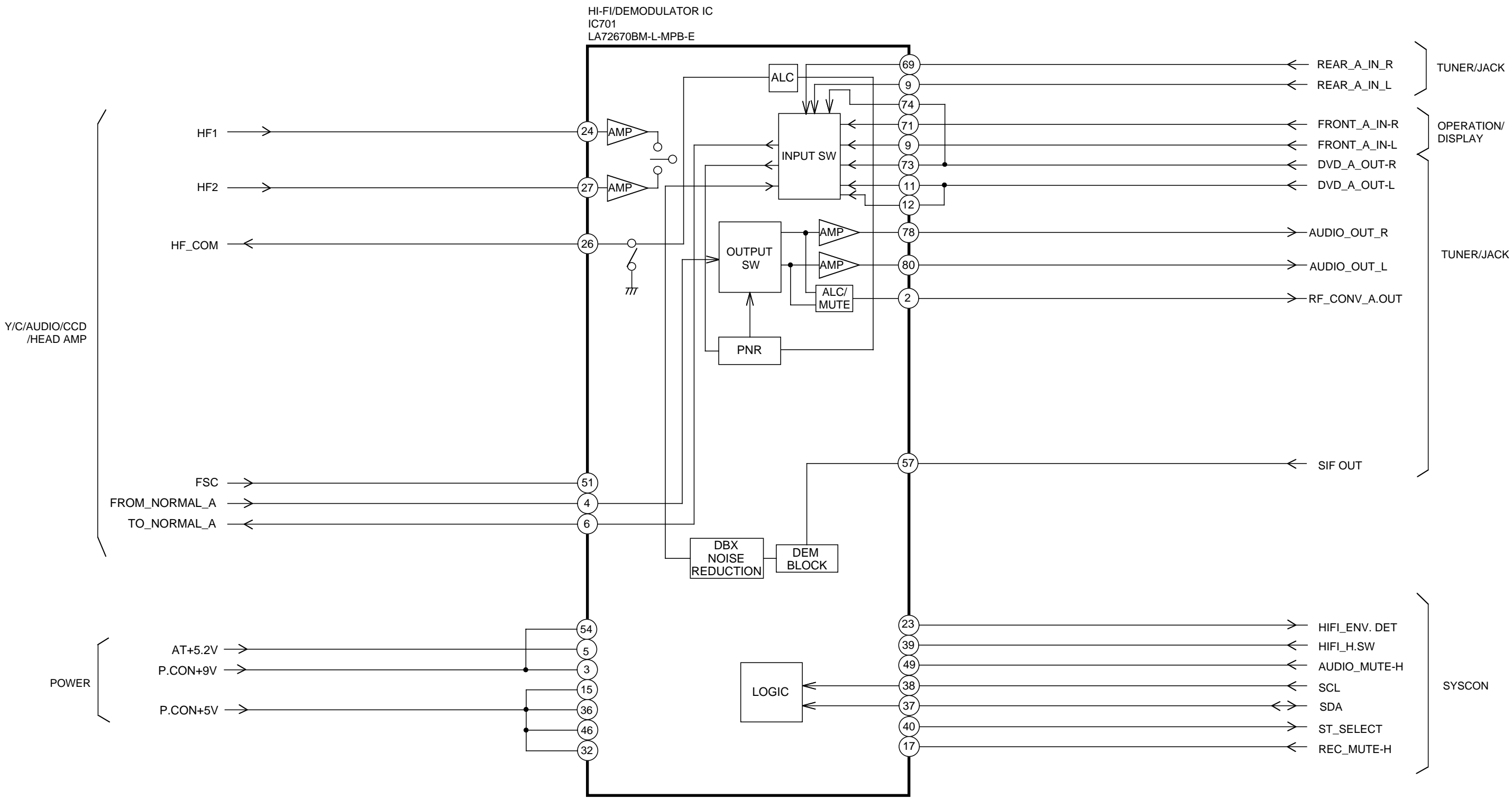
SYSTEM CONTROL BLOCK DIAGRAM



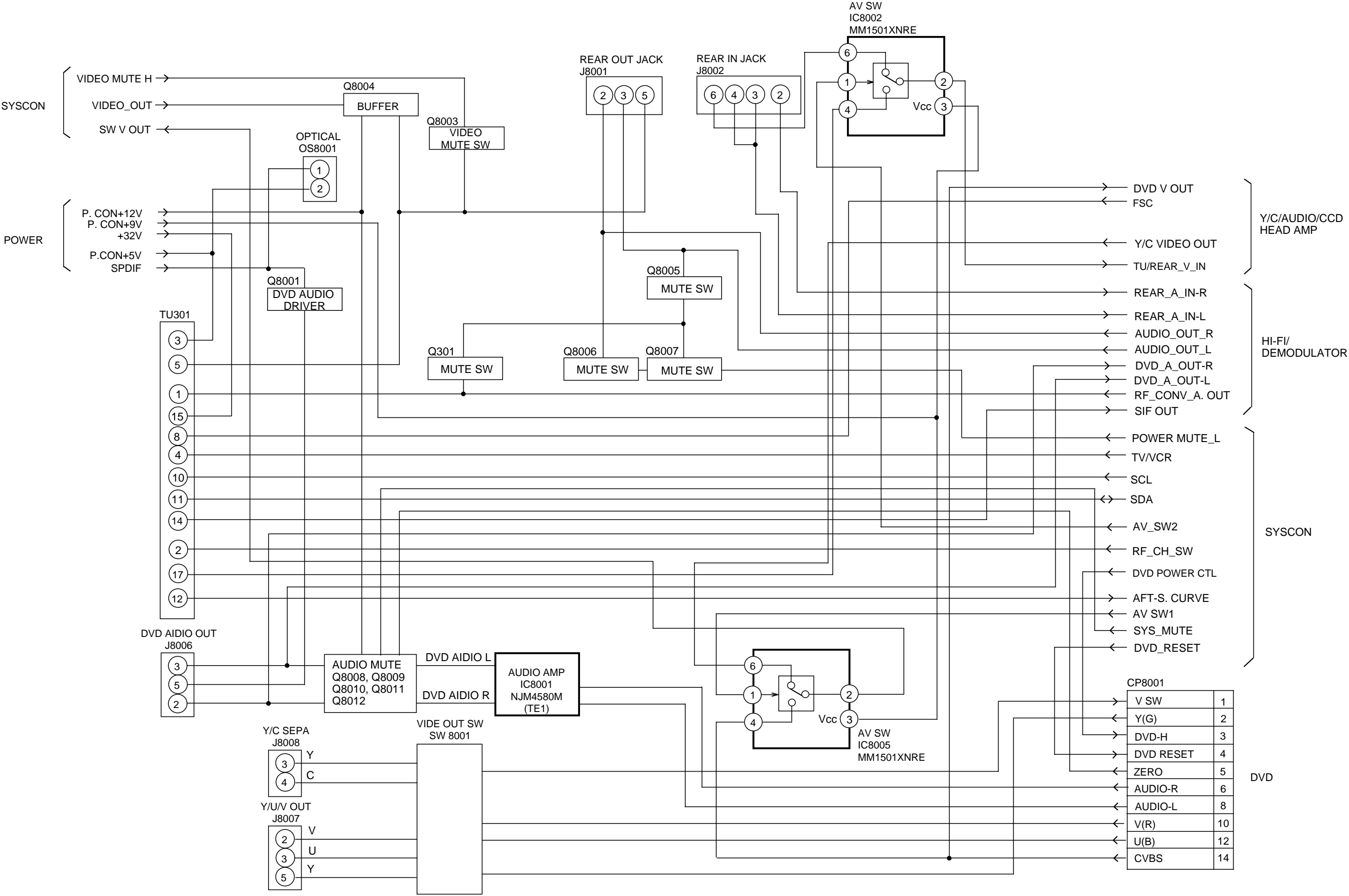
OPERATION/DISPLAY BLOCK DIAGRAM



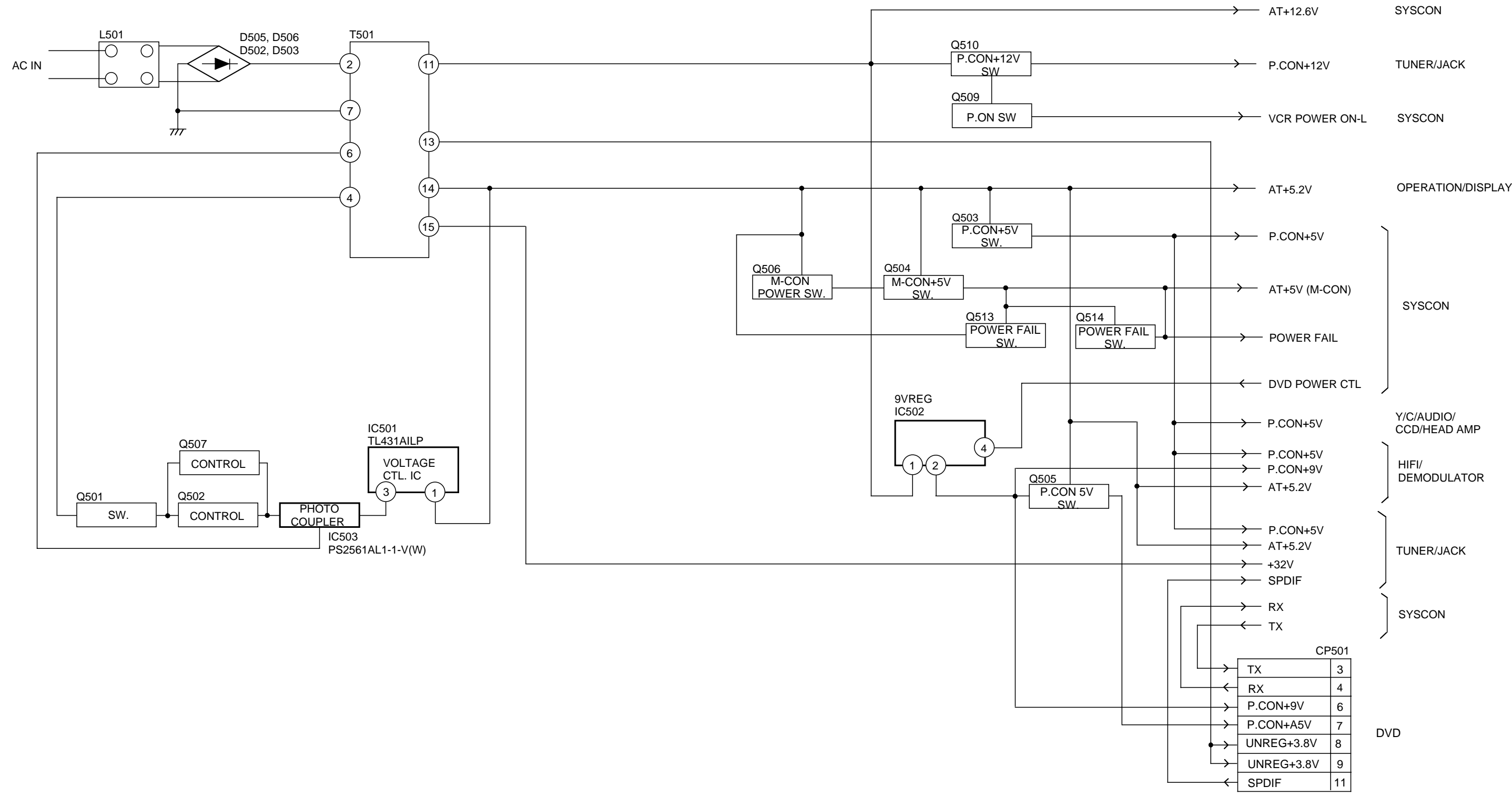
Hi-Fi/DEMODULATOR BLOCK DIAGRM



TUNER/JACK BLOCK DIAGRAM

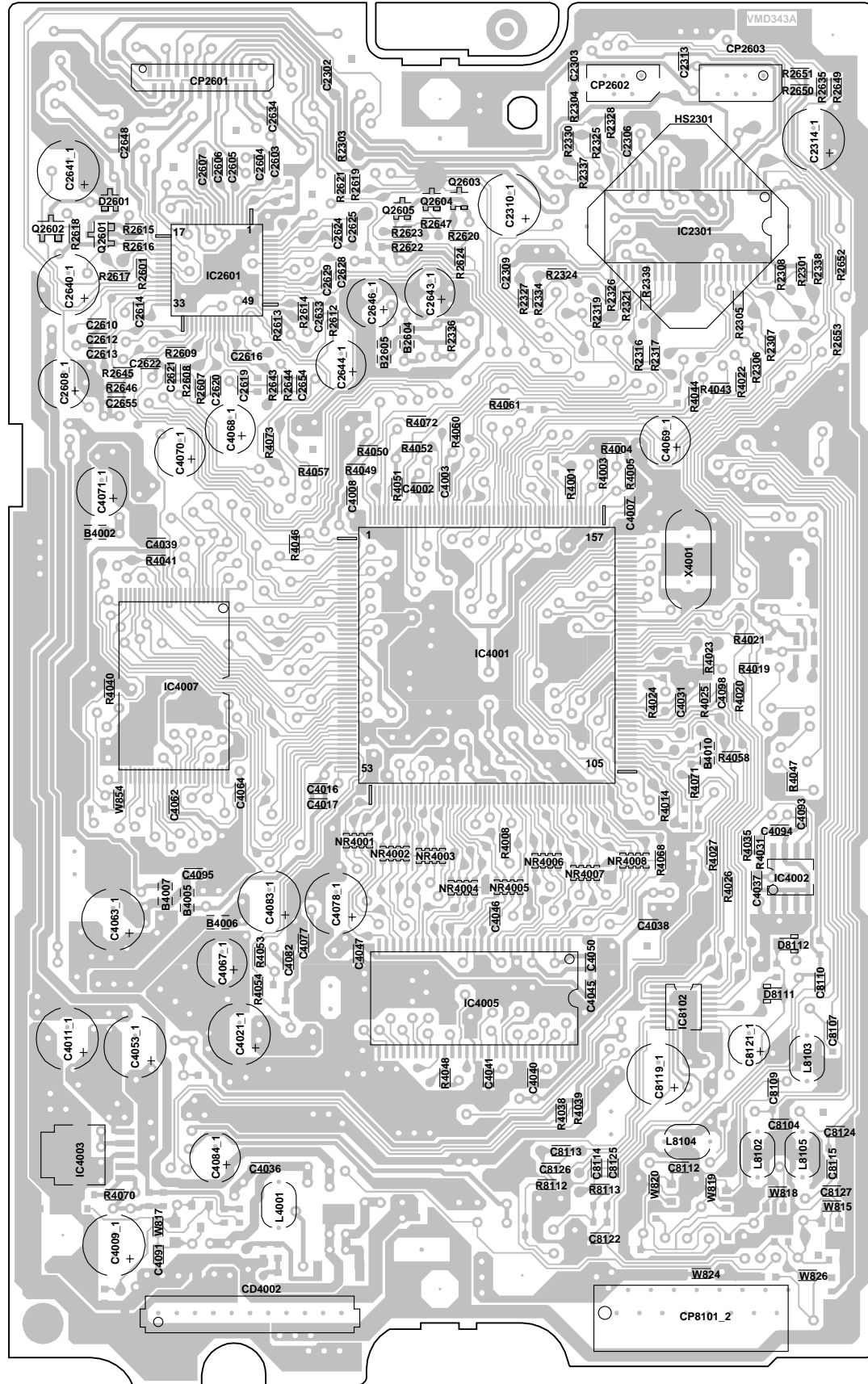


POWER BLOCK DIAGRAM

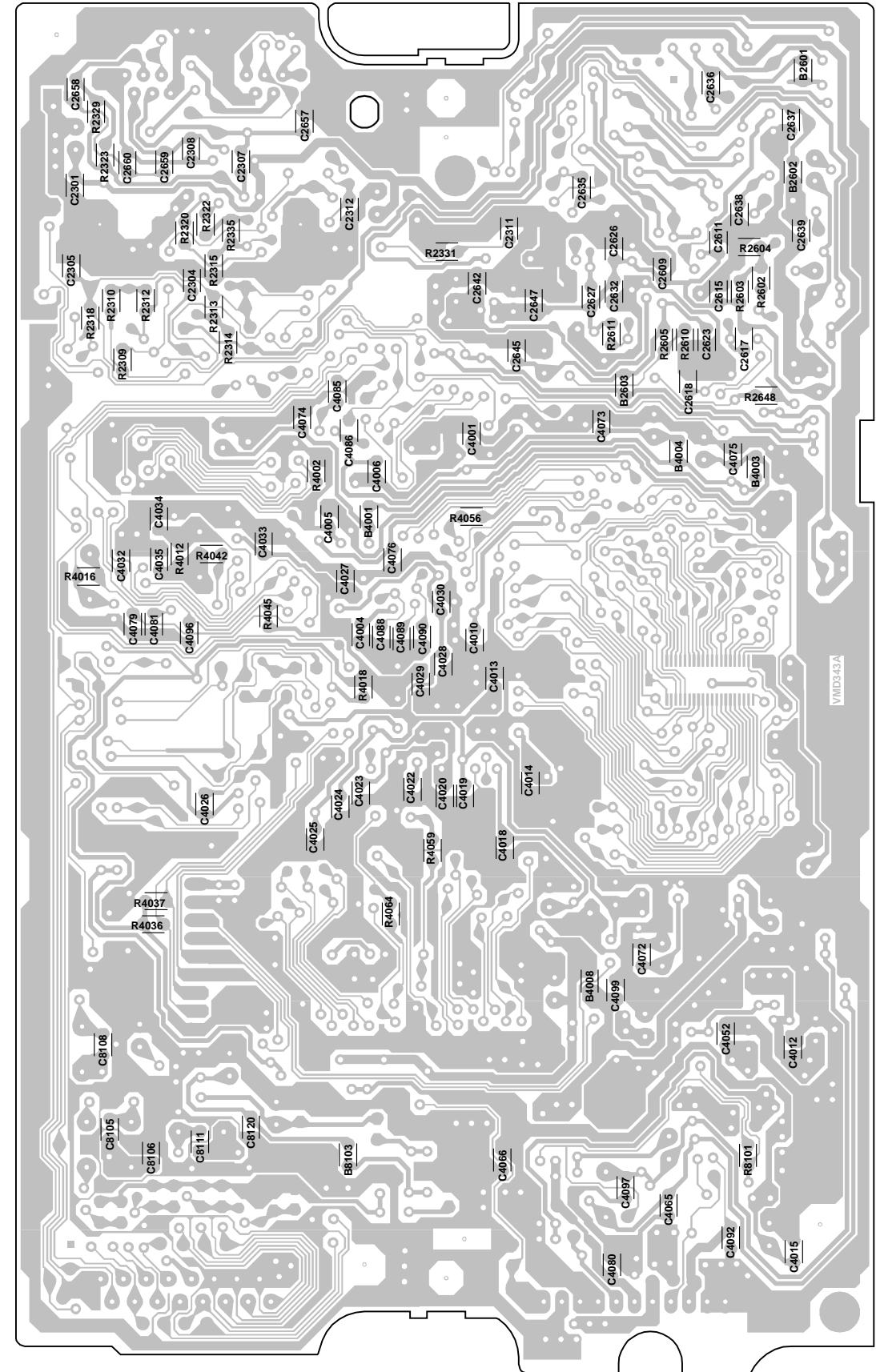


PRINTED CIRCUIT BOARDS

DVD (TOP SIDE)

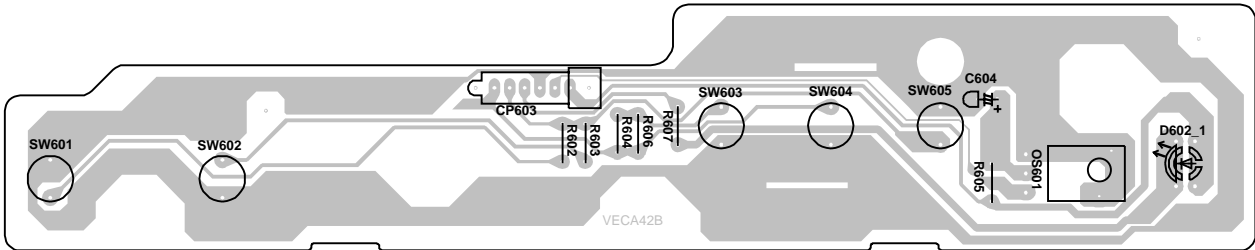


DVD (BOTTOM SIDE)

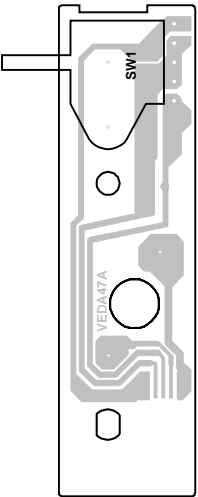


PRINTED CIRCUIT BOARDS

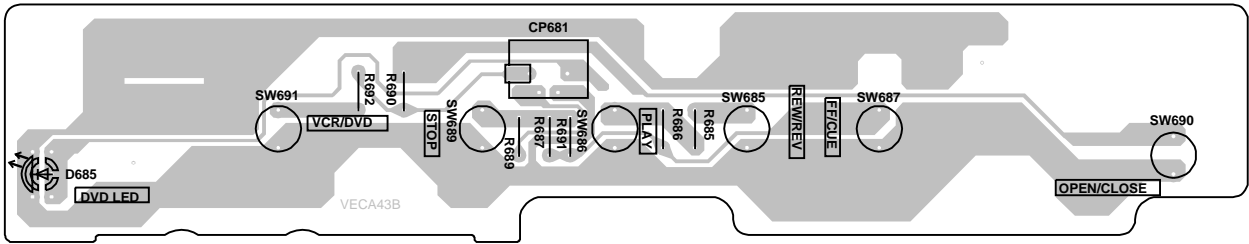
OPERATION 1
SOLDER SIDE



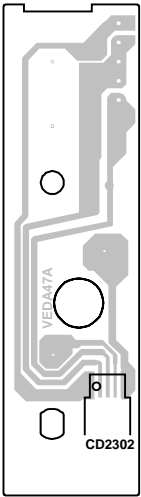
LOADING MOTOR (INSERTED PARTS)
SOLDER SIDE



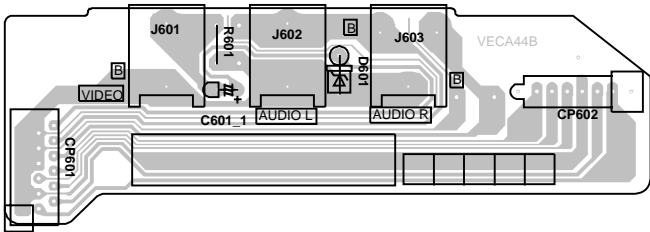
OPERATION 2
SOLDER SIDE



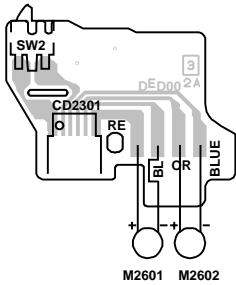
LOADING MOTOR (CHIP MOUNTED PARTS)
SOLDER SIDE



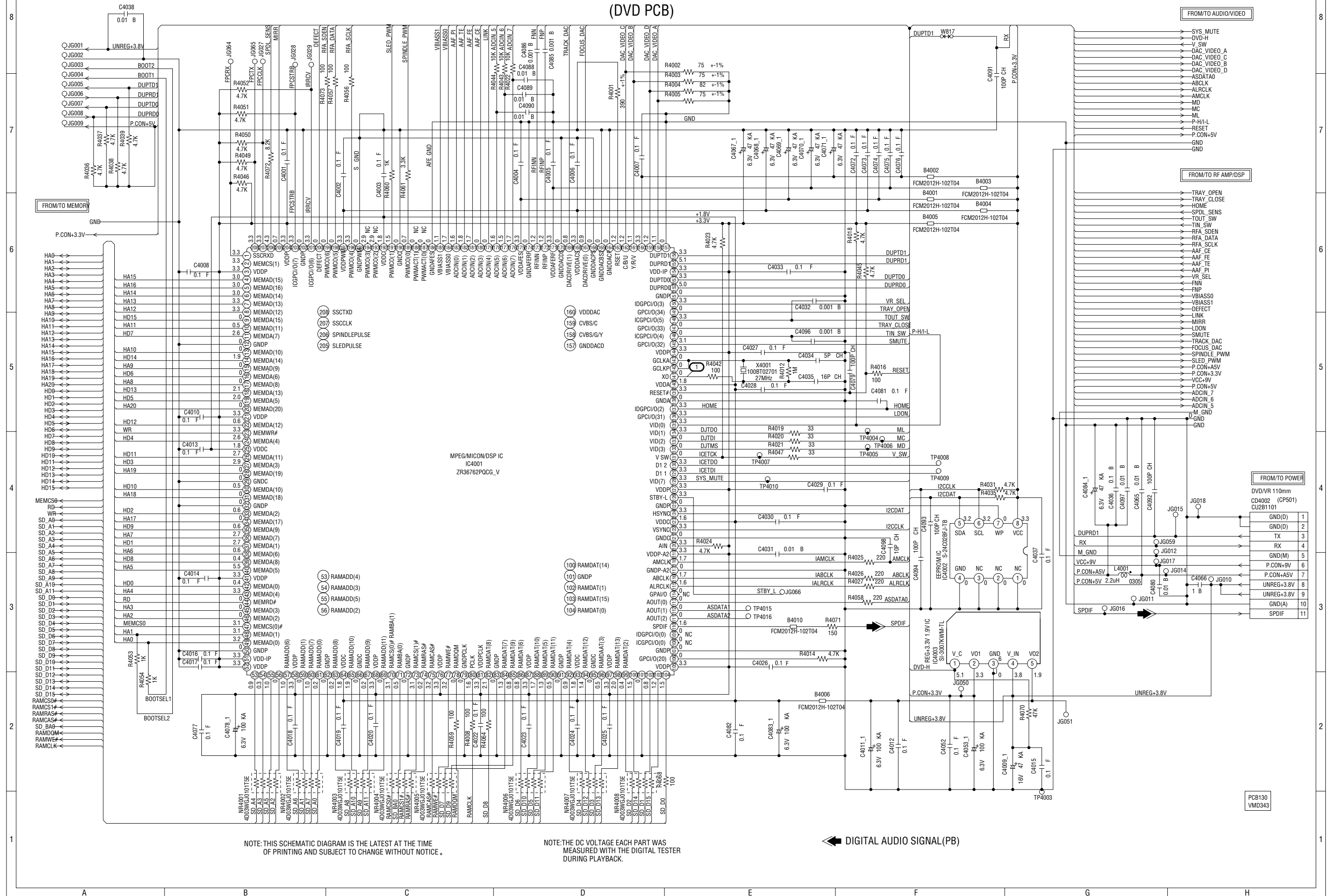
FRONT JACK
SOLDER SIDE



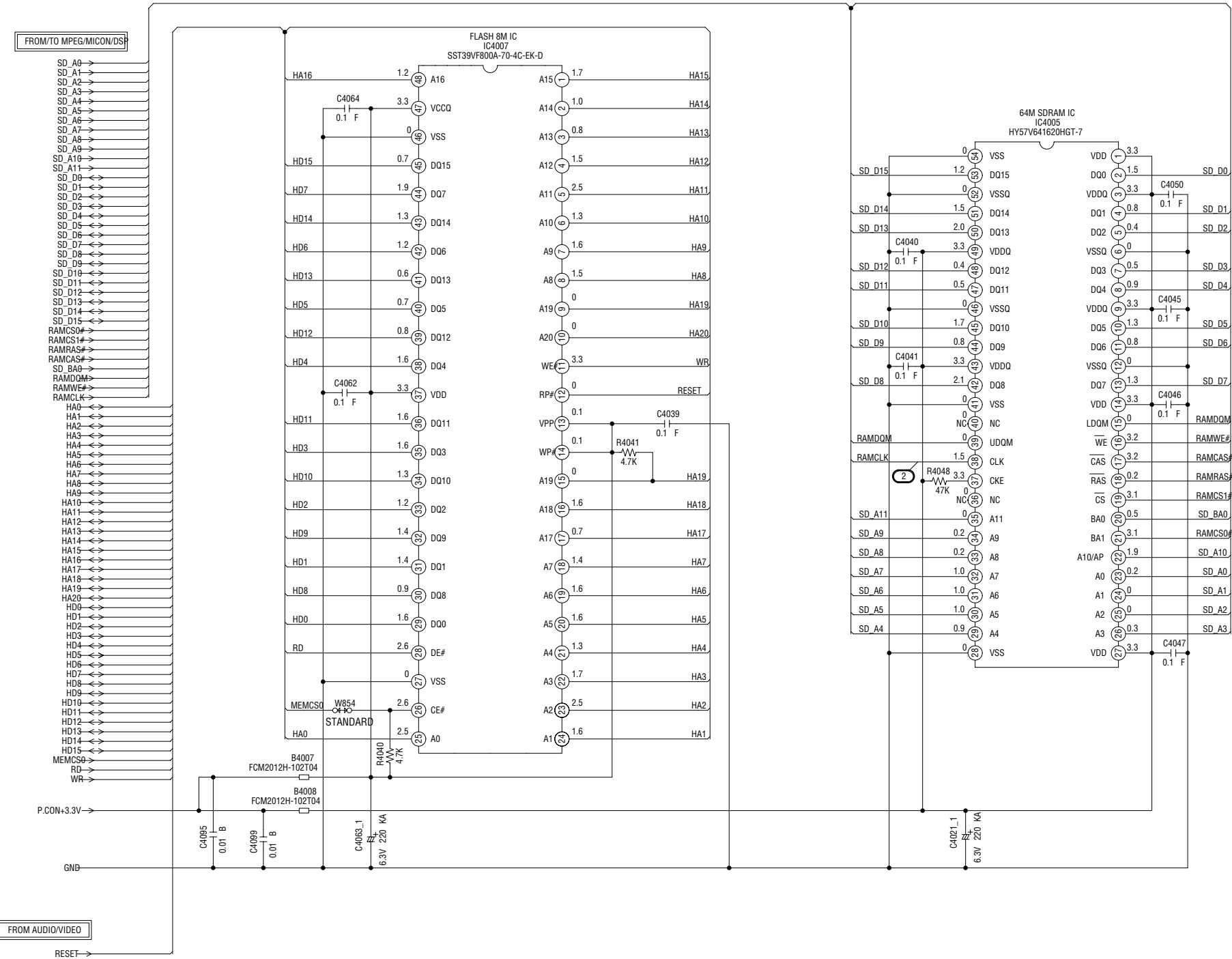
SW
SOLDER SIDE



MPEG/MICON/DSP SCHEMATIC DIAGRAM (DVD PCB)



MEMORY SCHEMATIC DIAGRAM
(DVD PCB)

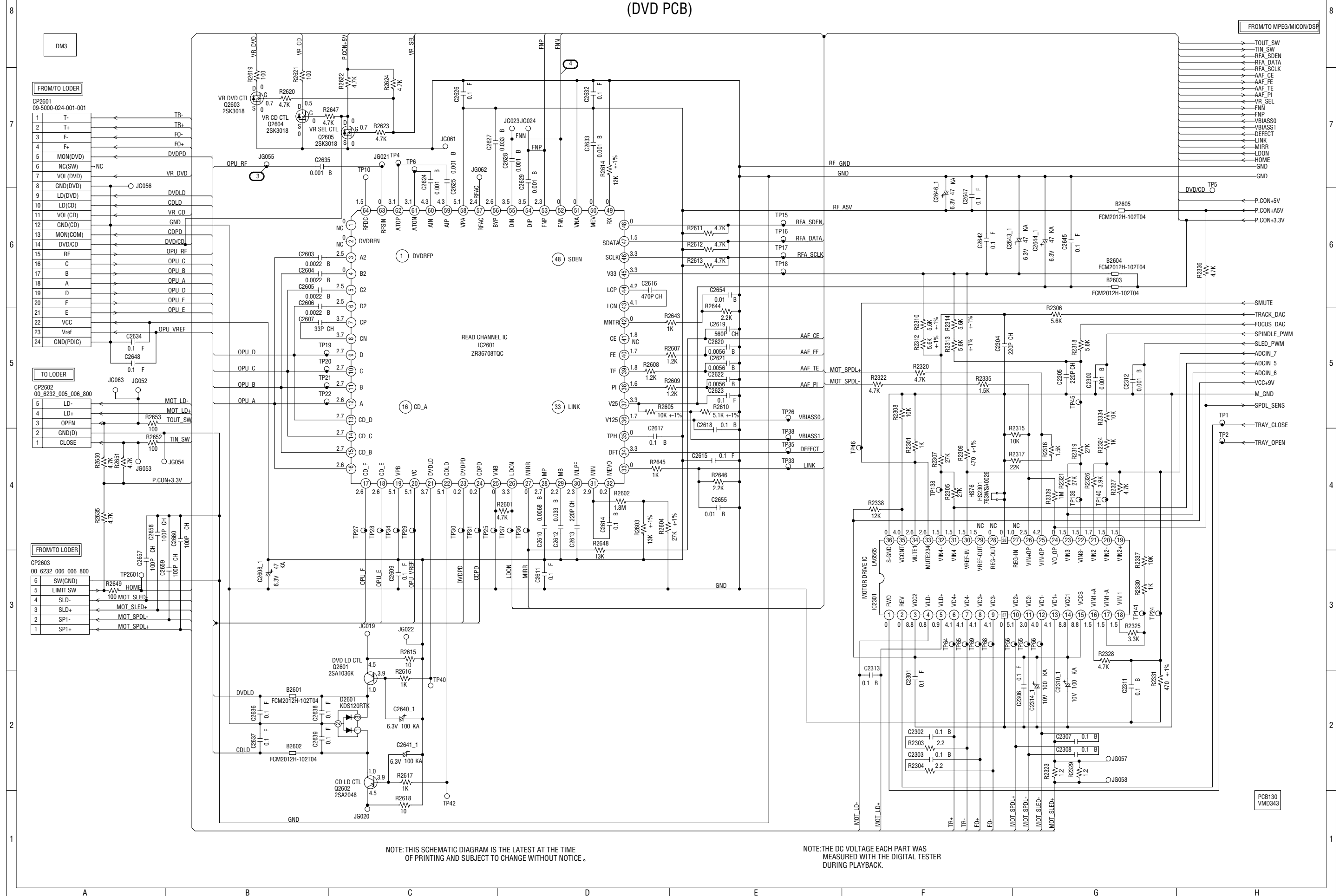


NOTE: THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME
OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE .

NOTE: THE DC VOLTAGE EACH PART WAS
MEASURED WITH THE DIGITAL TESTER
DURING PLAYBACK.

PCB130
VMD343

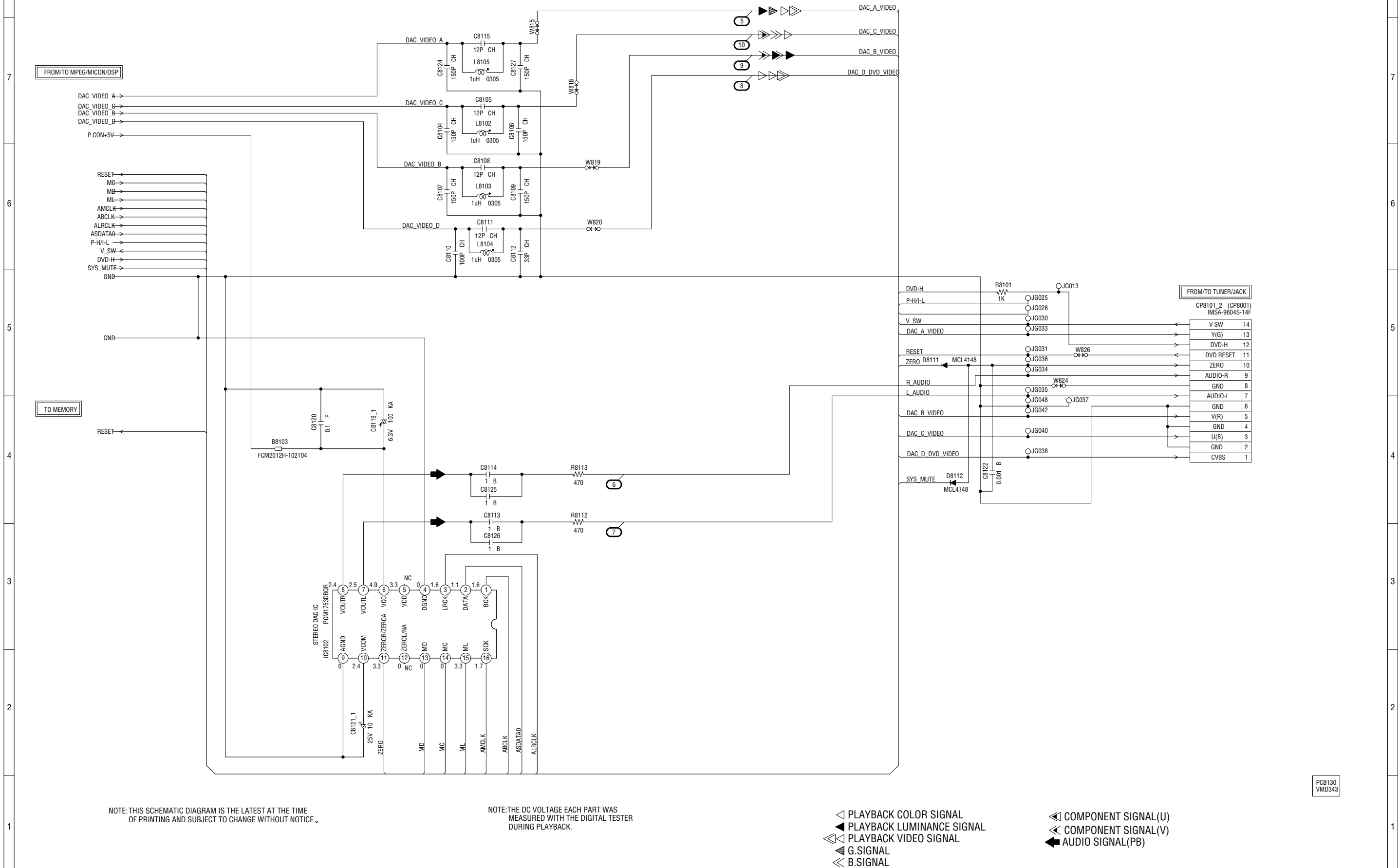
RF AMP/DSP SCHEMATIC DIAGRAM



NOTE: THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE.

NOTE:THE DC VOLTAGE EACH PART WAS MEASURED WITH THE DIGITAL TESTER DURING PLAYBACK.

AUDIO/VIDEO SCHEMATIC DIAGRAM
(DVD PCB)



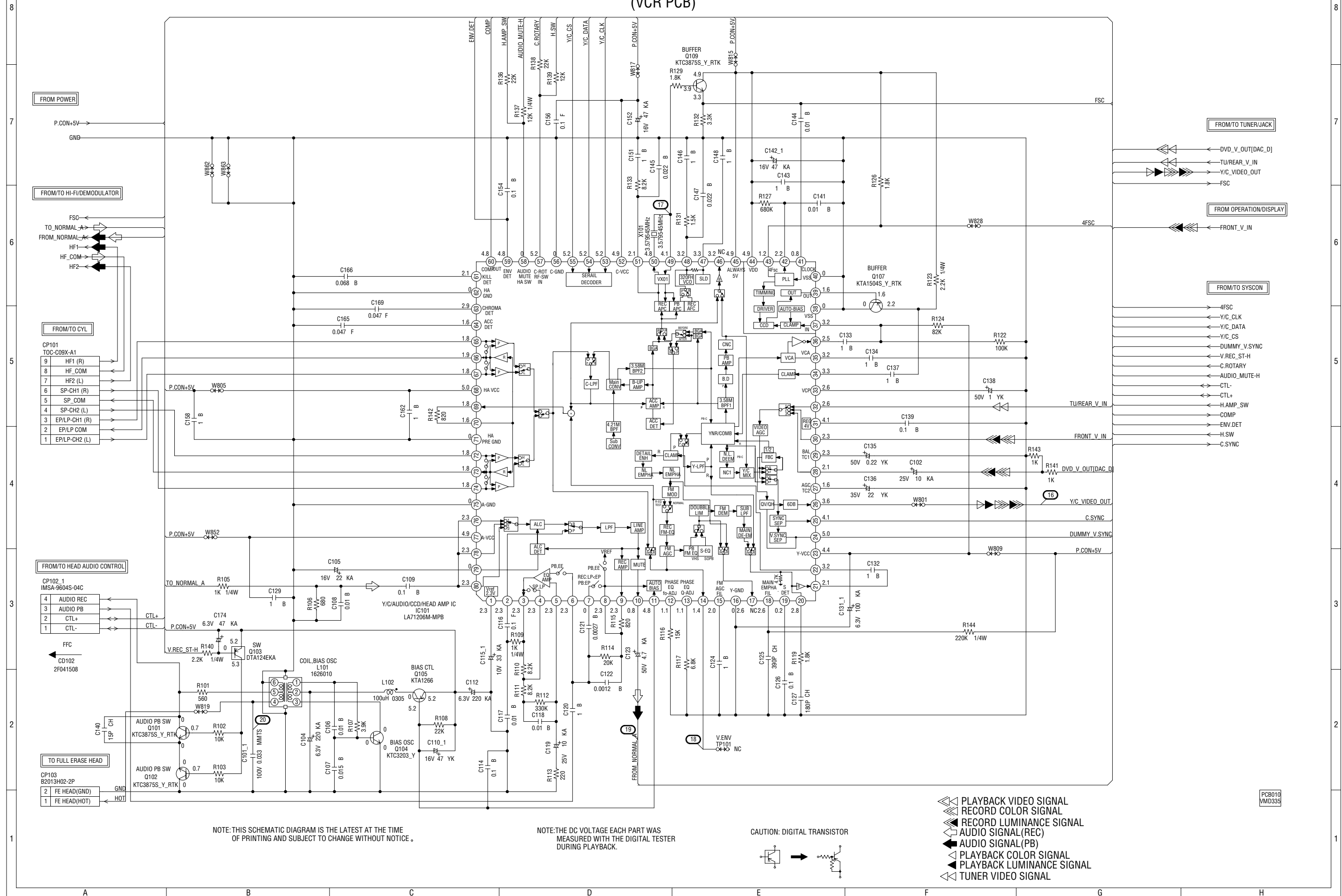
NOTE: THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME
OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE.

NOTE: THE DC VOLTAGE EACH PART WAS
MEASURED WITH THE DIGITAL TESTER
DURING PLAYBACK.

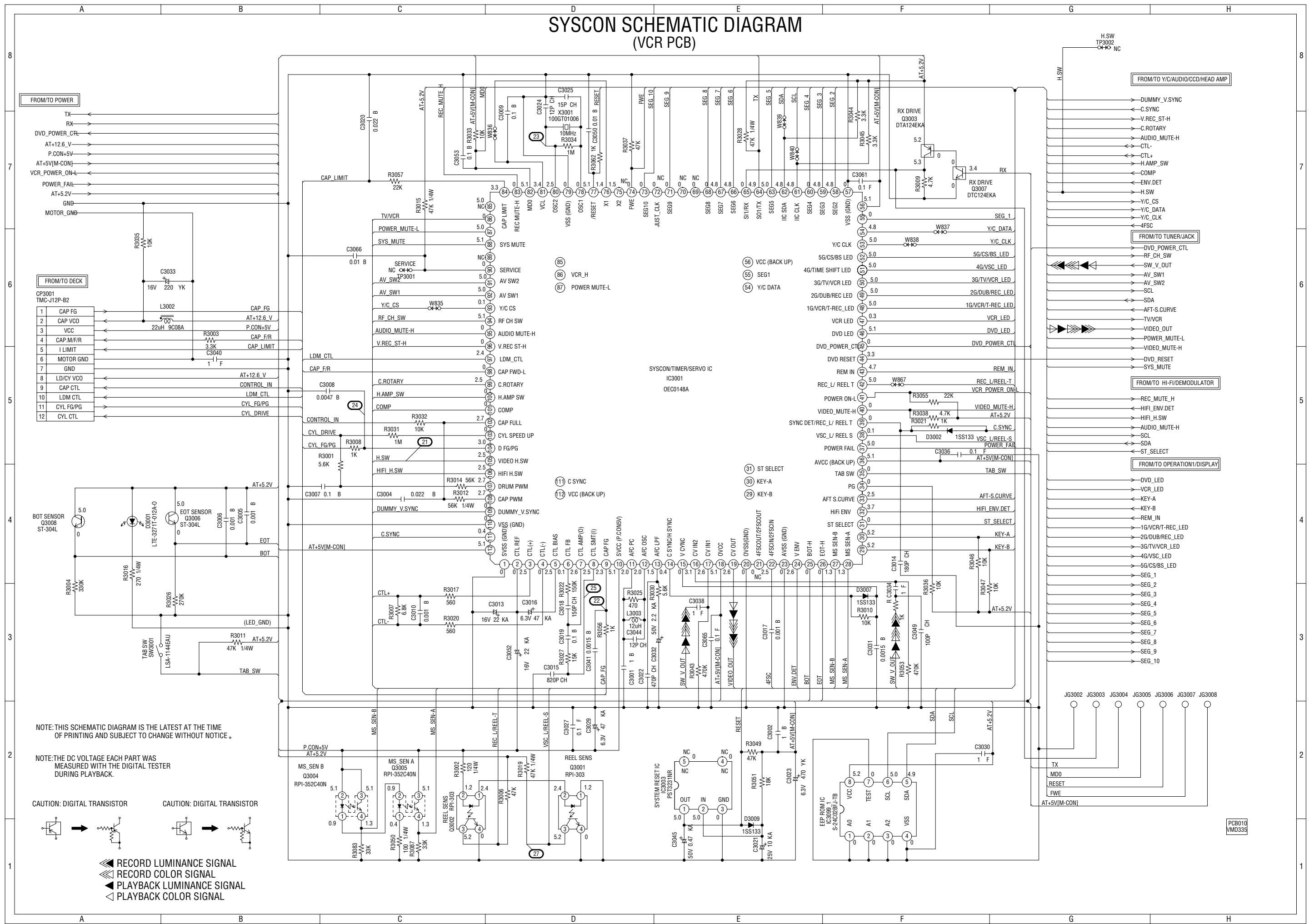
- ◁ PLAYBACK COLOR SIGNAL
- ▶ PLAYBACK LUMINANCE SIGNAL
- ◁◁ PLAYBACK VIDEO SIGNAL
- ▶▶ G.SIGNAL
- ◁ B.SIGNAL
- ◁◁ COMPONENT SIGNAL(U)
- ◁◁ COMPONENT SIGNAL(V)
- ▶▶ AUDIO SIGNAL(PB)

PCB130
VMD343

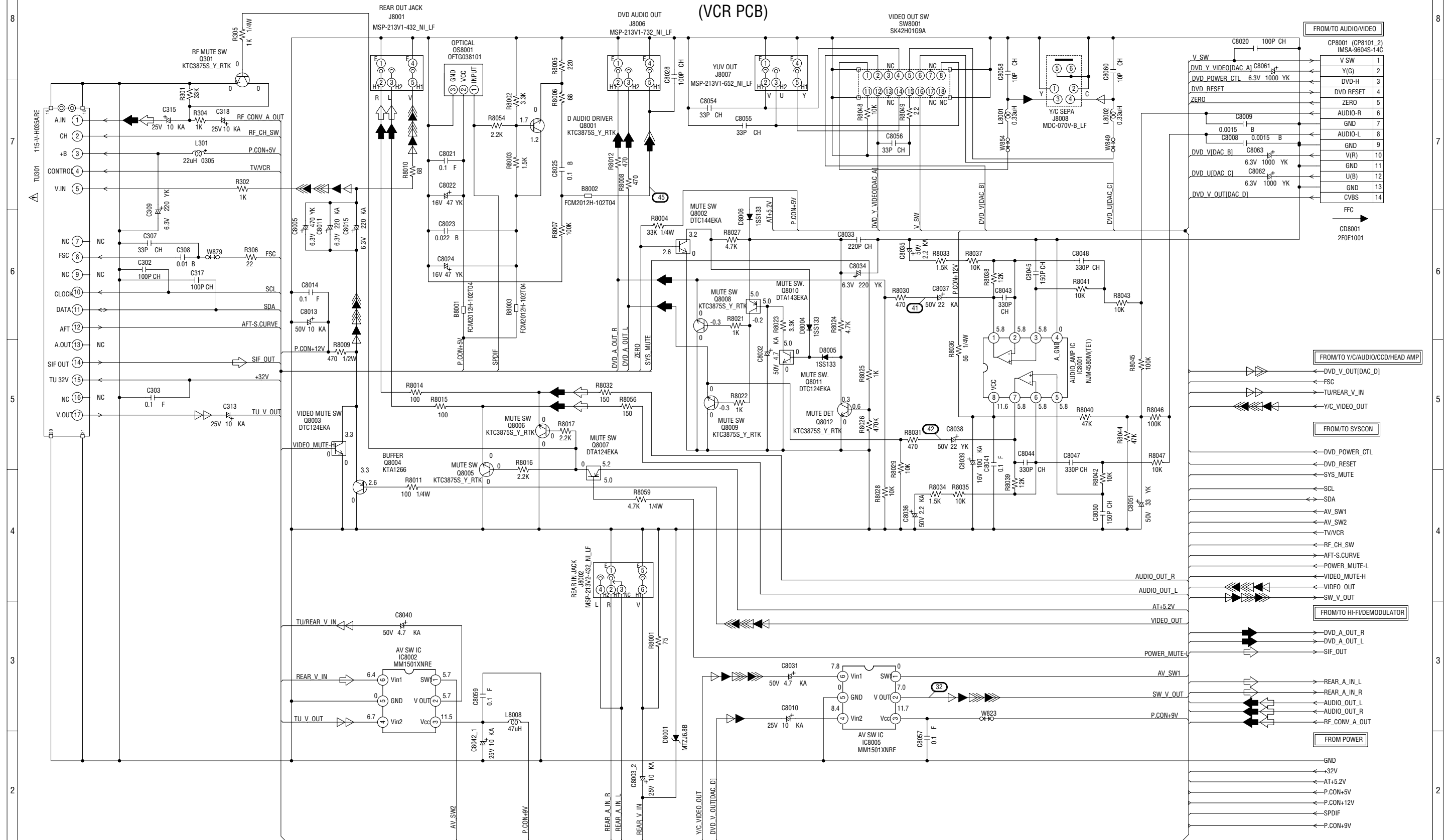
Y/C/AUDIO/CCD/HEAD AMP SCHEMATIC DIAGRAM (VCR PCB)



SYSCON SCHEMATIC DIAGRAM (VCR PCB)



(VCR PCB)

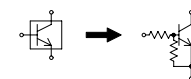


NOTE: THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE.

ATTENTION: LES PIÈCES RÉPARÉES PAR UN ÉTANT DANGEREUSES AU POINT DE VUE SÉCURITÉ N'UTILISER QUE CELLES DÉCRITES DANS LA NOMENCLATURE DES PIÈCES.

CAUTION SINCE THESE PARTS MARKED BY ARE CRITICAL FOR SAFETY, USE ONES DESCRIBED IN PARTS LIST ONLY .

CAUTION: DIGITAL TRANSISTOR



CAUTION: DIGITAL TRANSISTOR



- ◀◀◀ PLAYBACK VIDEO SIGNAL
- ◀◀◀ TUNER VIDEO SIGNAL
- ◀◀◀ PLAYBACK LUMINANCE SIGNAL
- ◀◀◀ PLAYBACK COLOR SIGNAL
- ◀◀◀ RECORD COLOR SIGNAL
- ◀◀◀ RECORD LUMINANCE SIGNAL
- ◀◀◀ AUDIO SIGNAL (REC)
- ◀◀◀ AUDIO SIGNAL (PB)

PCB010
VMD335

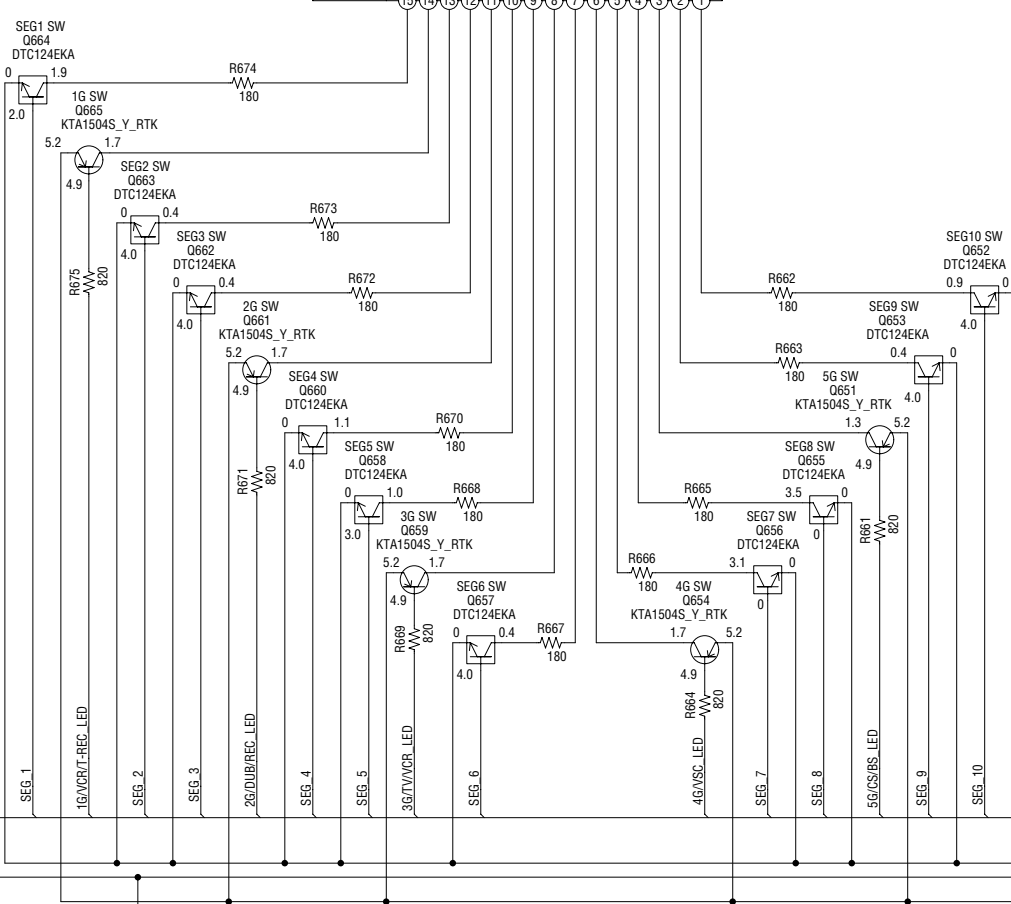
NOTE:THE DC VOLTAGE EACH PART WAS
MEASURED WITH THE DIGITAL TESTER
DURING PLAYBACK.

OPERATION/DISPLAY SCHEMATIC DIAGRAM
(VCR PCB)

FROM/TO OPERATION

CP652 (CP601)
TOC-G12X-A1

12	FRONT V IN	FRONT V IN
11	GND	
10	FRONT A IN L	FRONT A IN L
9	GND	
8	FRONT A IN R	FRONT A IN R
7	GND	
6	KEY-A	KEY-A
5	KEY-B	KEY-B
4	VCR LED	VCR_LED
3	GND	
2	REM IN	REM_IN
1	AT+5.2V	AT+5.2V



FROM/TO SYSCON

←	DVD_LED
←	VCR_LED
←	1G/VCR/T-REC_LED
←	2G/DUB/REC_LED
←	3G/TV/VCR_LED
←	4G/VSC_LED
←	5G/CS/BS_LED
←	SEG_1
←	SEG_2
←	SEG_3
←	SEG_4
←	SEG_5
←	SEG_6
←	SEG_7
←	SEG_8
←	SEG_9
←	SEG_10
→	REM_IN
→	KEY-B
→	KEY-A

TO HI-FI/DEMODULATOR

→	FRONT_A_IN_L
→	FRONT_A_IN_R

TO Y/C/AUDIO/CCD/HEAD AMP

→	FRONT_V_IN
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FROM POWER

←	LEM_VCC
←	AT+5.2V
←	GND

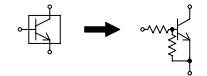
FROM/TO OPERATION

5	DVD_LED
4	GND
3	NC
2	KEY-A
1	KEY-B

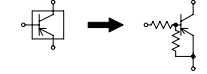
NOTE: THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME
OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE.

NOTE: THE DC VOLTAGE EACH PART WAS
MEASURED WITH THE DIGITAL TESTER
DURING PLAYBACK.

CAUTION: DIGITAL TRANSISTOR



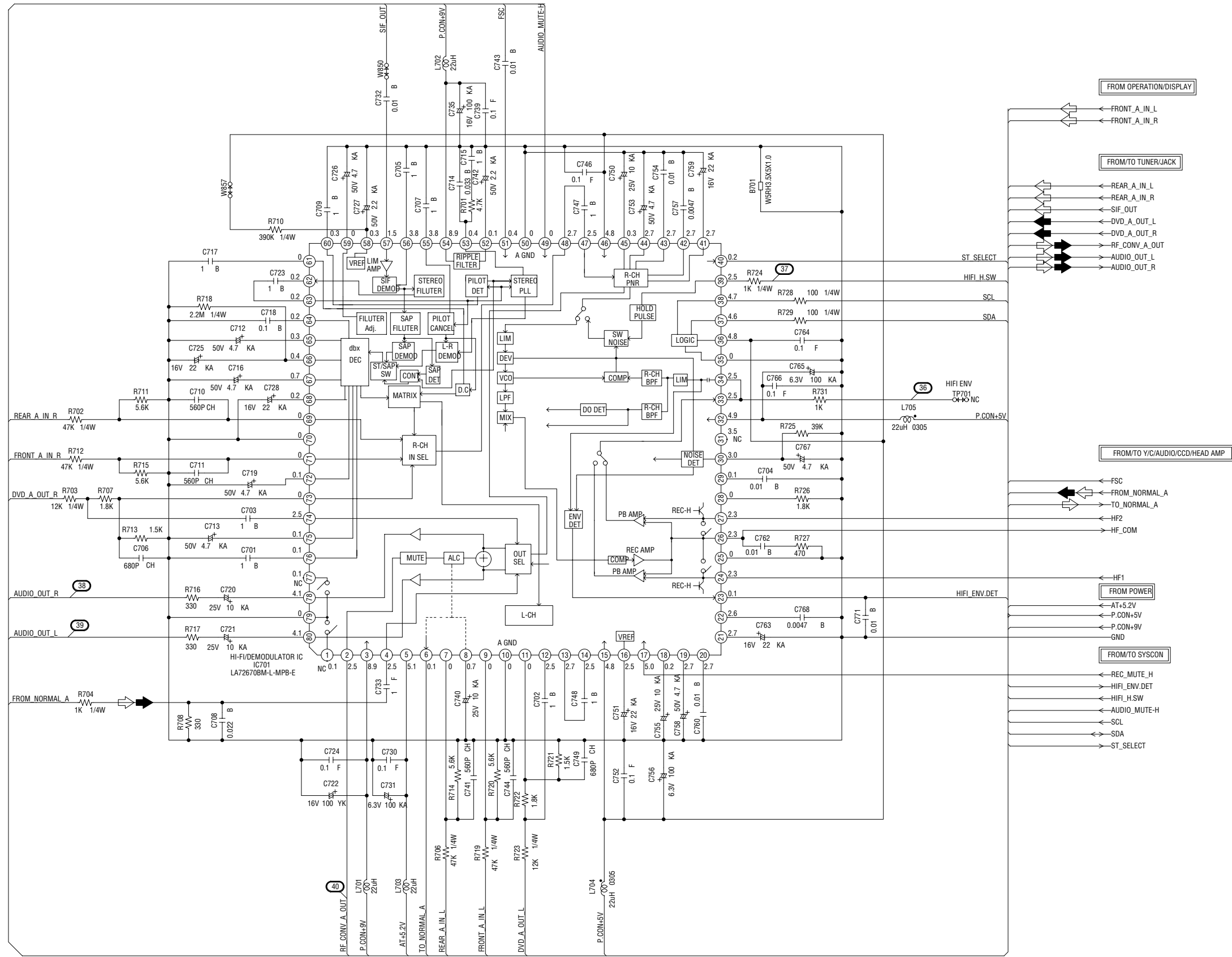
CAUTION: DIGITAL TRANSISTOR



- AUDIO SIGNAL(REC)
- RECORD COLOR SIGNAL
- RECORD LUMINANCE SIGNAL

PCB010
VMD335

Hi-Fi/DEMODULATOR SCHEMATIC DIAGRAM
(VCR PCB)



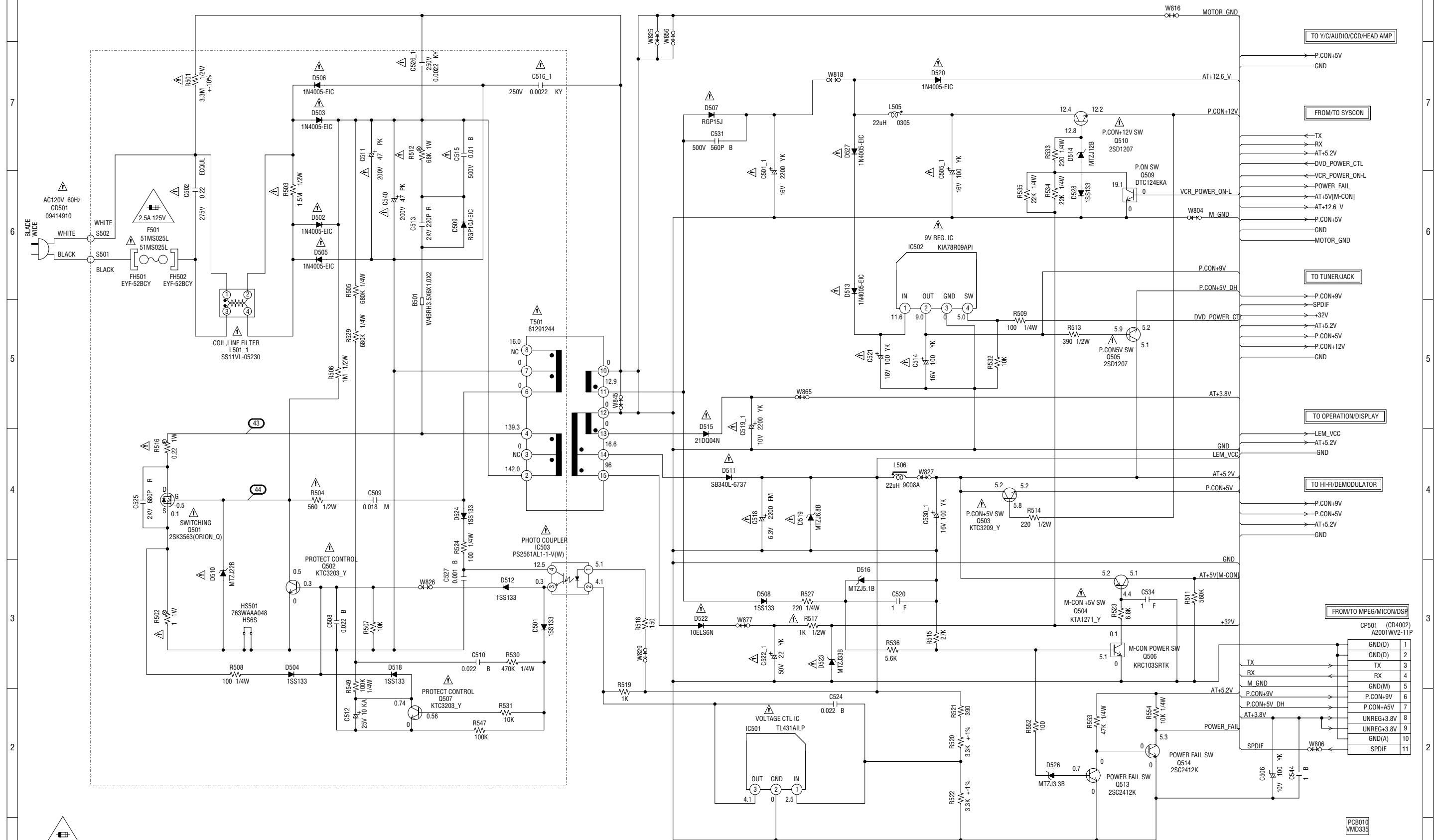
NOTE: THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME
OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE.


NOTE: THE DC VOLTAGE EACH PART WAS
MEASURED WITH THE DIGITAL TESTER
DURING PLAYBACK.

← AUDIO SIGNAL(REC)
→ AUDIO SIGNAL(PB)

PCB010
VMD335

POWER SCHEMATIC DIAGRAM (VCR PCB)




 **CAUTION** :FOR CONTINUED PROTECTION AGAINST FIRE HAZARD,
REPLACE ONLY WITH THE SAME TYPE FUSE 2.5A 125V(F501)

ATTENTION :POUR UNE PROTECTION CONTINUE LES RISQUES D'INCEIE
N'UTILISER QUE DES FUSIBLE DE MEME TYPE 2.5A 125V(F501).

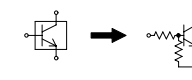
NOTE: THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE.

NOTE:THE DC VOLTAGE EACH PART WAS MEASURED WITH THE DIGITAL TESTER DURING PLAYBACK.

ATTENTION: LES PIÈCES RÉPARÉES PAR UN  ÉTANT DANGEREUSES AU POINT DE VUE SÉCURITÉ, N'UTILISER QUE CELLES DÉCRITES DANS LA NOMENCLATURE DES PIÈCES.

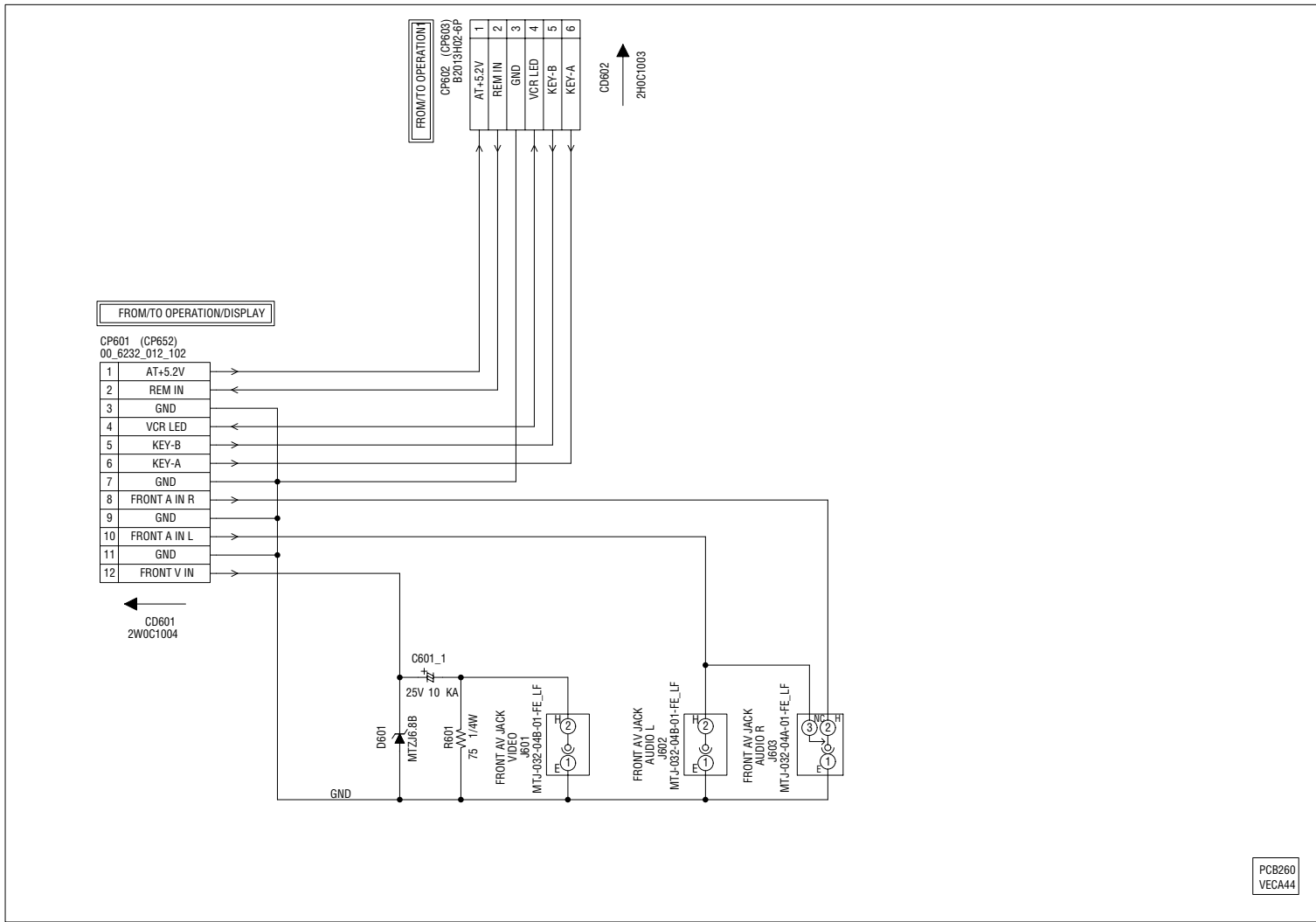
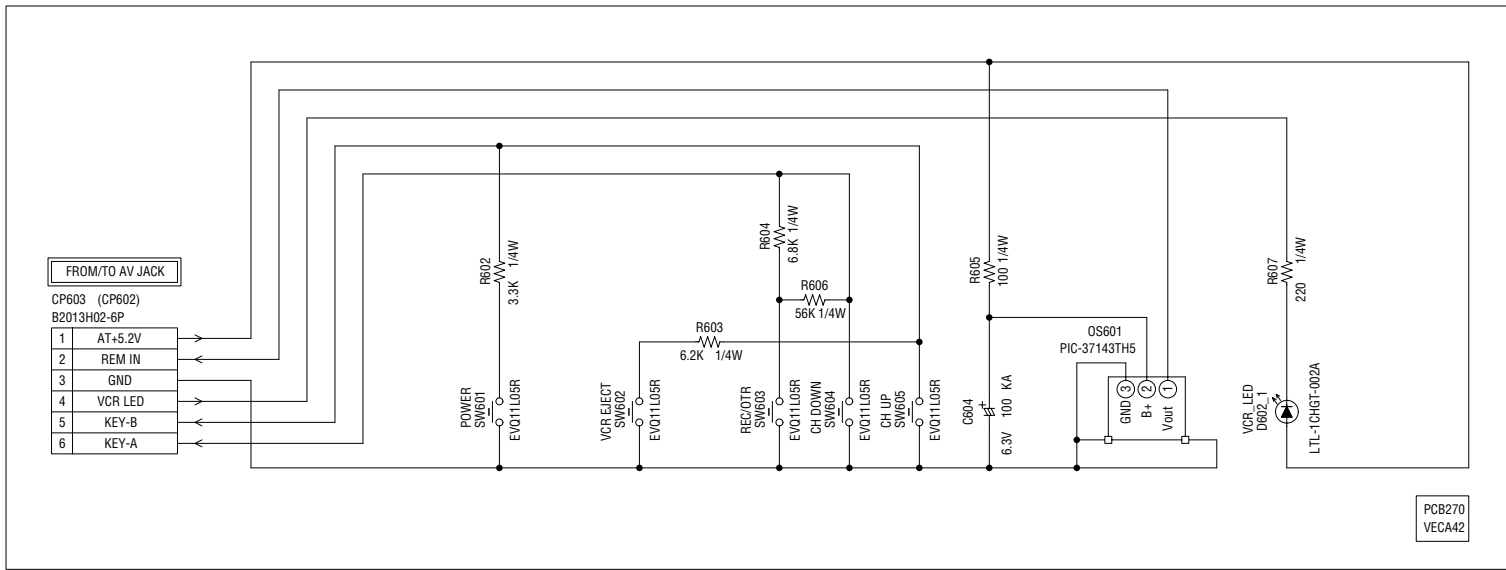
CAUTION SINCE THESE PARTS MARKED BY ARE CRITICAL FOR SAFETY, USE ONES DESCRIBED IN PARTS LIST ONLY .

CAUTION: DIGITAL TRANSISTOR

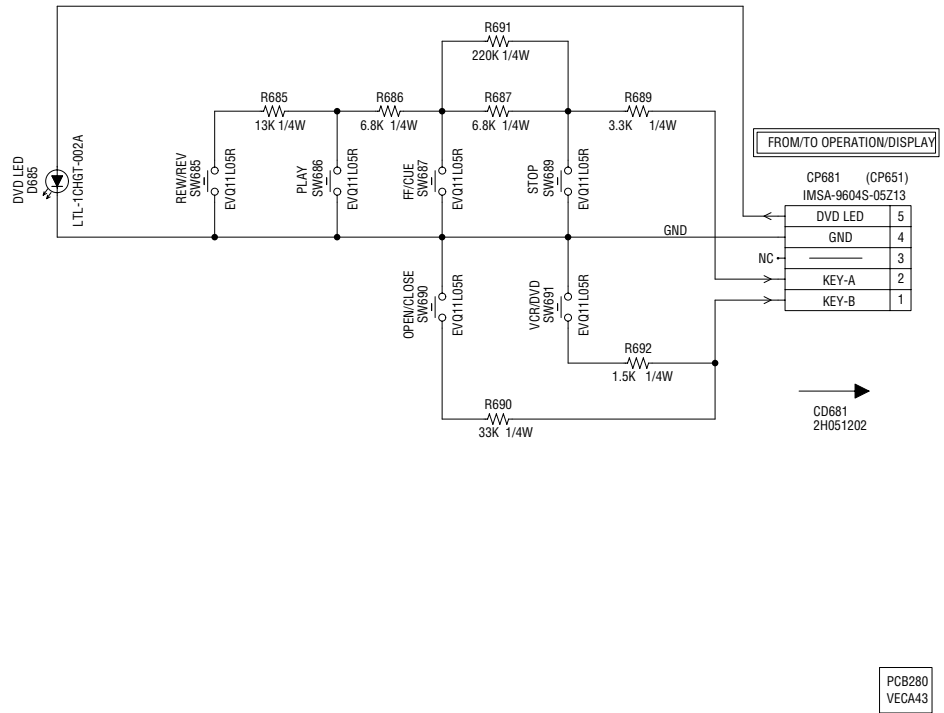


OPERATION SCHEMATIC DIAGRAM

(OPERATION1 PCB)



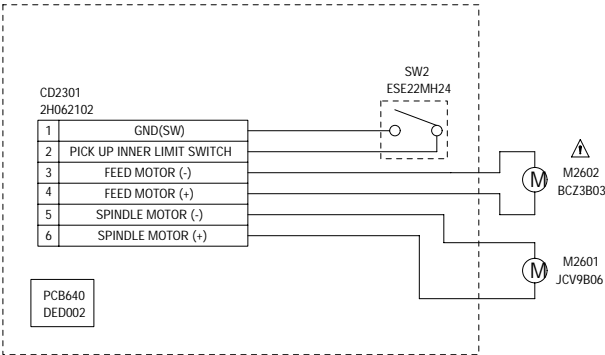
NOTE:THE DC VOLTAGE EACH PART WAS MEASURED WITH THE DIGITAL TESTER DURING PLAYBACK.



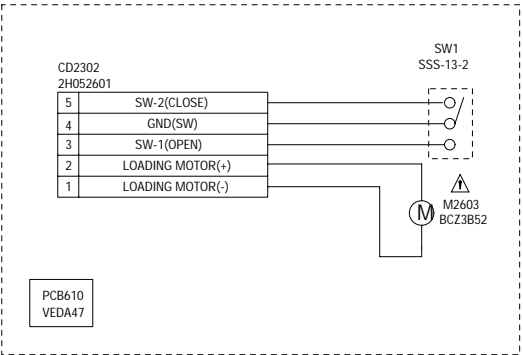
NOTE:THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE .

LOADING MOTOR/SW SCHEMATIC DIAGRAM

(SW PCB)

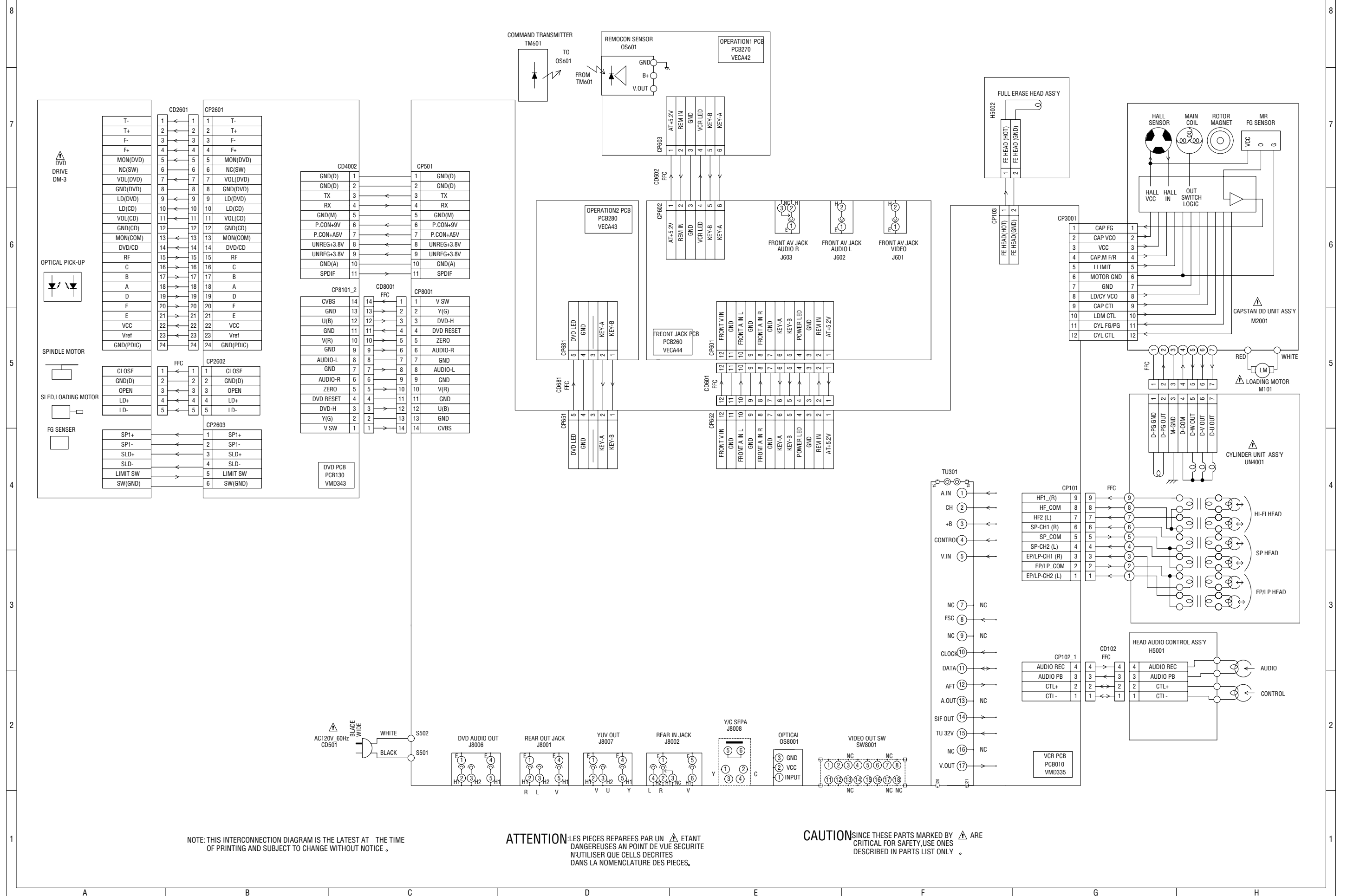


(LOADING MOTOR PCB)



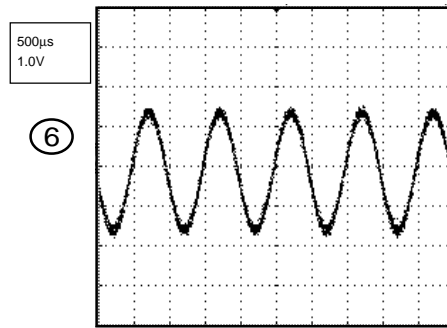
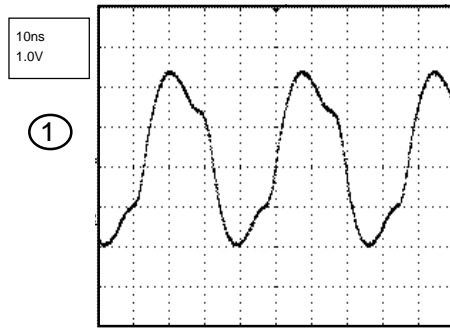
NOTE: THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME
OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE .

INTERCONNECTION DIAGRAM

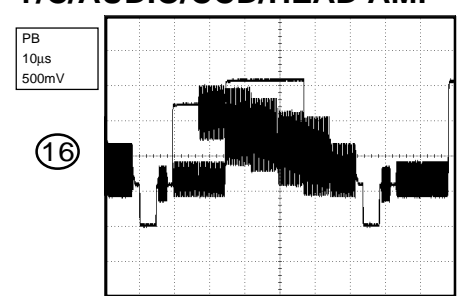


WAVEFORMS

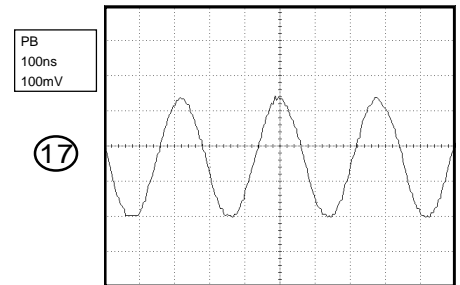
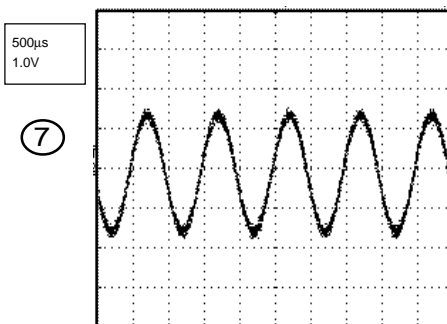
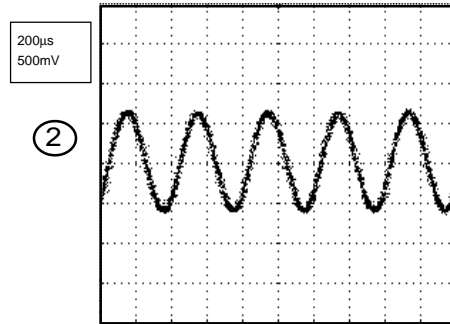
MPEG/MICON/DSP



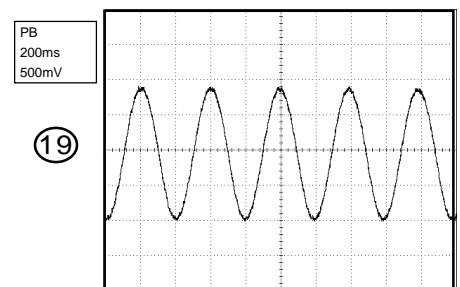
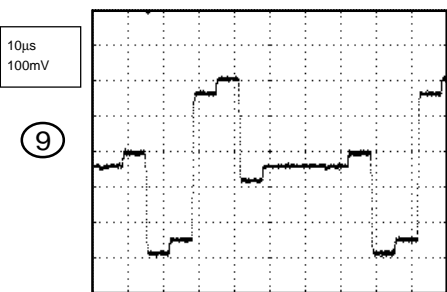
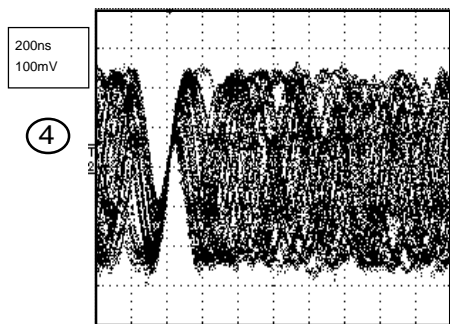
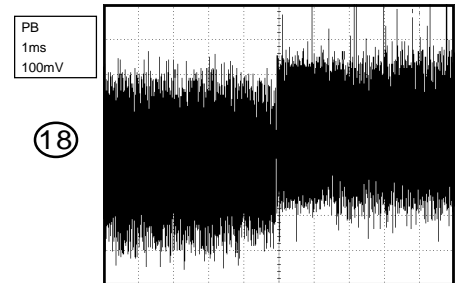
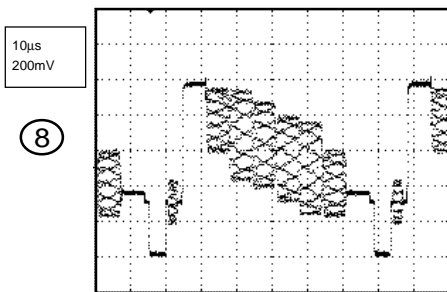
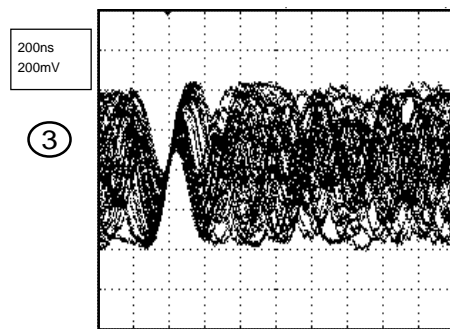
Y/C/AUDIO/CCD/HEAD AMP



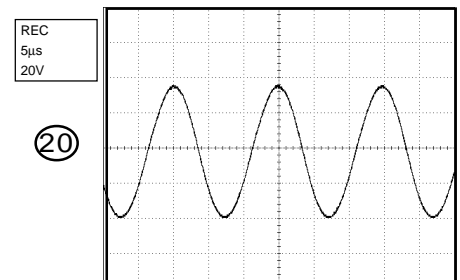
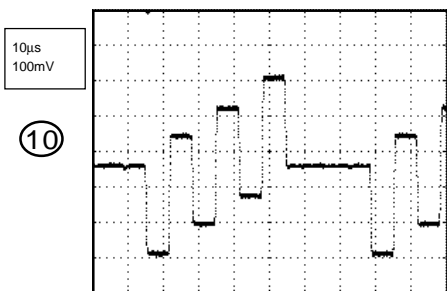
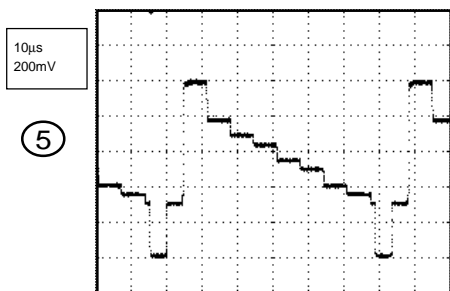
MEMORY



RF AMP/DSP



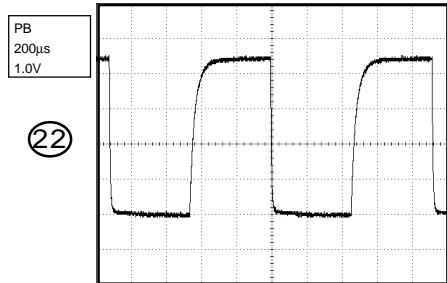
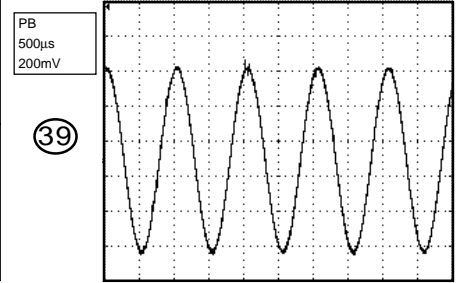
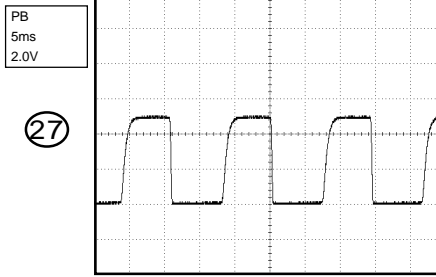
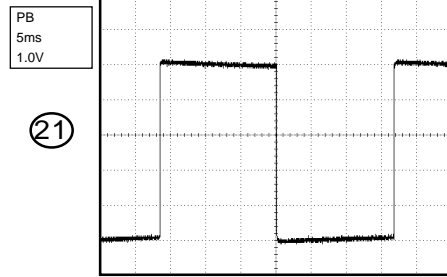
AUDIO/VIDEO



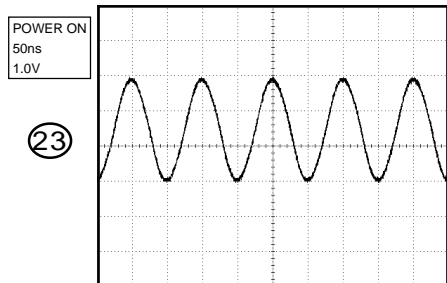
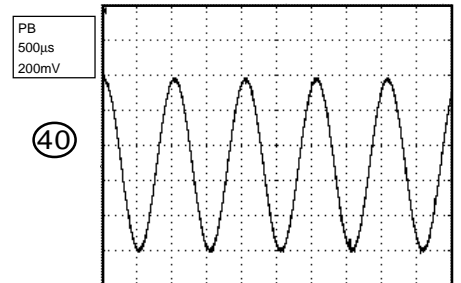
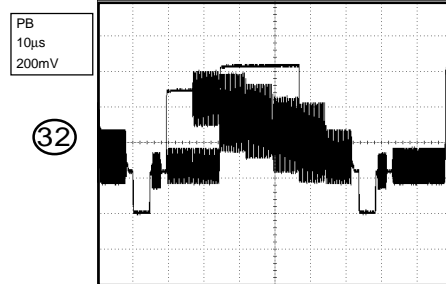
NOTE: The following waveforms were measured at the point of the corresponding balloon number in the schematic diagram.

WAVEFORMS

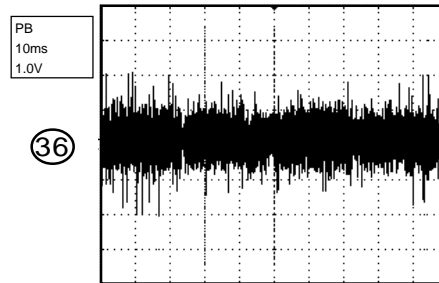
SYSCON



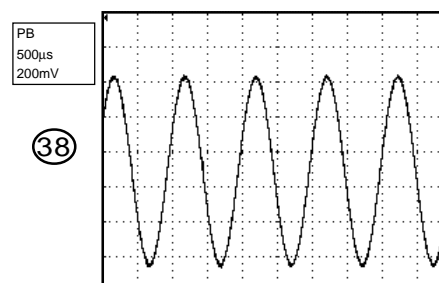
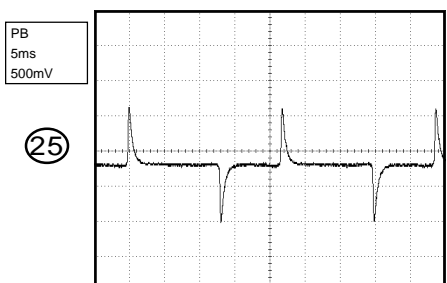
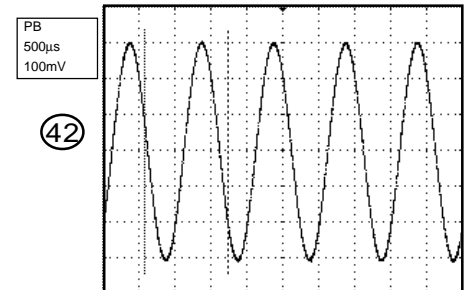
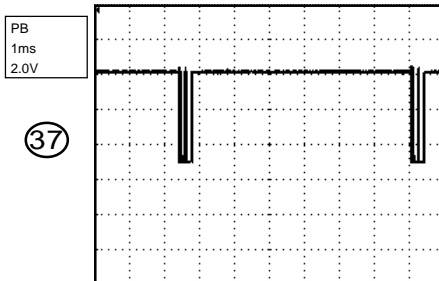
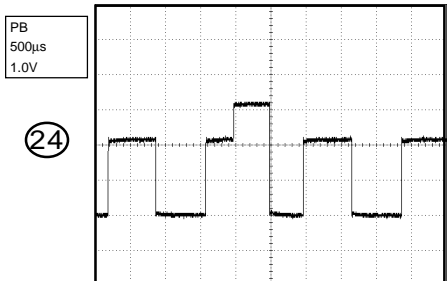
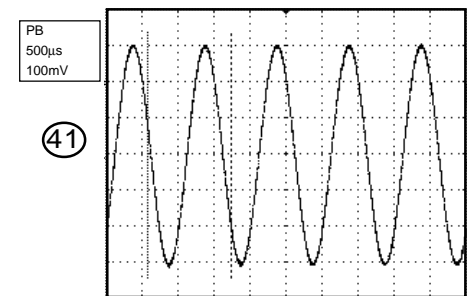
TUNER/JACK



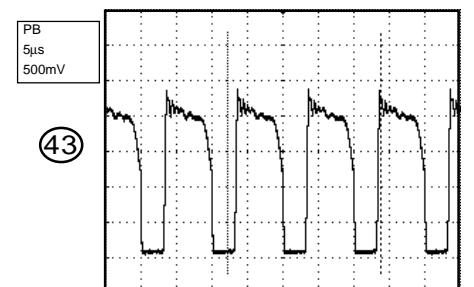
Hi-Fi/DEMODULATOR



TUNER/JACK

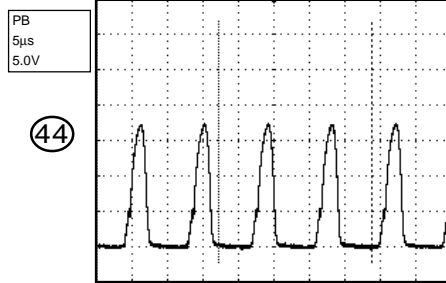


POWER

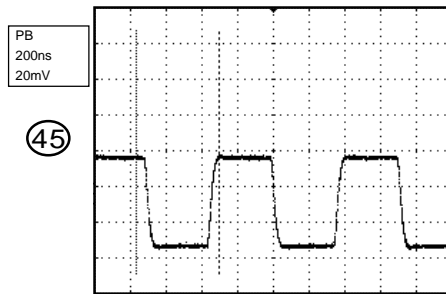


NOTE: The following waveforms were measured at the point of the corresponding balloon number in the schematic diagram.

WAVEFORMS



TUNER/JACK



NOTE: The following waveforms were measured at the point of the corresponding balloon number in the schematic diagram.

TOSHIBA CORPORATION

1-1, SHIBAURA 1-CHOME, MINATO-KU, TOKYO 105-8001, JAPAN