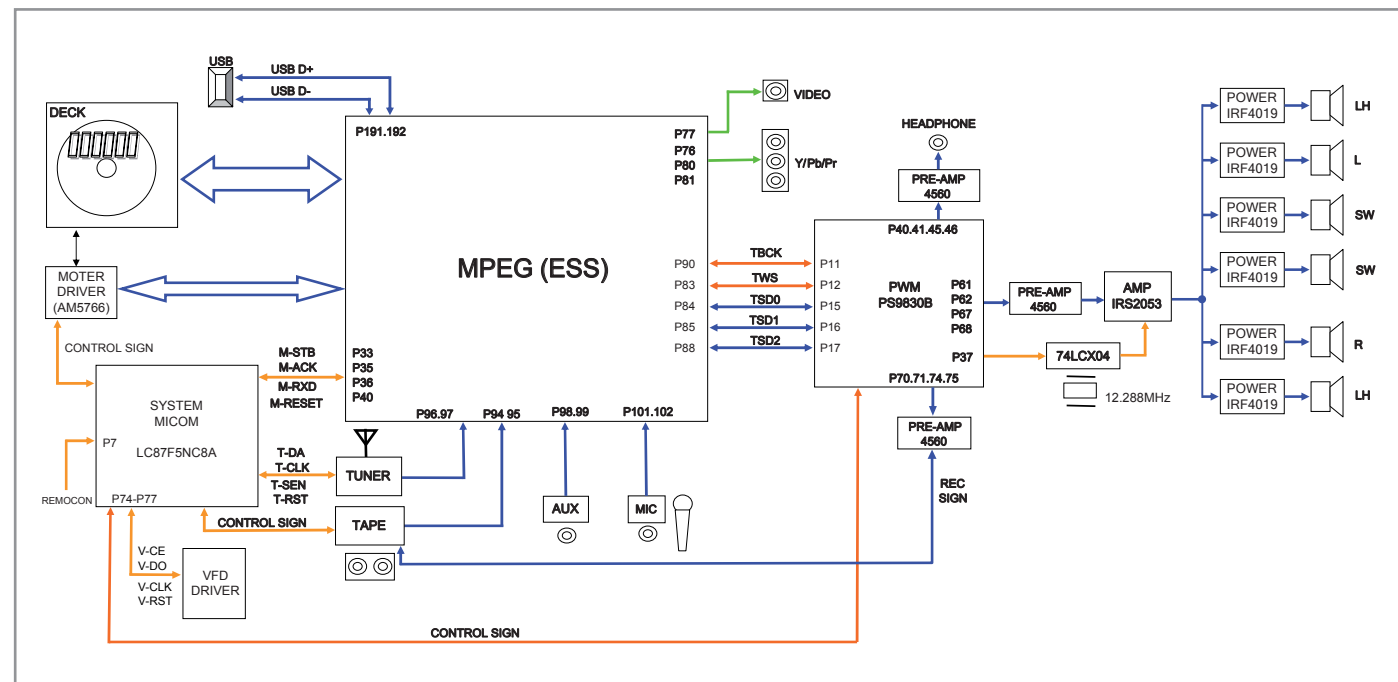


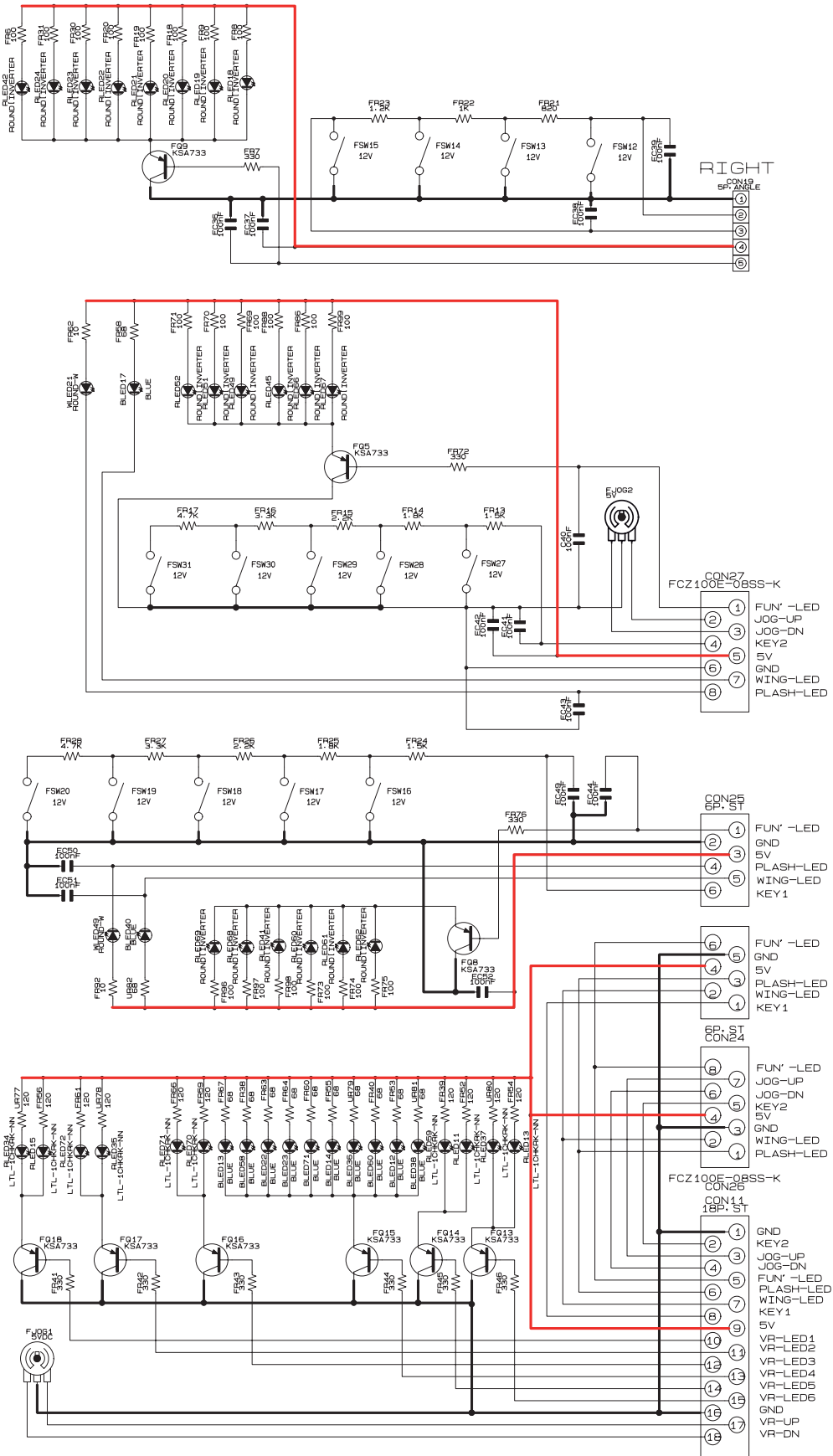
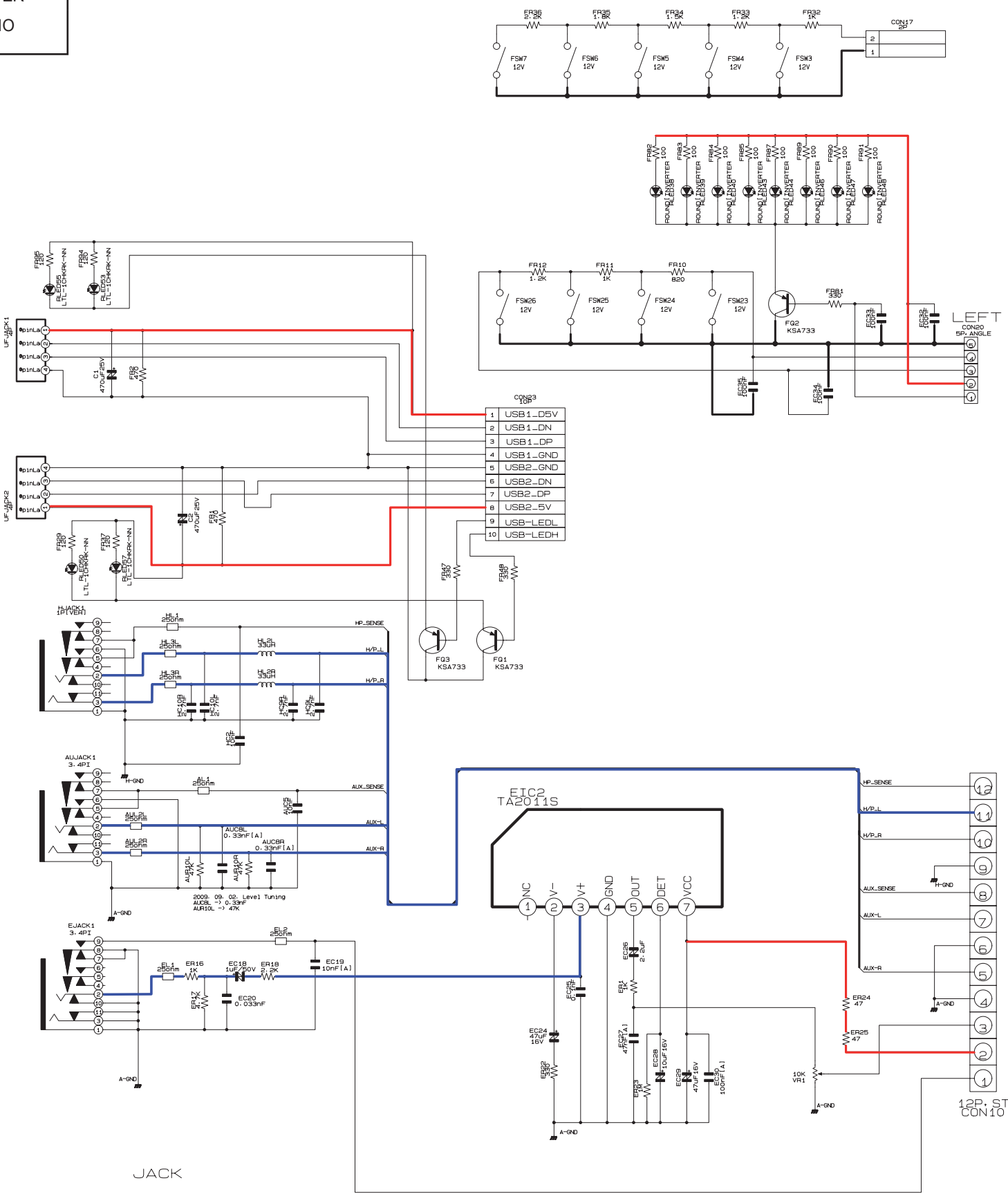
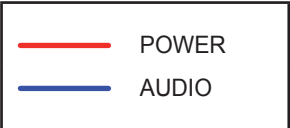
7. Schematic Diagram

7-1 Overall Block Diagram

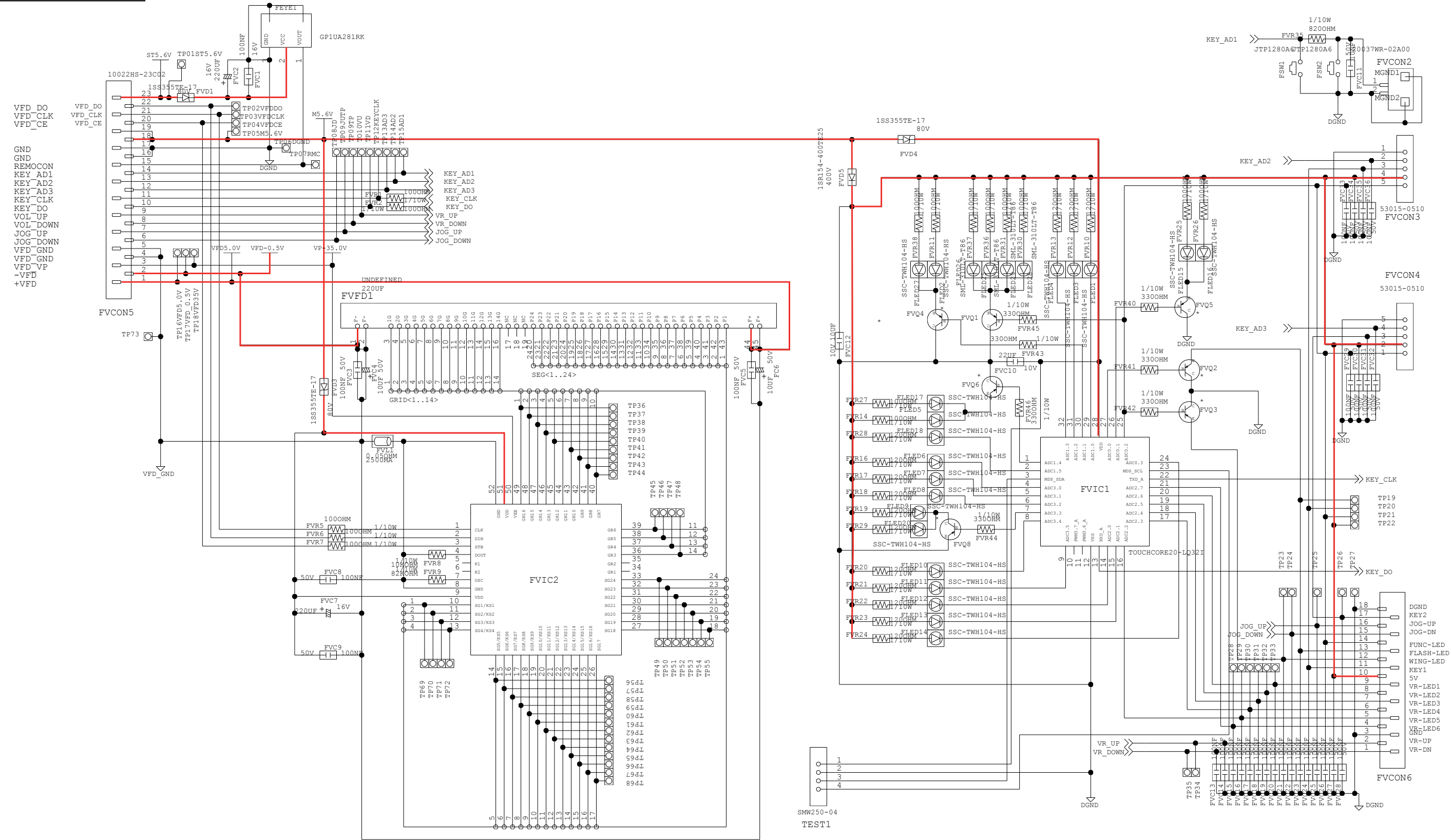


- The Main Micom IC is LC87F5NC8A. It's same as last year. MPEG chip is ES8391SCD. It's upgrade version of ES8390SCD.
- The Main Micom control every IC in this PCB.
- Video signal is come from DVD mecha and go to the MPEG IC. Then MPEG IC decode this signal to real video signal.
- Audio signal is come from DVD mecha, TUNER, AUX, MIC. Then MPEG IC decode to digital I2S signal. This signal goto the PS9830(PWM modulator) to convert PWM signal. PWM signal amplified at IR AMP stage and then Low Pass Filtered. This signal is real audio signal.

7-2 FRONT

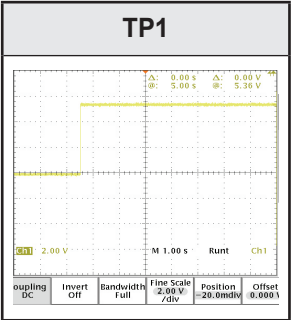
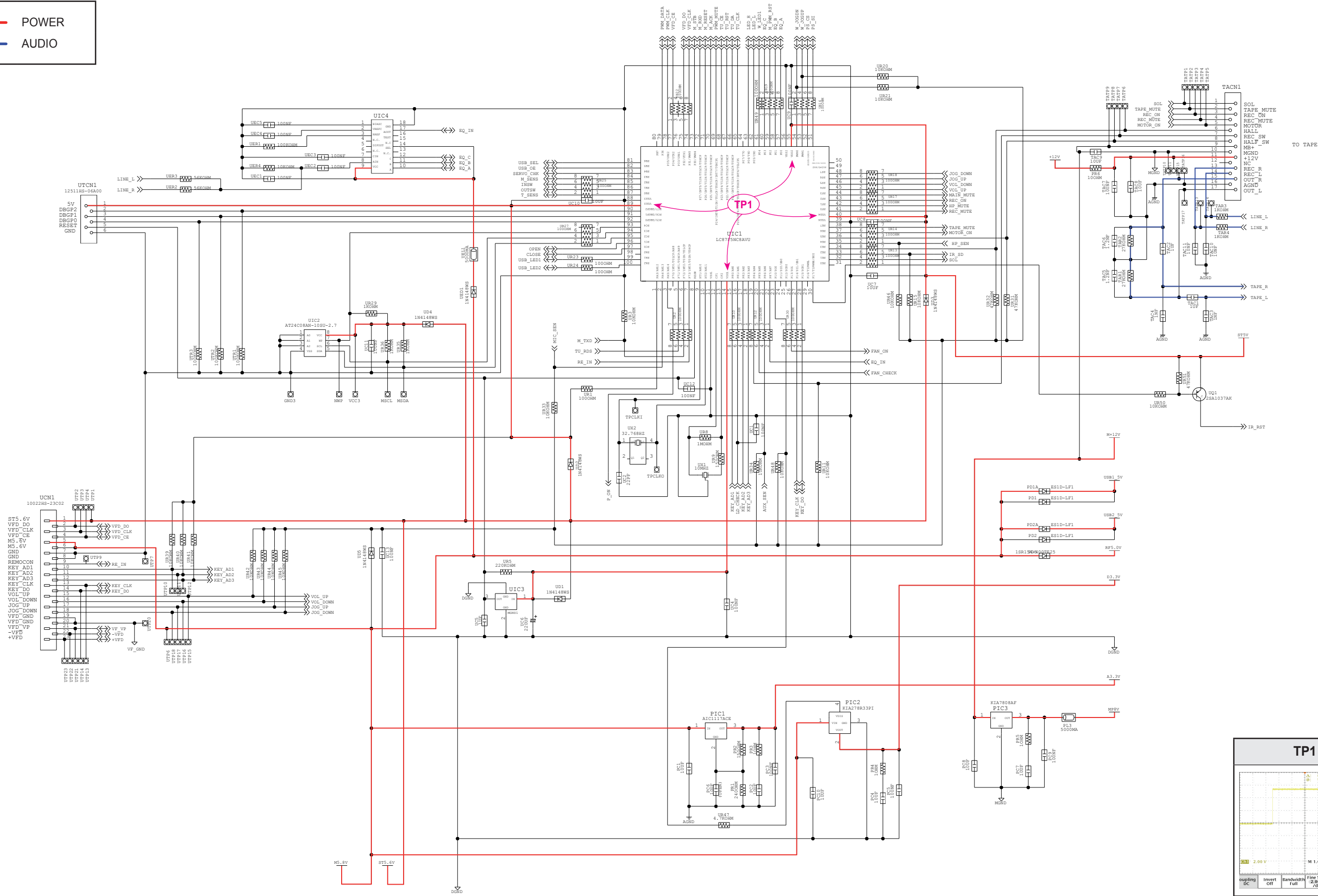


7-3 VFD





7-5 MICOM

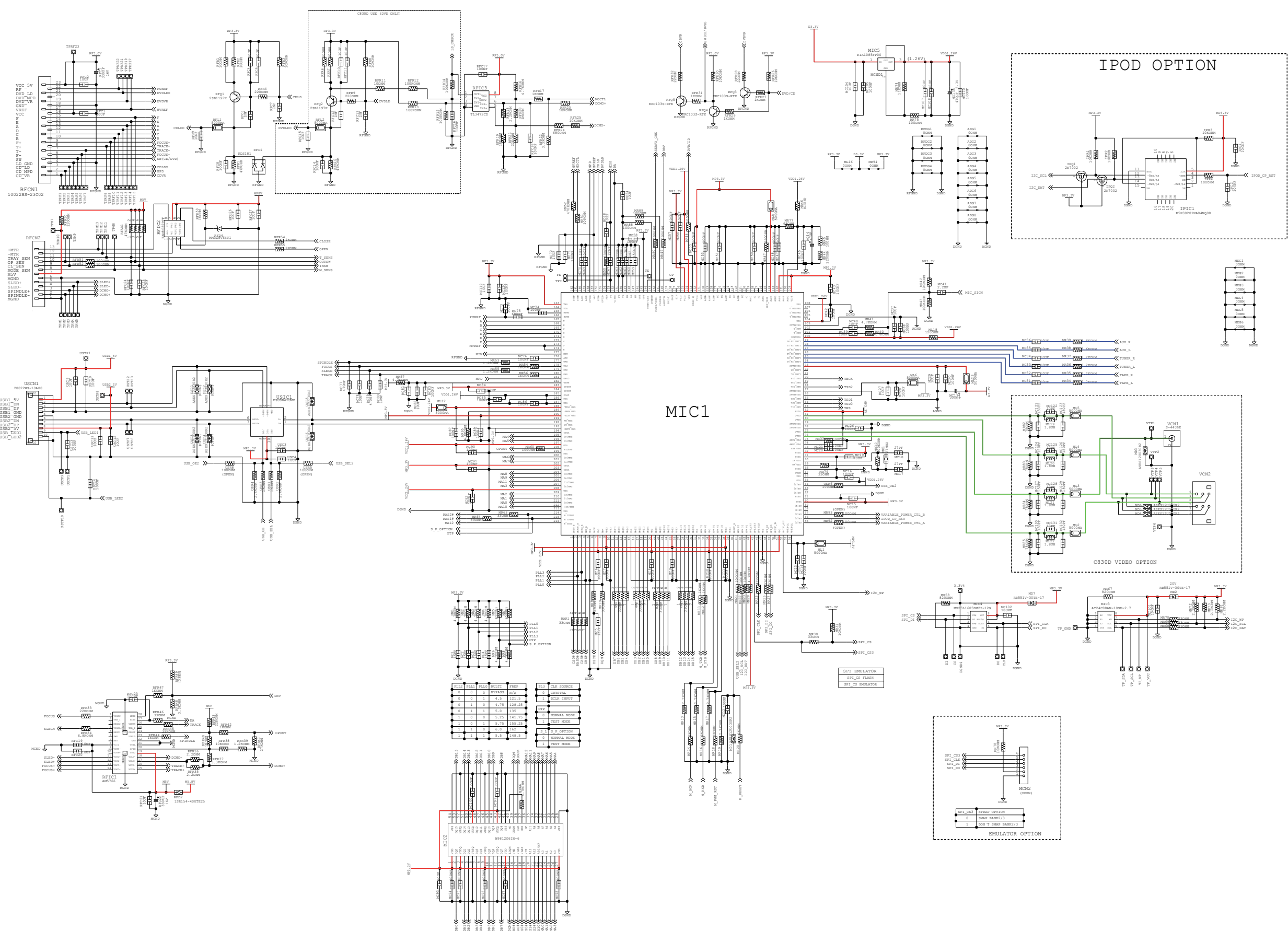


7-6 MPEG

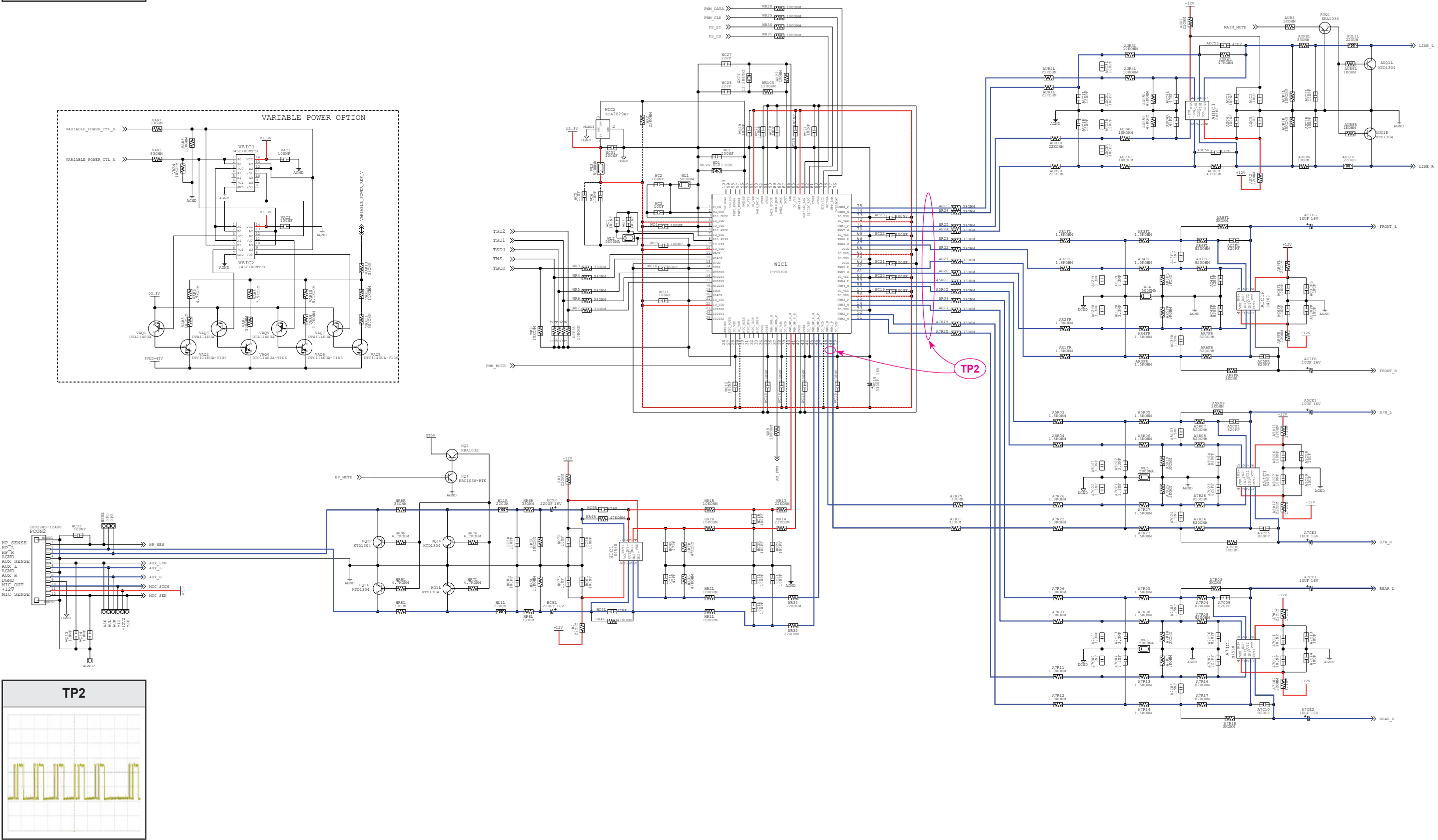
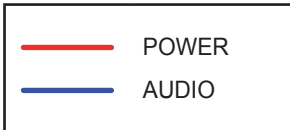
POWER

AUDIO

VIDEO



7-7 PWM



7-8 AMP

