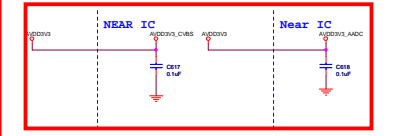
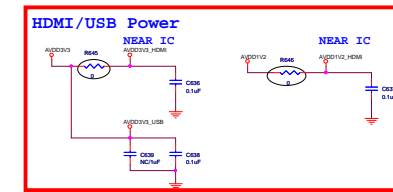
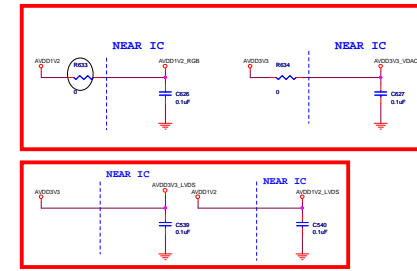
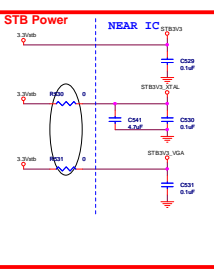
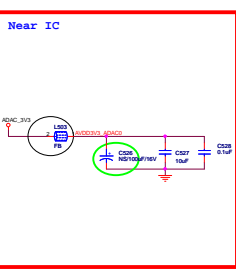
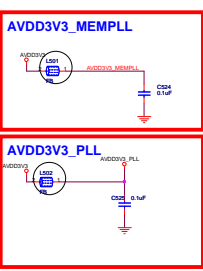
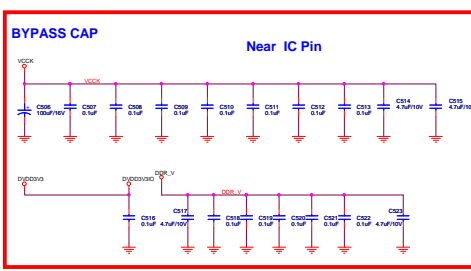
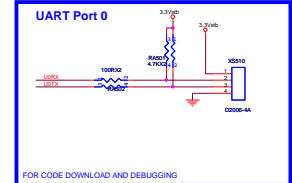
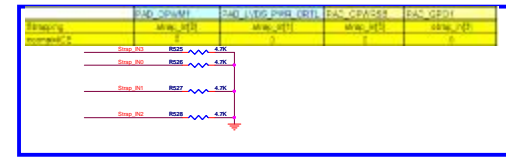
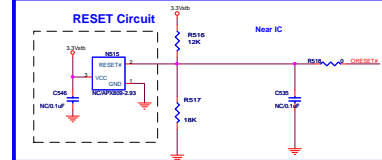
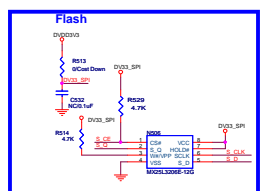
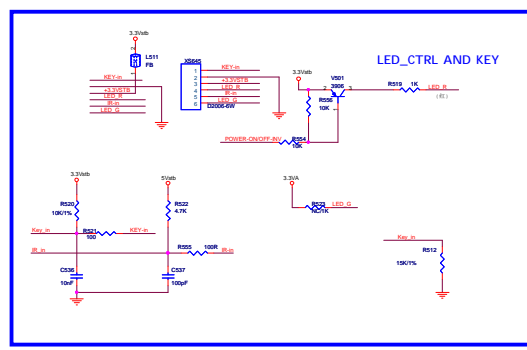
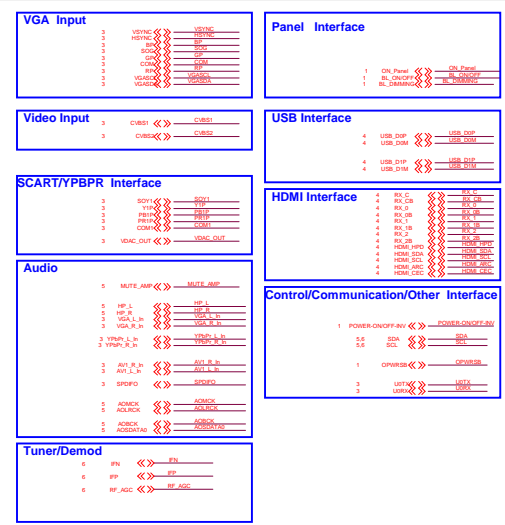
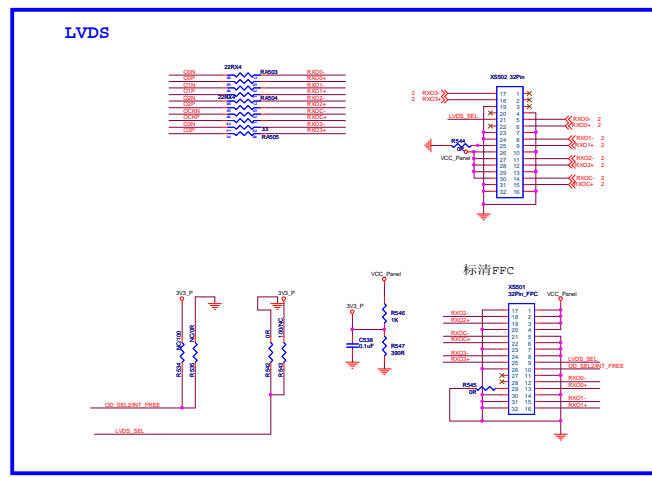
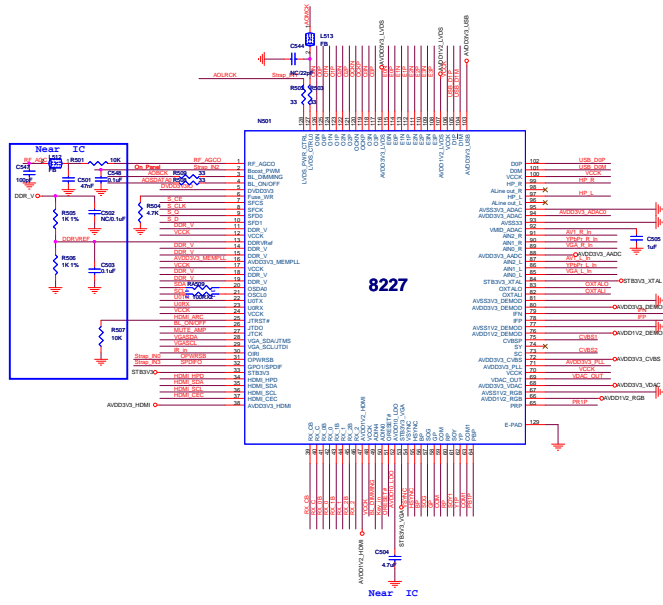


PIN Name	Function	STB GPIO
HDMI HPD	HDMI Hotplug	Y
JTRST#	SPI Flash Write Protect Control (L: WP Enable)	Y
JTDO	BL ON/OFF Control (L: ON)	Y
JTCK	AMP Mute Control (L: MUTE)	Y
VGA/SDA/JTMS	VGA/SDA	Y
VGA/SCL/JTDI	VGA/SCL	Y
OPWRSB	Power Module ON (L: ON)	Y
GPIO1/SPDIF	SPDIF OUT	Y
U0TX	U0TX	Y
U0RX	U0RX	Y
ADIN0	KEY IN	Y
ADIN4	BL_Dimming	Y
RF_AGCO	RF_AGCO	N
BoostPWM	LVDS Power Control(H: ON)	N
BL_Dimming	Aobck	N
BL_ON/OFF	Aosdata0	N
LVDS PWR Control	Aolrck	N
LVDS CTRL0	Aomck	N

Rev	History	P#	DATE
V0.1	INITIAL VERSION		
V1.0	3300 First VERSION		

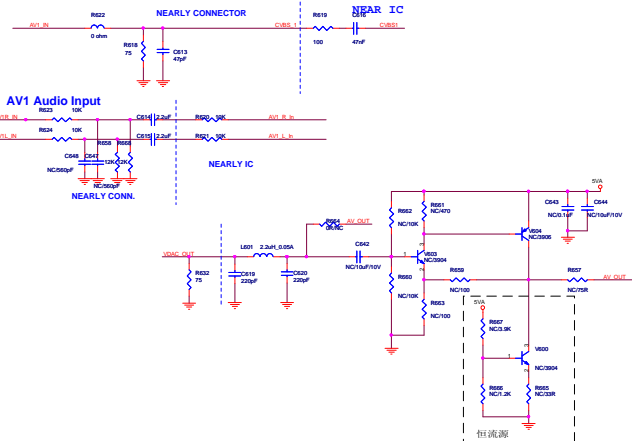
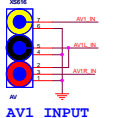


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工 艺	<1>E									
标准化	<=0>									
批准	<=0>									

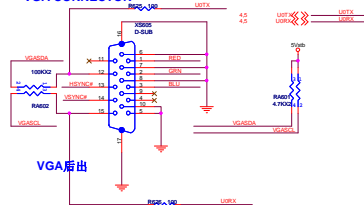
04 Main Chip/Bypass Cap

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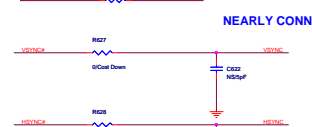
AV1 Input



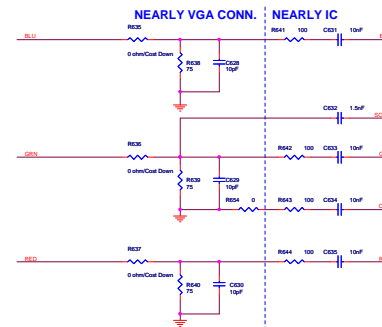
VGA CONNECTOR



VGA 输出

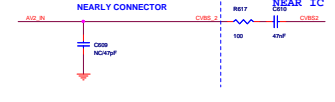


VGA Audio Input 使用YPbPr 的Audio

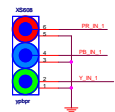
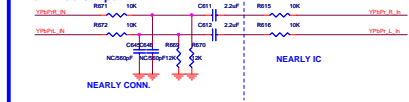


YPbPr & AV2 Input

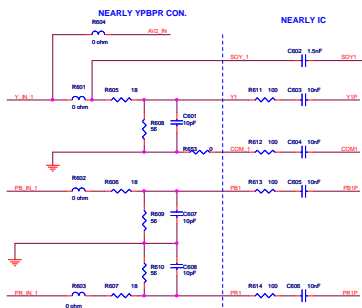
AV2 Video Input



YPbPr Audio Input



YPbPr&AV2&VGA INPUT



Audio	1	HP/OUT_L	HP/OUT_L
	2	HP/OUT_R	HP/OUT_R
	3	HP/MUTE	HP/MUTE
	4	YPbPr_L_R	YPbPr_L_R
	5	YPbPr_L_R	YPbPr_L_R
	6	YPbPr_L_R	YPbPr_L_R
	7	YPbPr_L_R	YPbPr_L_R
	8	YPbPr_L_R	YPbPr_L_R
	9	YPbPr_L_R	YPbPr_L_R
	10	YPbPr_L_R	YPbPr_L_R
	11	YPbPr_L_R	YPbPr_L_R
	12	YPbPr_L_R	YPbPr_L_R
	13	YPbPr_L_R	YPbPr_L_R
	14	YPbPr_L_R	YPbPr_L_R
	15	YPbPr_L_R	YPbPr_L_R

Video Input	1	CIN01	CIN01
	2	CIN02	CIN02
	3	CIN03	CIN03
	4	CIN04	CIN04
	5	CIN05	CIN05
	6	CIN06	CIN06
	7	CIN07	CIN07
	8	CIN08	CIN08
	9	CIN09	CIN09
	10	CIN10	CIN10
	11	CIN11	CIN11
	12	CIN12	CIN12
	13	CIN13	CIN13
	14	CIN14	CIN14
	15	CIN15	CIN15

SCART Interface	1	VMAC_OUT	VMAC_OUT
	2	VMAC_OUT	VMAC_OUT
	3	VMAC_OUT	VMAC_OUT
	4	VMAC_OUT	VMAC_OUT
	5	VMAC_OUT	VMAC_OUT
	6	VMAC_OUT	VMAC_OUT
	7	VMAC_OUT	VMAC_OUT
	8	VMAC_OUT	VMAC_OUT
	9	VMAC_OUT	VMAC_OUT
	10	VMAC_OUT	VMAC_OUT
	11	VMAC_OUT	VMAC_OUT
	12	VMAC_OUT	VMAC_OUT
	13	VMAC_OUT	VMAC_OUT
	14	VMAC_OUT	VMAC_OUT
	15	VMAC_OUT	VMAC_OUT

VGA Input	1	VSYNC	VSYNC
	2	Hsync	Hsync
	3	Red	Red
	4	Green	Green
	5	Blue	Blue
	6	Red	Red
	7	Green	Green
	8	Blue	Blue
	9	Red	Red
	10	Green	Green
	11	Blue	Blue
	12	Red	Red
	13	Green	Green
	14	Blue	Blue
	15	Red	Red

标记	数量	分区	单号	签名	日期	标记	数量	分区	单号	签名	日期	等级	标记
设计	1					设计	1						
审核	1					审核	1						
工艺	1					工艺	1						
标准化	1					标准化	1						
批准	1					批准	1						

06_AV1YPbPr



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Audio MUTE Circuit

12V

R203 NC390

N205

RESET#

VCC

GND

APX009-2.53

R201 150

R242 NC3R

AMP_RESET

200ms

R222 10K

R243 20K

AMP_RESET

3.3Vsb

R586 10K

AMP_EN

R223 1K

R214 NC47K

R219 4.7K

V214 3904

MUTE_AMP

3.3Vsb

R217 4.7K

R216 4.7K

V217 3904

HP MUTE

3.3Vsb

HI => MUTE

LO => UN-MUTE

Headphone output

Close to Mainchip

HP_L

HP_R

HPOL

HPOR

HPOUT_L

HPOUT_R

R224 0R20K

R226 0R20K

R228 NC47

R227 NC47

C227 NC0.1uF

C228 NC0.1uF

C239 220uF/16V

C238 220uF/16V

R232 NC47K

R231 NC47K

R235 10K/Cost Down

R236 10K/Cost Down

C224 NC470pF

C226 NC470pF

ESD

PESD3V3L2BT

DB05

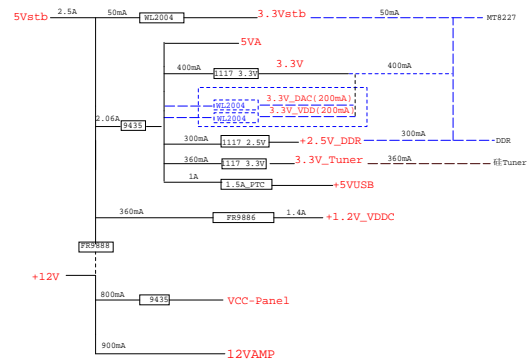
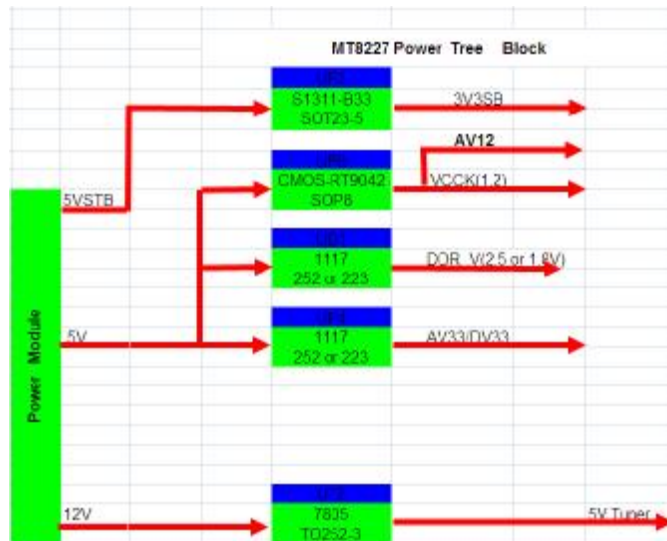
Control

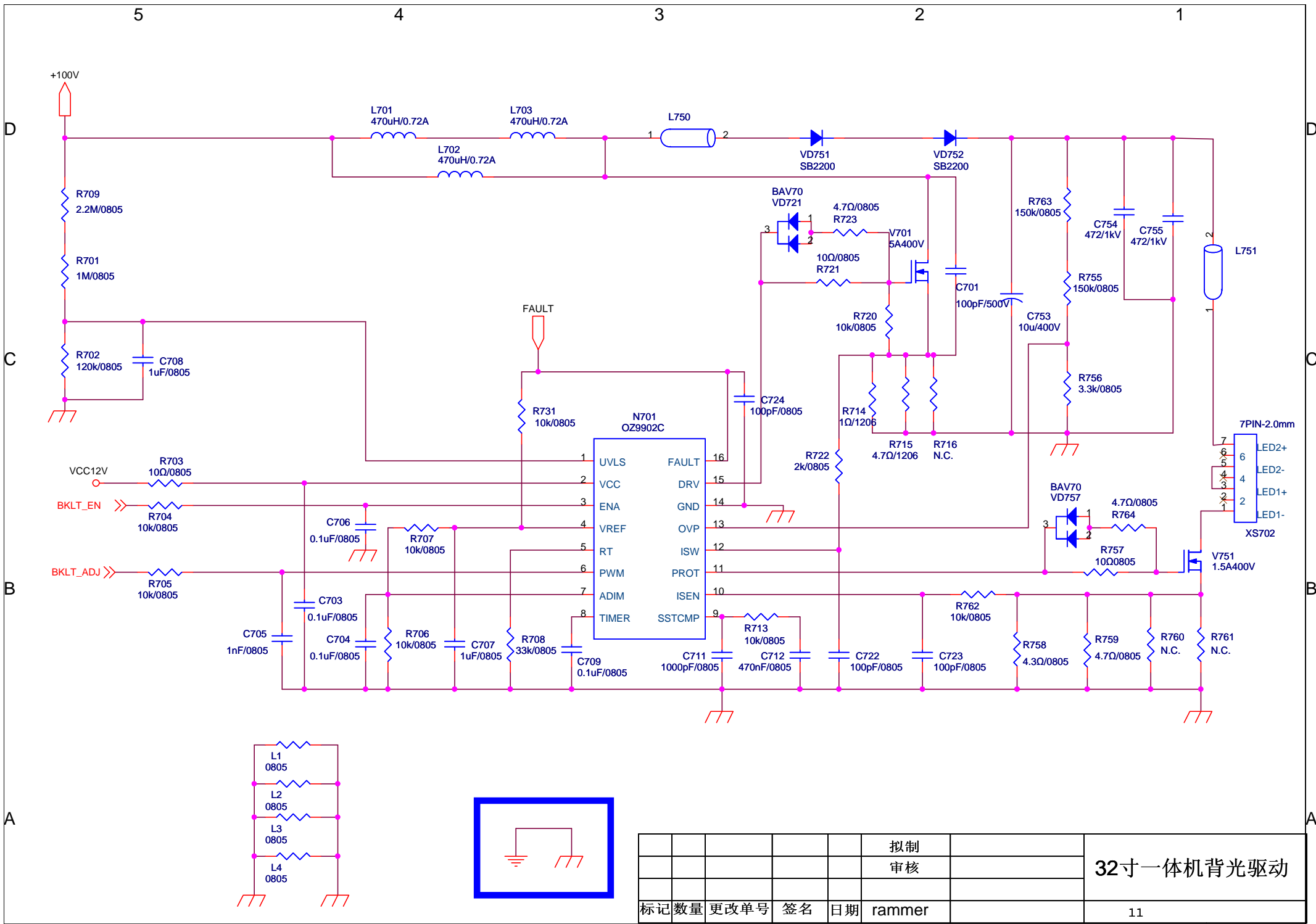
2	MUTE_AMP	<<>>	MUTE_AMP
2.6	SCL	<<>>	SCL
2.6	SDA	<<>>	SDA

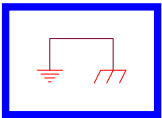
Audio

2	HP_L	《》	HP_L
2	HP_R	《》	HP_R
3	HPOUT_R	《》	HPOUT_R
3	HPOUT_L	《》	HPOUT_L
3	HP_MUTE	《》	HP_MUTE
2	AOMCK	《》	AOMCK
2	AOLRCK	《》	AOLRCK
2	AOBCK	《》	AOBCK
2	AOSDATA0	《》	AOSDATA0

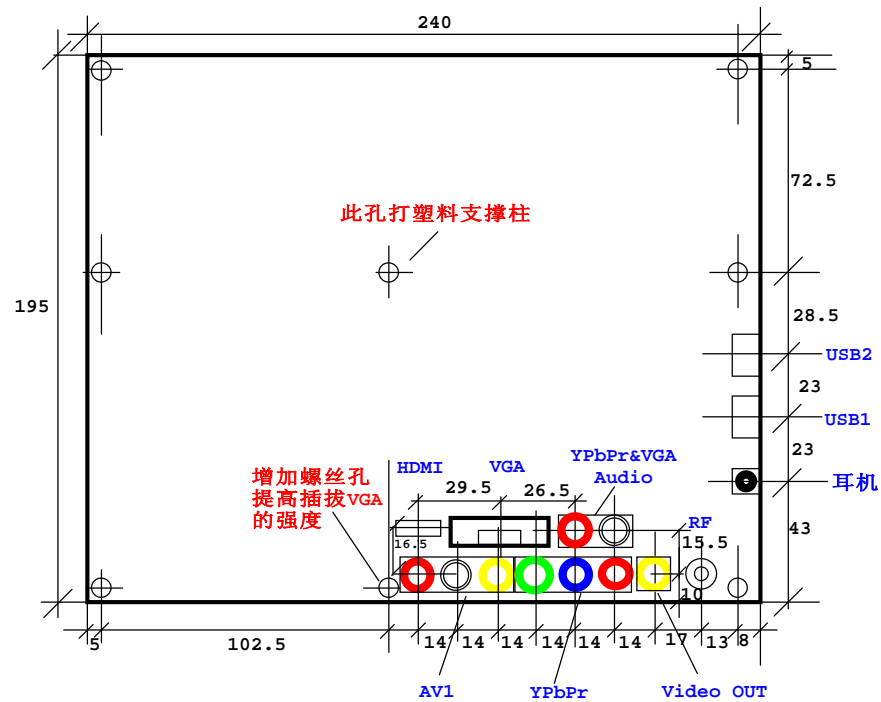
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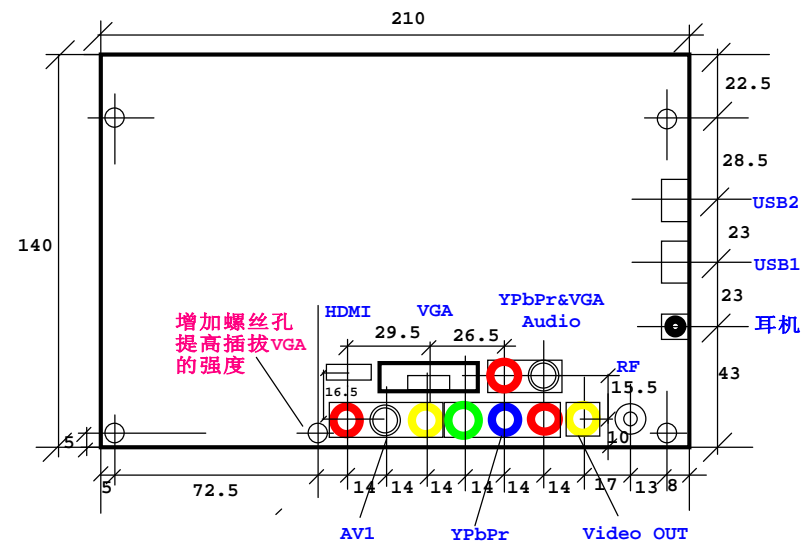




LED40/42F3300C



LED32F3300C



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