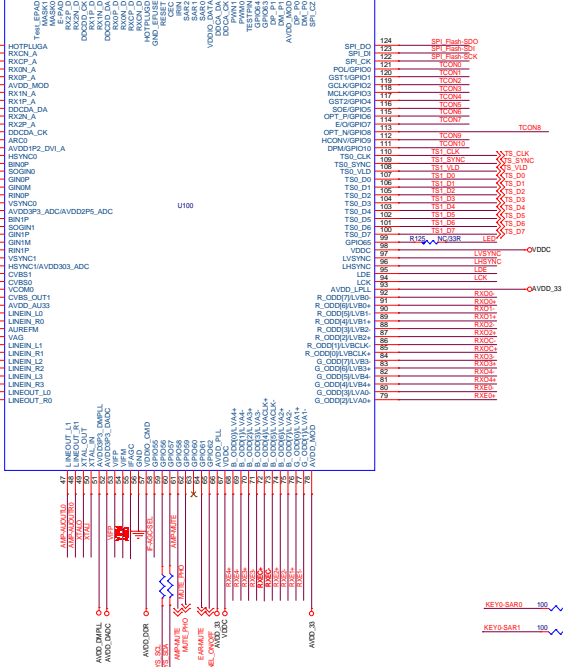
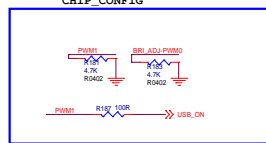
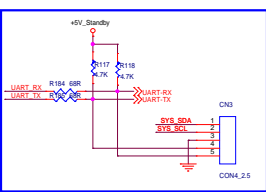
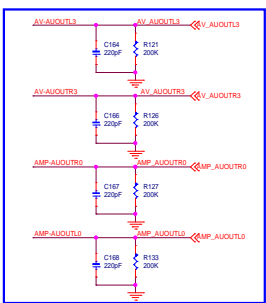
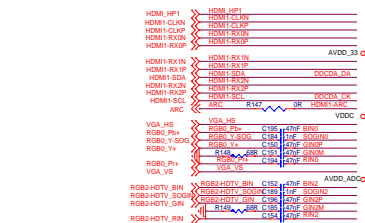


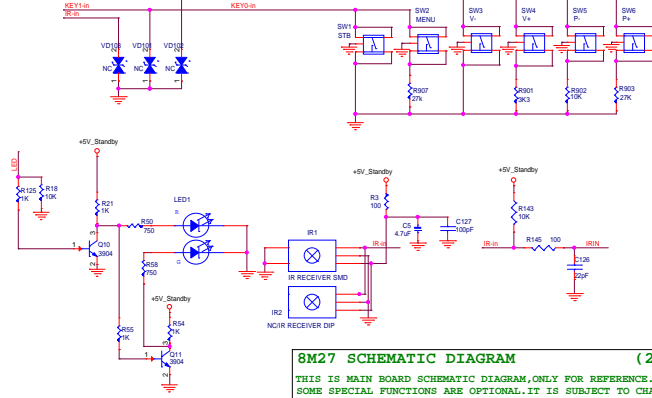
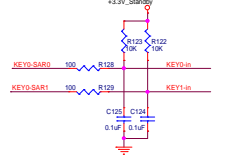
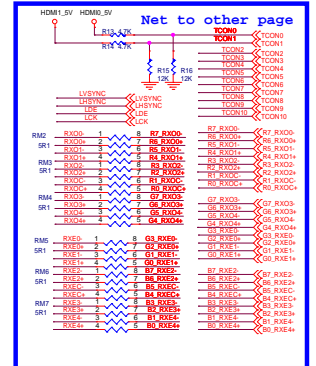
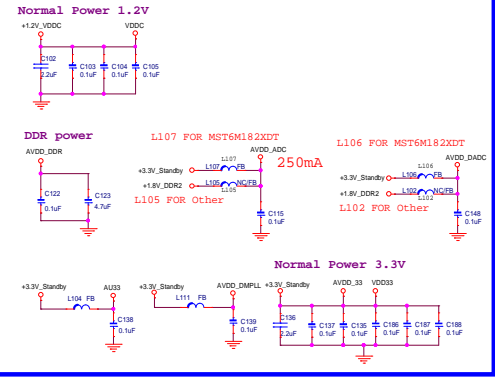
8M27 SCHEMATIC DIAGRAM

(1)

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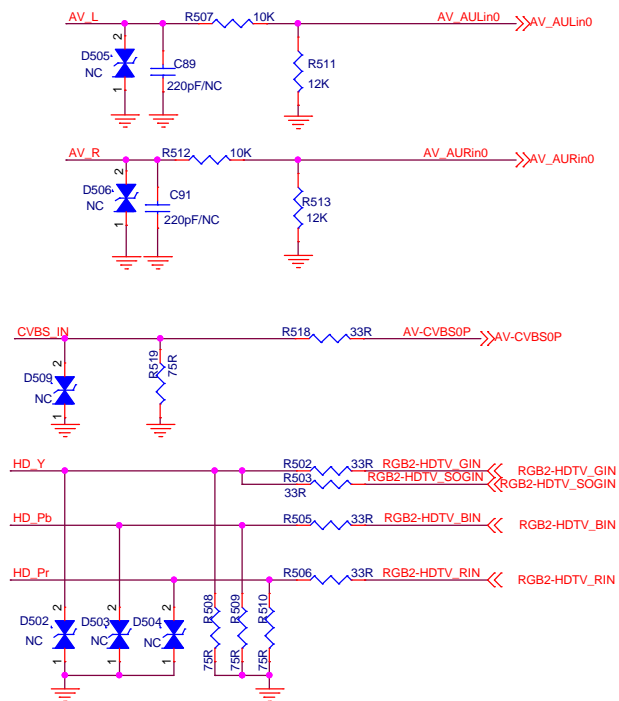
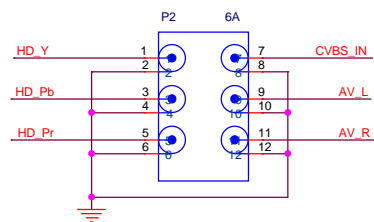
The diagram shows the SPI interface between the ATmega32 microcontroller and the DS18B20 temperature sensor. The ATmega32 is represented by a 28-pin package with pins labeled: 1 (CE#), 2 (VDD), 3 (HOLD#), 4 (WP#), 5 (BCK#), 6 (SI), 7 (VSS), 8 (EN#), 9 (A0), 10 (A1), 11 (A2), 12 (A3), 13 (A4), 14 (A5), 15 (A6), 16 (A7), 17 (A8), 18 (A9), 19 (A10), 20 (A11), 21 (A12), 22 (A13), 23 (A14), 24 (A15), 25 (A16), 26 (A17), 27 (A18), 28 (A19). The DS18B20 is represented by a 4-pin package with pins labeled: 1 (GND), 2 (VCC), 3 (DQ), 4 (NC). The connections are as follows: VCC of DS18B20 is connected to +3.3V Standby; GND of DS18B20 is connected to GND; DQ of DS18B20 is connected to SI of ATmega32; CE# of ATmega32 is connected to CSN of DS18B20; HOLD# of ATmega32 is connected to CSN of DS18B20; WP# of ATmega32 is connected to CSN of DS18B20; BCK# of ATmega32 is connected to DQ of DS18B20; SI of ATmega32 is connected to DQ of DS18B20; EN# of ATmega32 is connected to DQ of DS18B20. The ATmega32 is also connected to a 10k pull-up resistor to VCC and a 0.1μF capacitor to GND.



8M27 SCHEMATIC DIAGRAM (2)

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YPbPr/CVBS IN

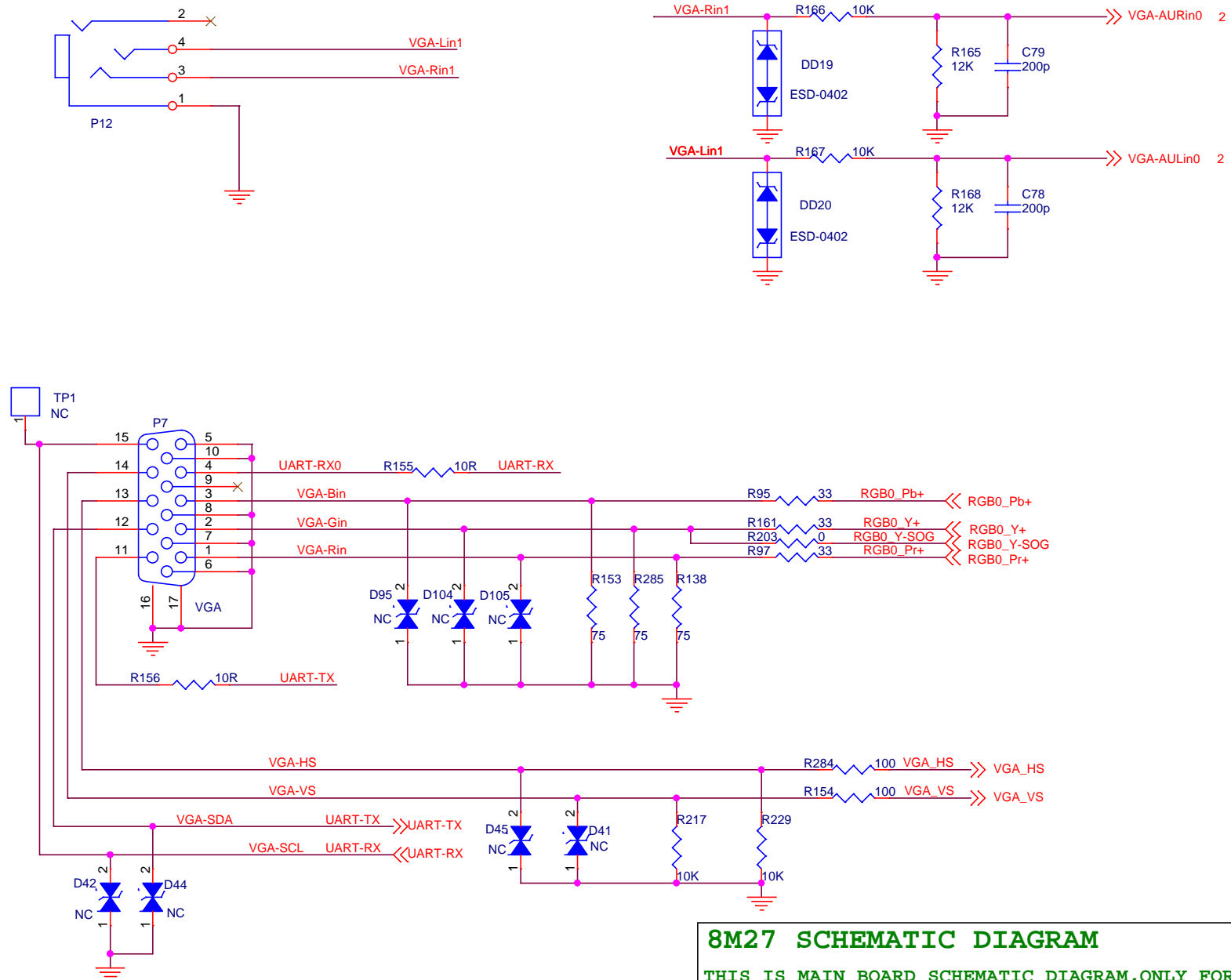


8M27 SCHEMATIC DIAGRAM

(3)

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VGA AUDIO INPUT

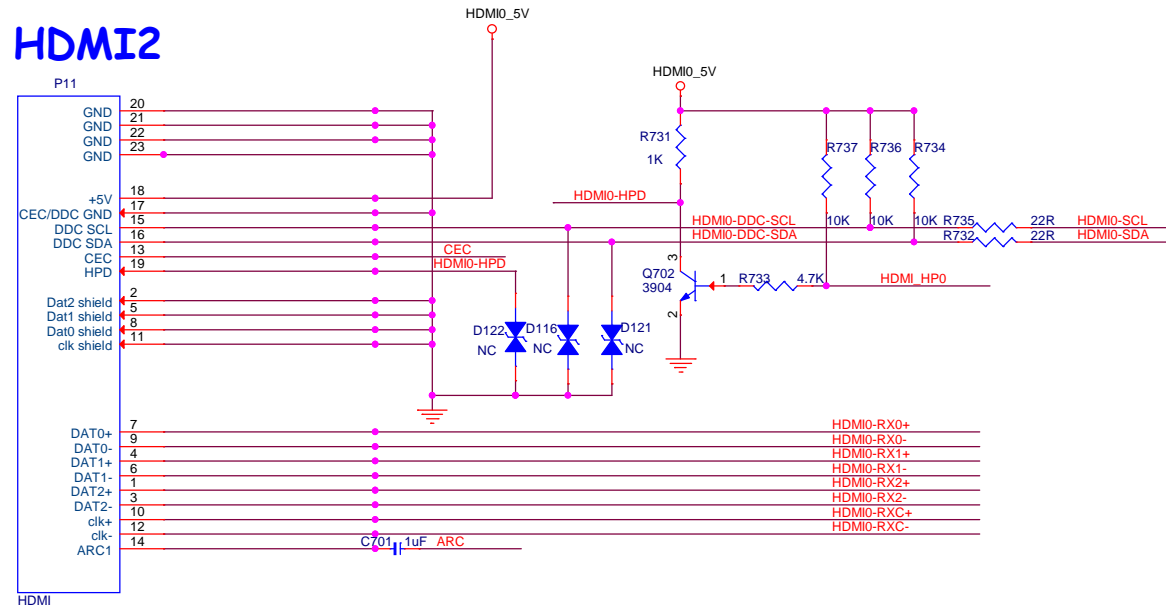


8M27 SCHEMATIC DIAGRAM

(4)

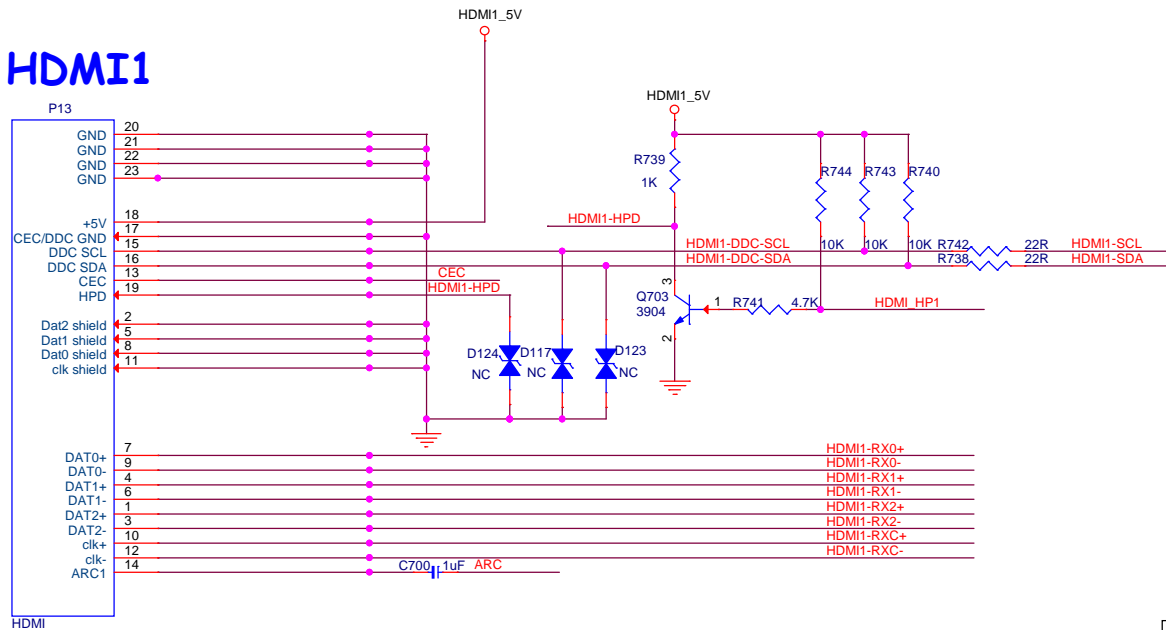
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HDMI2



| | | | | |
|------------|------|------|------------|------------|
| HDMIO-RX2+ | R701 | 10R | HDMIO-RX2P | HDMIO-RX2P |
| HDMIO-RX2- | R702 | 10R | HDMIO-RX2N | HDMIO-RX2N |
| HDMIO-RX1+ | R703 | 10R | HDMIO-RX1P | HDMIO-RX1P |
| HDMIO-RX1- | R704 | 10R | HDMIO-RX1N | HDMIO-RX1N |
| HDMIO-RX0+ | R705 | 10R | HDMIO-RX0P | HDMIO-RX0P |
| HDMIO-RX0- | R706 | 10R | HDMIO-RX0N | HDMIO-RX0N |
| HDMIO-RXC+ | R707 | 10R | HDMIO-CLKP | HDMIO-CLKP |
| HDMIO-RXC- | R708 | 10R | HDMIO-CLKN | HDMIO-CLKN |
| | | | HDMIO-HPD | HDMIO-HPD |
| | | | HDMIO-SCL | HDMIO-SCL |
| | | | HDMIO-SDA | HDMIO-SDA |
| | | | ARC | ARC |
| CEC | R709 | 200R | HDMI-CEC | HDMI-CEC |

HDMI1

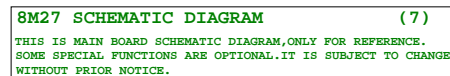
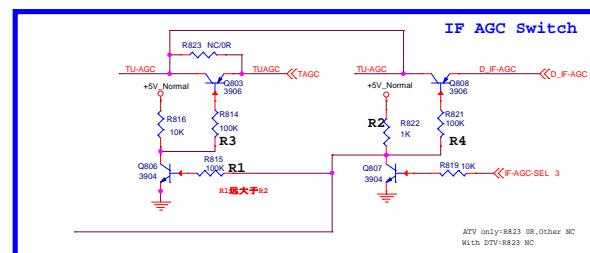


| | | | | |
|------------|------|-----|------------|------------|
| HDMI1-RX2+ | R716 | 10R | HDMI1-RX2P | HDMI1-RX2P |
| HDMI1-RX2- | R717 | 10R | HDMI1-RX2N | HDMI1-RX2N |
| HDMI1-RX1+ | R718 | 10R | HDMI1-RX1P | HDMI1-RX1P |
| HDMI1-RX1- | R719 | 10R | HDMI1-RX1N | HDMI1-RX1N |
| HDMI1-RX0+ | R720 | 10R | HDMI1-RX0P | HDMI1-RX0P |
| HDMI1-RX0- | R721 | 10R | HDMI1-RX0N | HDMI1-RX0N |
| HDMI1-RXC+ | R722 | 10R | HDMI1-CLKP | HDMI1-CLKP |
| HDMI1-RXC- | R723 | 10R | HDMI1-CLKN | HDMI1-CLKN |
| | | | HDMI1-HPD | HDMI1-HPD |
| | | | HDMI1-SCL | HDMI1-SCL |
| | | | HDMI1-SDA | HDMI1-SDA |
| | | | ARC | ARC |

8M27 SCHEMATIC DIAGRAM

(6)

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LVDS

