

**SANYO**

No.2917

**LA7655N**

Monolithic Linear IC

1-Chip IC for Color TV Signal Processing

The LA7655N, being a single-chip IC for NTSC color TV use, is a performance-improved version of the single-chip IC LA7650K heretofore in use and incorporates a circuit to process all types of signals (VIF, SIF, video, chroma, deflection) for color TVs based on the NTSC system. In designing this IC, its basic characteristics including synchronization performance are greatly improved, and taking into consideration its needed application in AV equipment such as VTRs, the necessity of adjustment is substantially reduced. Also, the number of components is reduced, and the element is made compact to achieve DIP42S (shrink type). A simple, compact color TV can be implemented by simply connecting a tuner, power supply, and output circuit to the LA7655N.

When using the LA7655N in conjunction with vertical output-use IC LA7835/7836, only one connection (vertical timing pulse) is required, with no connection required for feedback, thus simplifying layout of printed circuit pattern.

Single-chip IC variations on mother type LA7655N are available as shown in Table 1. Various needs such as improvement in performance or functions of color TV sets can be met by using these single-chip ICs in conjunction with peripheral ICs shown in Table 2.

#### Features

- Small-sized package
- Minimum number of external parts required
- VIF-SIF
  - Excellent buzz beat characteristics
  - High-gain VIF amplifier eliminating the need for a preamplifier
  - AGC speed can be increased.
- Video/audio simultaneous muting, or audio-only muting possible
- Video-Chroma
  - A quadratic differentiation circuit allowing soft video tone operation is also incorporated.
  - Adjustment-free chroma sync
- The LA7690 (flesh color corrector IC) can be connected to the LA7655N.
- Deflection
  - Adjustment-free horizontal, vertical sync
  - Dual AFC system with excellent horizontal noise characteristics
  - Vertical sync stabilizing circuit which is scarcely affected by motor noise.

#### Maximum Ratings at $T_a = 25^\circ\text{C}$

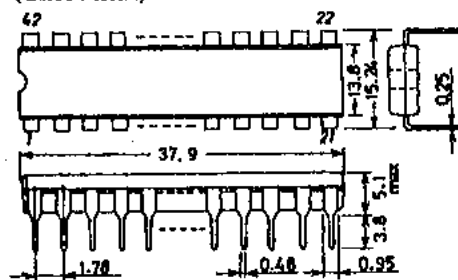
Maximum Supply Voltage	$V_0$ max	12	V
	$V_{11}$ max	12	V
Maximum Supply Current	$I_{20}$ max	16	mA
Allowable Power Dissipation	$P_d$ max	1.3	W
Operating Temperature	$T_{\text{opg}}$	-10 to +65	$^\circ\text{C}$

 $T \leq 65^\circ\text{C}$ 

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The application circuit diagrams and circuit constants herein are included as an example and provide no guarantee for designing equipment to be mass-produced. The information herein is believed to be accurate and reliable. However, no responsibility is assumed by SANYO for its use, nor for any infringements of patents or other rights of third parties which may result from its use.

#### Case Outline 3025B-D42SIC (unit: mm)



SANYO: DIP42S

Specifications and information herein are subject to change without notice.

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N308Y'T, TS No.2917-1/6

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Storage Temperature	Tstg	-55 to +125	unit
Circuit Current	I <sub>36</sub> max	-6	mA
	I <sub>4</sub> max	-3	mA
FBP Input Current	I <sub>21</sub> max	5	mA
	I <sub>19</sub> max	10	mA

Operating Conditions at Ta = 25°C

Recommended Supply Voltage	V <sub>9</sub>	9	V
	V <sub>11</sub>	9	V
Recommended Supply Current	I <sub>20</sub>	13	mA
Operating Voltage Range	V <sub>9</sub> op	8 to 10	V
	V <sub>11</sub> op	8 to 10	V
Operating Current Range	I <sub>20</sub> op	10 to 16	mA

Operating Characteristics at Ta = 25°C, V<sub>CC</sub> = V<sub>9</sub> = V<sub>11</sub> = 9V, I<sub>CC</sub> = I<sub>20</sub> = 13mA

[Circuit Voltage, Current]		min	typ	max	unit
Horizontal Supply Voltage	V <sub>20</sub>	V <sub>CC</sub> = 9V, I <sub>CC</sub> = 13mA	7.3	7.8	8.3 V
Supply Current	I <sub>9</sub> + I <sub>11</sub>	V <sub>CC</sub> = 9V, I <sub>CC</sub> = 13mA	67	93	124 mA
[VIF] f <sub>p</sub> = 58.75MHz					
Video Detector DC Output Voltage	V <sub>36</sub>	Quiescent	4.2	4.6	5.0 V
AFT Output Voltage	V <sub>38</sub>	Quiescent	2.8	4.2	5.7 V
Maximum RF AGC Control Voltage	V <sub>40H</sub>	CW = 85dBμ, RF AGC VR = min	7.6	8.0	8.3 V
Minimum RF AGC Control Voltage	V <sub>40L</sub>	CW = 85dBμ, RF AGC VR = max	0	0.01	0.3 V
VIF Input Sensitivity	V <sub>i</sub>	VIF input level at which video output is 0.8Vp-p (40%MOD).	30	36	42 dBμ
VIF AGC Control Range	GR	Maximum input (V <sub>0</sub> = 0.8Vp-p) - input sensitivity	62	70	dB
[VIF Maximum Permissible Input	V <sub>i</sub> max	VIF input level at which video output is +1dB.	102	110	dBμ
Video Detector Output	V <sub>036</sub>	V <sub>i</sub> = 80dBμ, AM = 78%MOD	1.7	2.0	2.3 Vp-p
Differential Gain	DG	V <sub>i</sub> = 80dBμ, 87.5%, VIDEO MOD	3.0	10	%
Differential Phase	DP	V <sub>i</sub> = 80dBμ, 87.5%, VIDEO MOD	3.0	10	deg
Video S/N	S/N	V <sub>i</sub> = 80dBμ, AM = 78% MOD	47	53	dB
CW					
Sync-Tip Level	V <sub>36</sub> TIP	CW = 80dBμ	2.0	2.3	2.6 V
[Video Frequency Characteristic	f <sub>c</sub>	Frequency at which video output is down 3dB.	5.0	7.0	MHz
VIF Intermodulation	I <sub>920</sub>	V3.58MHz/V920kHz, V <sub>i</sub> = 80dBμ	28	35	dB
Maximum AFT Control Voltage	V <sub>36H</sub>	CW = 80dBμ, frequency change	8.2	8.6	8.9 V
Minimum AFT Control Voltage	V <sub>36L</sub>	CW = 80dBμ, frequency change	0.1	0.3	0.8 V
AFT Detector Sensitivity	Sf	CW = 80dBμ, frequency change	50	80	120mV/kHz
AFT Switch Operation	V <sub>AFT SW</sub>	Test with sweep signal.	1.0	5.0	V
Start Voltage					
Black Noise Threshold Level	V <sub>BTH</sub>	Test with sweep signal.	1.2	1.5	1.8 V
White Noise Threshold Level	V <sub>WTH</sub>	Test with sweep signal.	4.9	5.3	5.7 V
[SIF.AF] f <sub>s</sub> = 4.5MHz					
Sound D Input Limiting Sensitivity	V <sub>ilim</sub>	SIF input level at which detection output is down 3dB.	48	55	dBμ

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FM Detector Output Voltage	V <sub>O1</sub>	V <sub>i</sub> = 100dB $\mu$ , $\Delta f = \pm 25$ kHz		380	550	750	mVrms
FM Detector Output Distortion	THD	V <sub>i</sub> = 100dB $\mu$ , $\Delta f = \pm 25$ kHz			0.4	1.0	%
AM Rejection	AMR	V <sub>i</sub> = 100dB $\mu$ , $\frac{FM:\Delta f = \pm 25k}{AM:30\%}$		40	53		dB
AF Amp Voltage Gain	G <sub>AF</sub>	V <sub>i</sub> = 100mVrms, f = 400Hz		18	20	22	dB
AF Maximum Output Voltage	V <sub>O4</sub> max	Output level at which AF amp output distortion is 10%		2.0	3.0		Vrms
AF Electronic Attenuator Range	A <sub>1T</sub>	V <sub>i</sub> = 200mVrms, f = 400Hz		70	80		dB
[Video]							
Soft Video Tone Variable Range	$\Delta$ Soft	f = 2MHz, 100mVp-p, video tone VR: 4V $\rightarrow$ 9V		-6	-4	-2	dB
Sharp Video Tone Variable Range	$\Delta$ Sharp	f = 2MHz, 100mVp-p, video tone VR: 4V $\rightarrow$ 9V		8	11	14	dB
Video Voltage Gain	GV	f = 100kHz, 100mVp-p, contrast VR: 9V, video tone VR: 4V		17	20	23	dB
Contrast Control Center	C <sub>CEN</sub>	f = 100kHz, 100mVp-p, contrast VR: 6V		0.45	0.57	0.69	Vp-p
Contrast Variable Range	$\Delta$ CV	Contrast VR: 3V $\rightarrow$ 9V		19	21	23	dB
Bright Control	B <sub>RI1</sub>	Bright VR: 2V		5.8			V
	B <sub>RCEN</sub>	Bright VR: 4.5V		2.6	3.1	3.6	V
	B <sub>RI2</sub>	Bright VR: 7V				1.2	V
Frequency Response	f <sub>v</sub>	Contrast VR: 6V, video tone VR: 4V, 3dB down		5	7		MHz
DC Transmission	R <sub>DC</sub>	Input: stair step signal, 200mVp-p		88	93		%
[Chroma]							
ACC Amplitude Characteristics	ACC1	+6dB		-3	0	+3	dB
	ACC2	-20dB		-7		+2	dB
ACC Phase Characteristics	ACCP1	+6dB		-3	0	+3	deg
	ACCP2	-20dB		-7		+7	deg
Killer Operating Point	EK			-51	-44	-37	dB
Color Control Minimum	EC min	Color VR: 0V, Contrast VR: 9V				30	mVp-p
Color Residue							
Color Control Center	EC CEN	Color VR: 4.5V, Contrast VR: 6V		1.6	2.4	3.2	Vp-p
Minimum Demodulator Output	EC max	Color VR: 9V, contrast VR: 9V		4.0	5.0		Vp-p
Contrast Color Variable Range	$\Delta$ CC	Color VR: B-Y = 2.5mVp-p, contrast VR: 3V $\rightarrow$ 9V		17.5	19	20.5	dB
Tint Control Center	T <sub>CEN</sub>	Tint VR: 4.5V, color VR: 4.5V, contrast VR: 6V		-16	-4	+8	deg
Tint Variable Range	$\Delta$ T	Tint VR: 0V $\leftarrow$ 4.5V $\rightarrow$ 9V, color VR: 4.5V, contrast VR: 6V		$\pm 40$			deg
APC Pull-In Range	$\Delta f_{APC}$			$\pm 350$			Hz
Demodulator Output Ratio	R/B	Monochrome signal, contrast VR: 6V, color VR: B-Y = 1Vp-p		0.81	0.90	0.98	times
G/B		Monochrome signal, contrast VR: 6V, color VR: B-Y = 1Vp-p		0.24	0.30	0.38	times

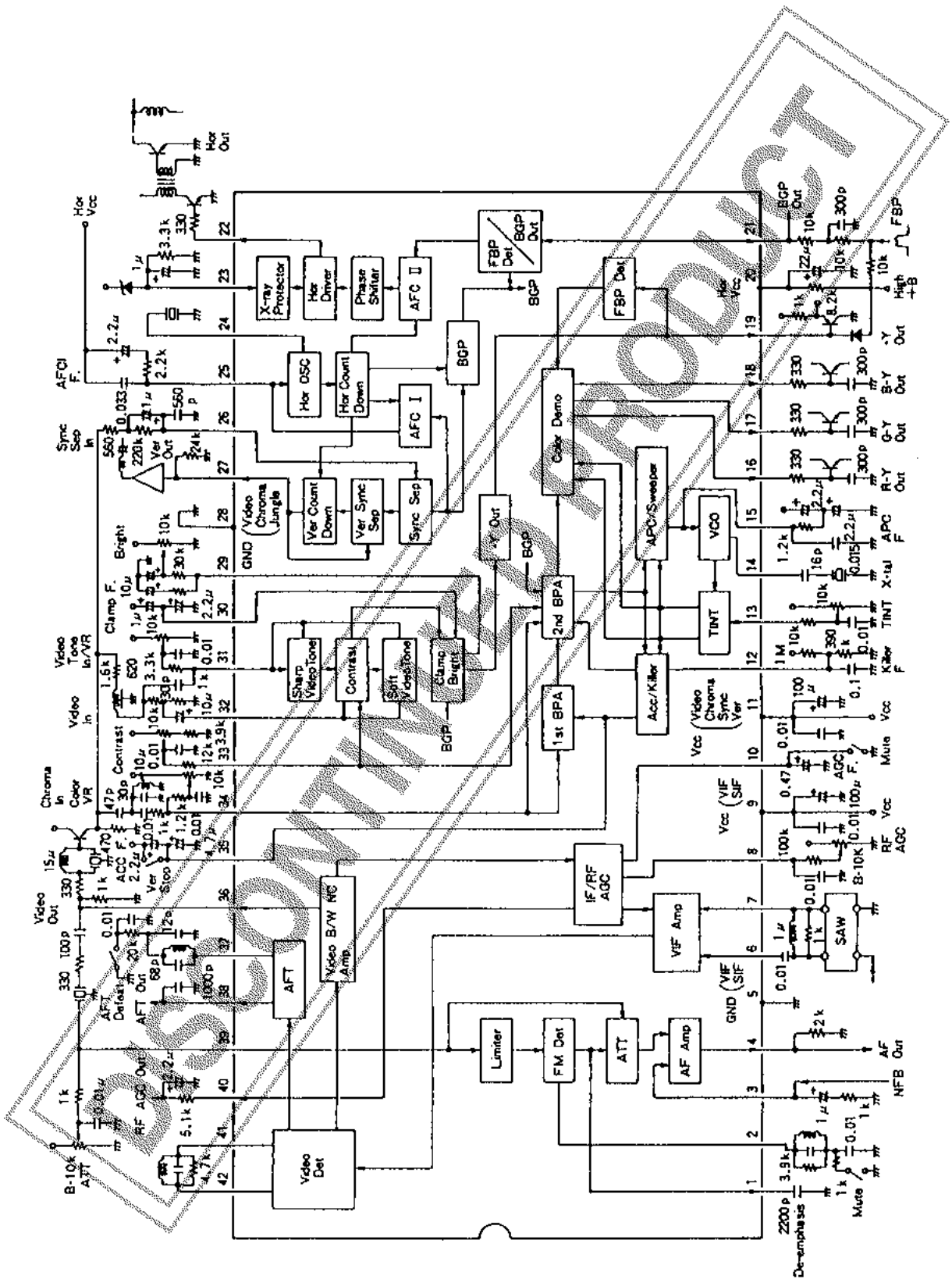
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			min	typ	max	unit
Demodulator Phase Angle	$\angle R_B$	Monochrome signal, contrast VR:6V, color VR:B-Y=1V <sub>po</sub>	97	105	113	deg
	$\angle G_B$	Monochrome signal, contrast VR:6V, color VR:B-Y=1V <sub>po</sub>	-130	-120	-110	deg
Demodulator Output DC Voltage	V <sub>C-Y</sub>	Burst signal only, color VR:0V	4.7	5.2	5.7	V
Demodulator Output Offset Voltage	$\Delta V_{C-Y}$	Burst signal only, color VR:0V	-300	0	+300	mV
Dependence of Demodulation Output on V <sub>CC</sub>	$\Delta E_C$	V <sub>CC</sub> =9V±1V	0	10	20	%/V
Residual Carrier	E <sub>car</sub>	Quiescent, killer OFF, color VR:0V			0.3	V <sub>p-p</sub>
[Deflection]						
Sync Separator Input DC Level	VS DC		6.0	6.3	6.6	V
Vertical Free-Running Period	TV max	Input: horizontal sync signal only	296.5	297	297.5	H
Vertical Minimum-Running Period	TV min		224.5	225	225.5	H
Vertical Blanking Pulse Width	P <sub>W</sub> VBL		17.25	17.5	17.75	H
Vertical Blanking Pulse Voltage	P <sub>H</sub> VBL		7.0	7.5		V
Vertical Output Pulse Width	PW V <sub>OUT</sub>		8.25	8.5	8.75	H
Vertical Output Pulse Voltage	V <sub>OUT H</sub> V <sub>OUT M</sub> V <sub>OUT L</sub>		5.7 4.2	6 4.5	6.3 4.8	V V V
Vertical External Trigger Load Resistance	R <sub>TR</sub>		2.7	3.6		kΩ
Vertical Automatic Synchronizer Stop Voltage	V <sub>SAS</sub>			1.9	2.4	V
Vertical Operation Start Voltage	S <sub>VV</sub>				4	V
Horizontal Free-Running Frequency Deviation	$\Delta f_H$	Deviation from 15.734kHz	-70	30	130	Hz
Dependence of Horizontal Free-Running Frequency on V <sub>CC</sub>	$\Delta f_H V_{CC}$	V <sub>20</sub> =6.7V reference value		2		Hz
Horizontal Sync Pull-in Range	f <sub>H</sub> PULL	Deviation from 15.734kHz	±400			Hz
Horizontal Output Pulse Width	P <sub>WH</sub> OUT		21.8	23.8	25.8	μs
Horizontal Output Pulse Phase	H <sub>PF</sub> H <sub>P</sub> CEN H <sub>PR</sub>		13 3.4	15 3.9		μs μs μs
Horizontal Operation Start Voltage	S <sub>HV</sub>			4.3	5	V
AFC II FBP Peak Voltage	F <sub>BPF</sub>		3.3	3.9	4.5	V
Burst Gate Pulse Delay Time	T <sub>d</sub> BGP		0.2	0.6	1.2	μs
Burst Gate Pulse Width	PW BGP		2.7	3.7	4.7	μs
VCR SW Input Voltage	V <sub>CR</sub>			0.65	1.3	V
X-ray Protector Operation Voltage	V <sub>HD</sub>		0.55	0.65	0.75	V

NTSC 1 chip IC  
LA7655N



## LA7655N

Single-chip ICs (Table 1)

	LA7655N	LA7651P		LA7652N
		Pin 4 open	Pin 4 GND	
Applications	<ul style="list-style-type: none"> <li>• Mother type</li> <li>• Suited for general-purpose CTV</li> </ul>	<ul style="list-style-type: none"> <li>• High-grade AV set</li> </ul>	<ul style="list-style-type: none"> <li>• Intended for standard base band interface of CATV</li> </ul>	<ul style="list-style-type: none"> <li>• Making use of video signal during vertical blanking period (ex. automatic kine-bias)</li> </ul>
(Features)				
<ul style="list-style-type: none"> <li>• Quadratic differentiation input polarity</li> <li>• Vertical blanker</li> <li>• Black noise canceler</li> <li>• Screen brightness at no video signal input mode</li> <li>• IF AGC</li> <li>• ATT &amp; AF amp</li> </ul>	Positive  With With  ※1 Dark  With With	Negative  With With  ※2 Bright  With Without	Negative  With Without  ※3 Bright  Without Without	Positive  Without With  Dark  With With
Remarks	<ul style="list-style-type: none"> <li>• Advanced model of LA7650K</li> <li>• Pin-compatible with LA7650K, with no change required in applications</li> </ul>	<ul style="list-style-type: none"> <li>• Quadratic differentiation possible using external TR, L, C</li> </ul>	<ul style="list-style-type: none"> <li>• Can be used in conjunction with base band interface IC LA7970 to satisfy EIA IS-15.</li> </ul>	<ul style="list-style-type: none"> <li>• Vertical blanker removed for applications where video signal is required during vertical blanking period (ex. automatic kine-bias)</li> </ul>
Availability of sample (Schedule)	Available	January, 1989		Unfixed

\*1 Brightness uncontrollable. Can be made brighter by using an external circuit.

\*2 Brightness controllable

\*3 For availability of sample, contact our sales department.

Peripheral ICs (Table 2)

	Applications	Package	Features
LA7510	Quasi-parallel 1st SIF	SEP9	<ul style="list-style-type: none"> <li>• Quasi-parallel circuit configuration can be implemented simply by connecting a very small-sized SEP9.</li> </ul>
LA7970	CATV base band interface	DIP24S	<ul style="list-style-type: none"> <li>• Can be used in conjunction with LA7651P to satisfy complicated EIA IS-15 easily.</li> </ul>
LA7696	ON-screen display interface	DIP20S	<ul style="list-style-type: none"> <li>• External input... R, G, B graphic input (TTL level)</li> <li>• Black border function to make characters clear</li> <li>• Auto green circuit</li> </ul>
LA7760 LA7761	US stereo decoder	DIP30S DIP28S	<ul style="list-style-type: none"> <li>• US bilingual, stereo decoder</li> </ul>
LA7952 LA7953 and others	AV switch	SEP9 to DIP30S	<ul style="list-style-type: none"> <li>• Switch IC for external input</li> </ul>
LA7835 LA7836	Vertical output	SEP13II	<ul style="list-style-type: none"> <li>• Selected according to CRT size. Vertical output with ramp generator LA7835 1.8Ap-p LA7836 2.2Ap-p</li> </ul>