

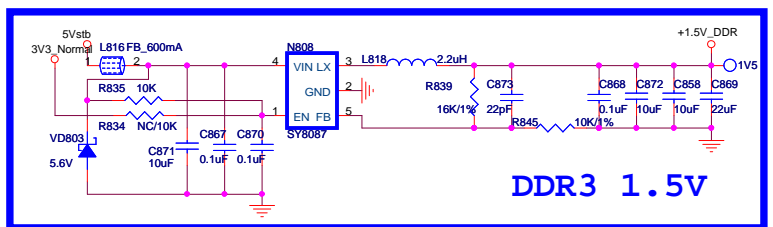
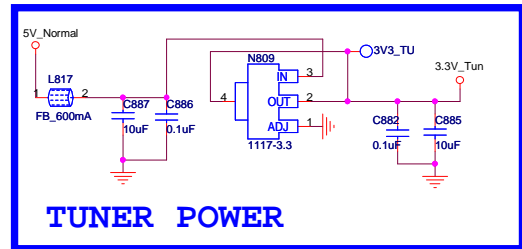
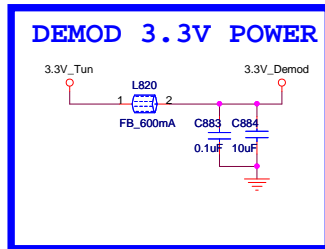
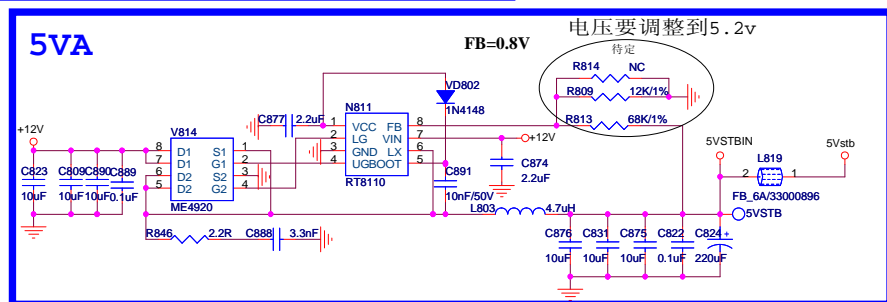
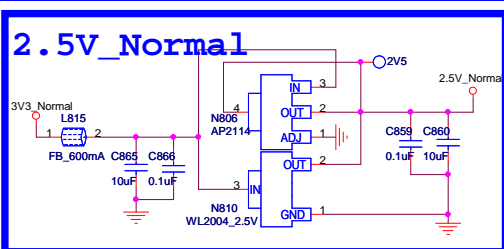
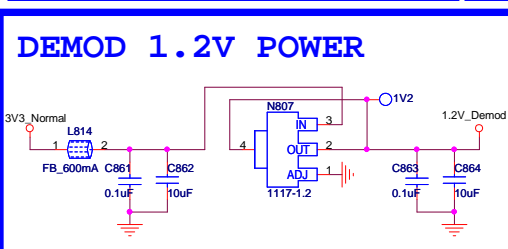
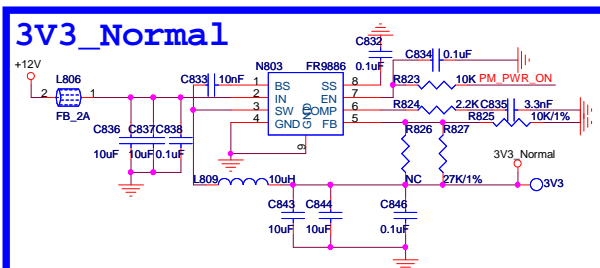
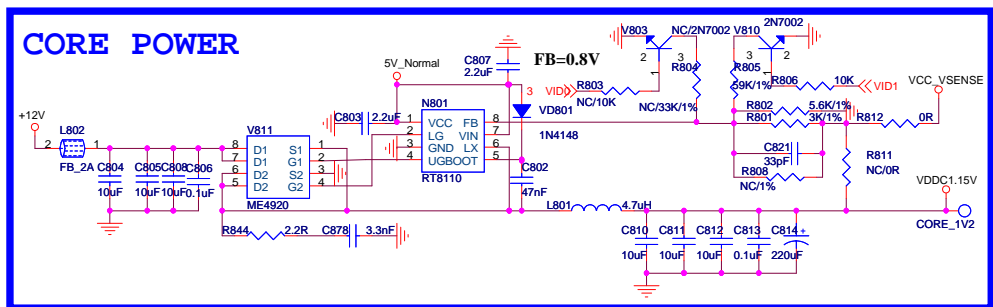
KONKA DTV SCH Design

KDL42XS715AN

拟制:

标准化:

批准:

[illegible]

Mode Selection

```

//I2S_OUT_M0,I2S_OUT_BCK_, I2S_OUT_MCK_, PAD_PWM01, PAD_PWM0
4'b000000 B01_M0_E0 (External SPI)
4'b000111 CA9_S0MCK_M0_E0
4'b001001 CA9_S01_M0_E0 (External SPI)
4'b001110 CA9_ROM_M0_E0
4'b011001 CA9_S0MCK_M0_E0

```

3V3_Nominal

3.3Vohm

SPI_S01

RESET

3.3Vusb
N515
RESET
VCC
C505 0.1uF
APX810
GND
R584 330K
100R
5Vusb
System RST
C506 0.1uF

Debug port

5Vusb
RA502 10K
100R
X5B11
K
C505-4A
UART-RX
UART-TX

LED_IR_KEY

3.3Vusb
X5B30 C505-4W
1
2
3
4
5
6
7
8
X5B27 NC-D1550-06
A
1
2
3
4
5
6
7
8
KEY0
C563 0.1uF
STB POWER
IR
LOGO POWER
3.3Vusb
V501 10K
V502 10K
R405 10K
R407 10K
R500 1K
R504
V504
V503
3.3Vusb
V50TBIN
R573 NC
R564 100pF
IR in R405 100R
IR SVNC
5V Normal
R500 1K
LOGO POWER
V501 10K
V502 10K
R504
POWER ON

LED_GREEN

3.3Vusb
R500 1K
LOGO POWER
V501 10K
V502 10K
R504
LED_GREEN R407 10K
V503
3.3Vusb
V50TBIN
R573 NC
R564 100pF
IR in R405 100R
IR SVNC
5V Normal
R500 1K
LOGO POWER
V501 10K
V502 10K
R504
POWER ON

[illegible]

Pin 1 to Pin 40 connection diagram for the 40-pin connector. The diagram shows two rows of pins with their respective functions and connections to the board. The top row (pins 1-20) includes functions like I/O, HP_MUTE, VDD, PANNEL, AMP_MUTE, PWM, and various control signals. The bottom row (pins 21-40) includes functions like HP_MUTE, VDD, PANNEL, AMP_MUTE, PWM, and various control signals. The diagram is color-coded with red and blue lines to indicate different connection types.

Pin 1 to Pin 40 Connection Table:

Pin	Function	Connection
1	I/O_R	R670 4.7K
2	GPIOA	R671 4.7K
3	HP_AUJ	R672 4.7K
4	M_SCL	R661 4.7K
5	M_SDA	R662 4.7K
6	Power_RST	R663 4.7K
7	Power_RST	R664 4.7K
8	LVDS_SEL	R665 4.7K
9	LVDS_SEL	R666 4.7K
10	LVDS_EN	R667 4.7K
11	LVDS_EN	R668 4.7K
12	LVDS_EN	R669 4.7K
13	LVDS_EN	R673 4.7K
14	LVDS_EN	R674 4.7K
15	LVDS_EN	R675 4.7K
16	LVDS_EN	R676 4.7K
17	LVDS_EN	R677 4.7K
18	LVDS_EN	R678 4.7K
19	LVDS_EN	R679 4.7K
20	LVDS_EN	R680 4.7K
21	HP_MUTE	R649 4.7K
22	HP_MUTE	R650 4.7K
23	VDD	R651 4.7K
24	PANNEL	R652 4.7K
25	PANNEL	R653 4.7K
26	AMP_MUTE	R654 4.7K
27	AMP_MUTE	R655 4.7K
28	PWM	R656 4.7K
29	PWM	R657 4.7K
30	PWM	R658 4.7K
31	PWM	R659 4.7K
32	PWM	R660 4.7K
33	PWM	R661 4.7K
34	PWM	R662 4.7K
35	PWM	R663 4.7K
36	PWM	R664 4.7K
37	PWM	R665 4.7K
38	PWM	R666 4.7K
39	PWM	R667 4.7K
40	PWM	R668 4.7K

Pin 1 to Pin 40 Connection Table (continued):

Pin	Function	Connection
41	HP_MUTE	R649 4.7K
42	HP_MUTE	R650 4.7K
43	VDD	R651 4.7K
44	PANNEL	R652 4.7K
45	PANNEL	R653 4.7K
46	AMP_MUTE	R654 4.7K
47	AMP_MUTE	R655 4.7K
48	PWM	R656 4.7K
49	PWM	R657 4.7K
50	PWM	R658 4.7K
51	PWM	R659 4.7K
52	PWM	R660 4.7K
53	PWM	R661 4.7K
54	PWM	R662 4.7K
55	PWM	R663 4.7K
56	PWM	R664 4.7K
57	PWM	R665 4.7K
58	PWM	R666 4.7K
59	PWM	R667 4.7K
60	PWM	R668 4.7K

Pin 1 to Pin 40 Connection Table (continued):

Pin	Function	Connection
61	HP_MUTE	R649 4.7K
62	HP_MUTE	R650 4.7K
63	VDD	R651 4.7K
64	PANNEL	R652 4.7K
65	PANNEL	R653 4.7K
66	AMP_MUTE	R654 4.7K
67	AMP_MUTE	R655 4.7K
68	PWM	R656 4.7K
69	PWM	R657 4.7K
70	PWM	R658 4.7K
71	PWM	R659 4.7K
72	PWM	R660 4.7K
73	PWM	R661 4.7K
74	PWM	R662 4.7K
75	PWM	R663 4.7K
76	PWM	R664 4.7K
77	PWM	R665 4.7K
78	PWM	R666 4.7K
79	PWM	R667 4.7K
80	PWM	R668 4.7K

Pin 1 to Pin 40 Connection Table (continued):

Pin	Function	Connection
81	HP_MUTE	R649 4.7K
82	HP_MUTE	R650 4.7K
83	VDD	R651 4.7K
84	PANNEL	R652 4.7K
85	PANNEL	R653 4.7K
86	AMP_MUTE	R654 4.7K
87	AMP_MUTE	R655 4.7K
88	PWM	R656 4.7K
89	PWM	R657 4.7K
90	PWM	R658 4.7K
91	PWM	R659 4.7K
92	PWM	R660 4.7K
93	PWM	R661 4.7K
94	PWM	R662 4.7K
95	PWM	R663 4.7K
96	PWM	R664 4.7K
97	PWM	R665 4.7K
98	PWM	R666 4.7K
99	PWM	R667 4.7K
100	PWM	R668 4.7K

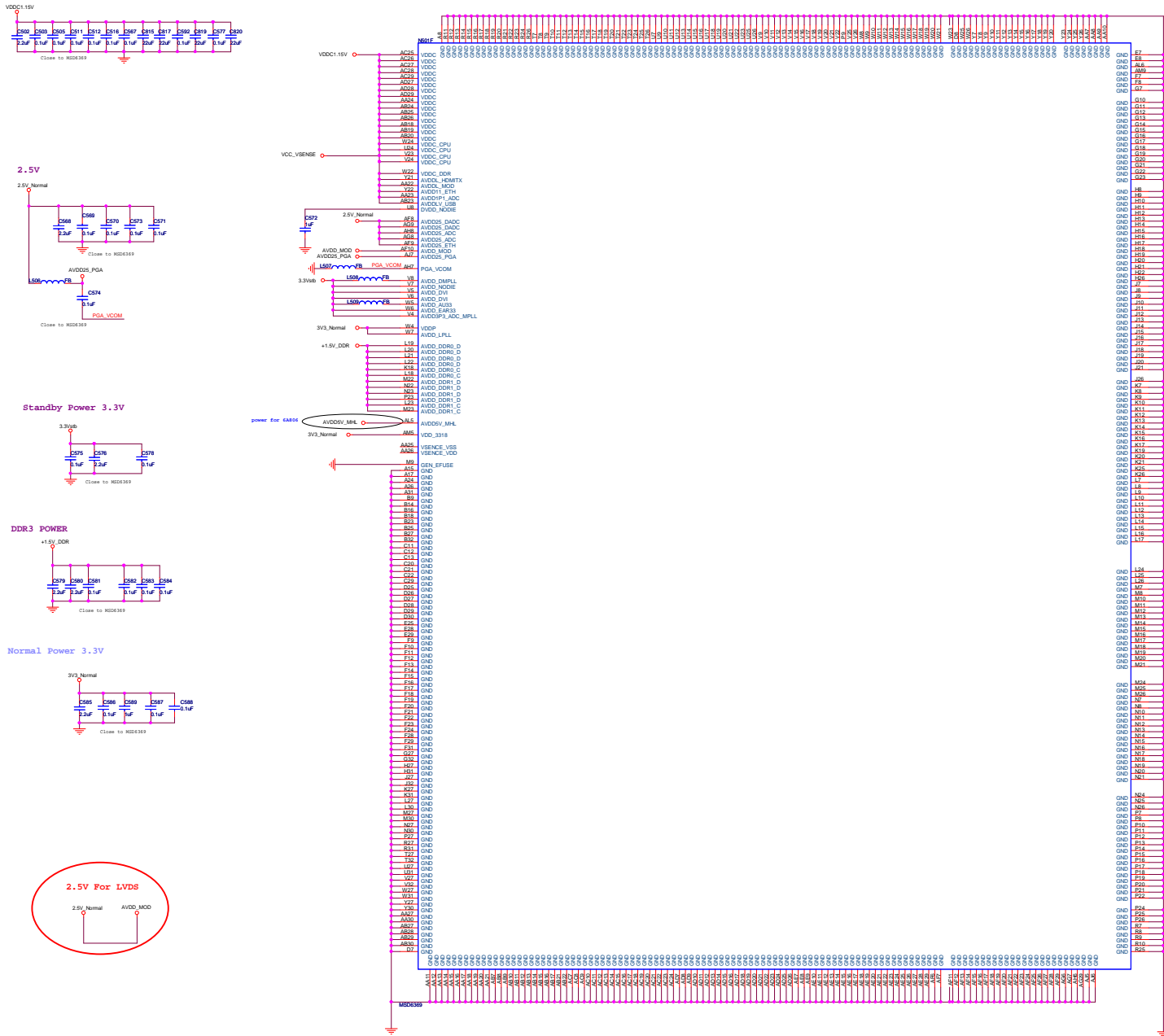
Pin 1 to Pin 40 Connection Table (continued):

Pin	Function	Connection
101	HP_MUTE	R649 4.7K
102	HP_MUTE	R650 4.7K
103	VDD	R651 4.7K
104	PANNEL	R652 4.7K
105	PANNEL	R653 4.7K
106	AMP_MUTE	R654 4.7K
107	AMP_MUTE	R655 4.7K
108	PWM	R656 4.7K
109	PWM	R657 4.7K
110	PWM	R6

[illegible]

				拟制		02.MSD6369(RGB/C/HDMI)
				审核		
标记数量	更改单号	签名	日期			第 2 张

Power interface



					拟制		03.MSD6369(Power/GND)
					审核		
标记	数量	更改单号	签名	日期			第 3 张

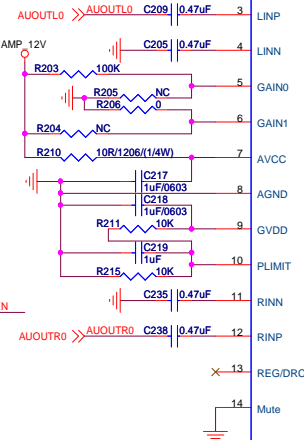
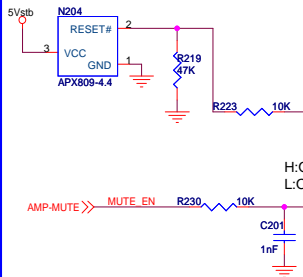
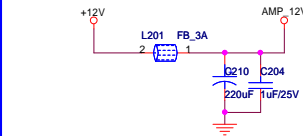
VIDEO OUT

AV/YPbPr

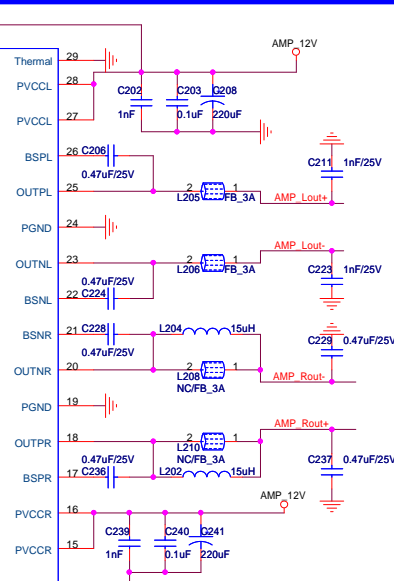
					拟制		04.YPbPr&AV
					审核		
标记	数量	更改单号	签名	日期			第 4 张

AMP

GAIN0	GAIN1	GAIN (dB)
0	0	20
0	1	26
1	0	32
1	1	36



N202
RT9108
注意焊盘

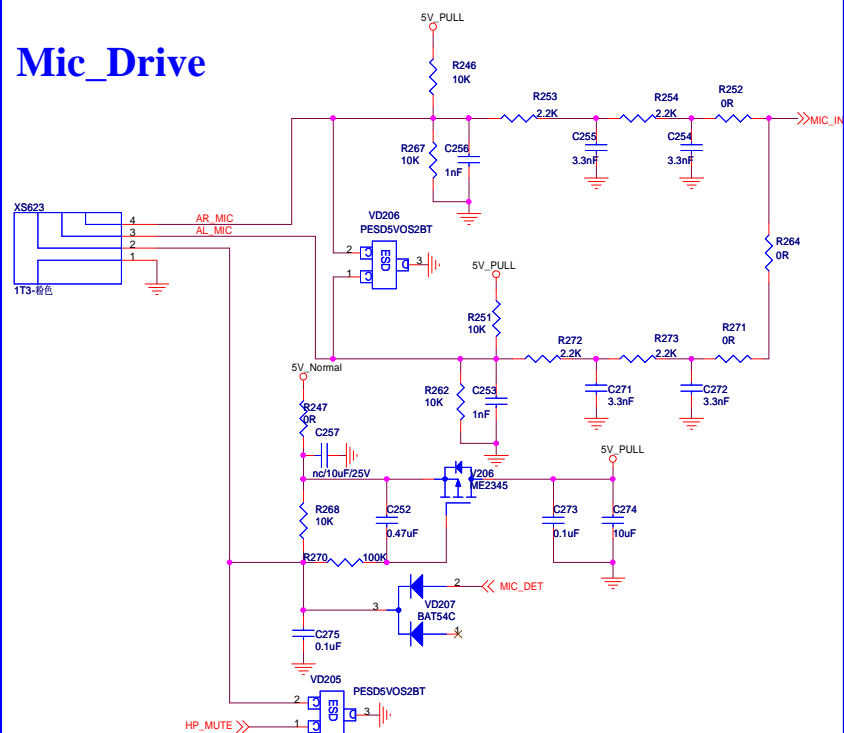


8欧姆喇叭
XS201

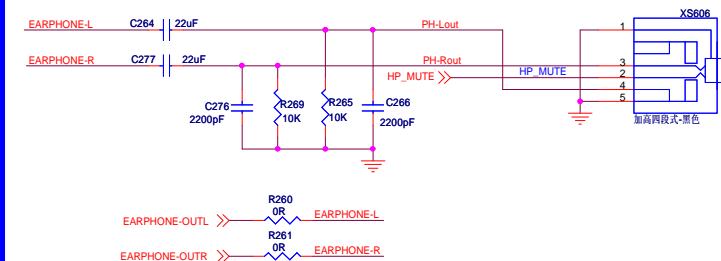
AMP_Rout+ 1
AMP_Rout- 2
AMP_Lout+ 3
AMP_Lout- 4

D2006-4W

Mic_Drive



EAR PHONE

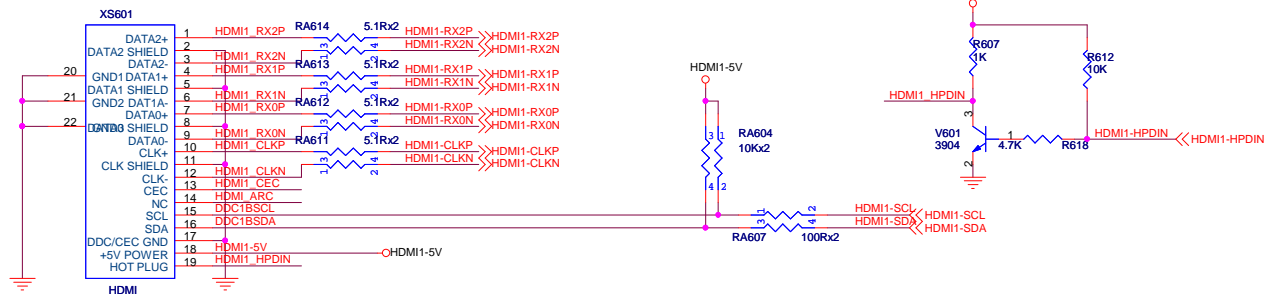


					拟制	
					审核	
标记	数量	更改单号	签名	日期		

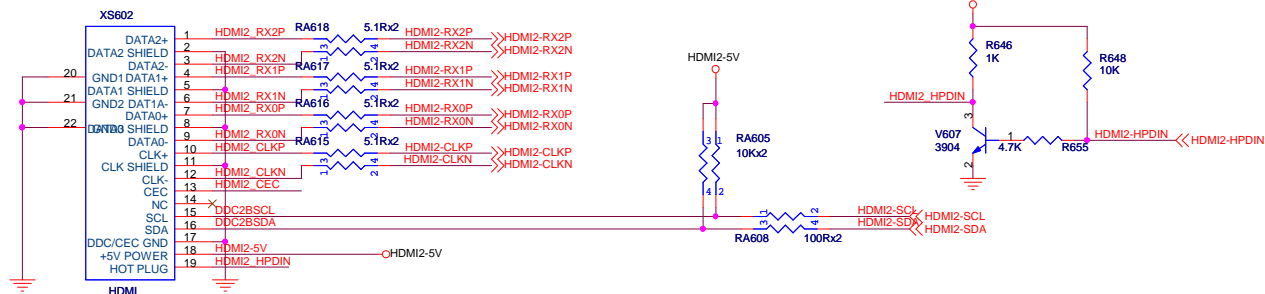
05.Amp

第 5 张

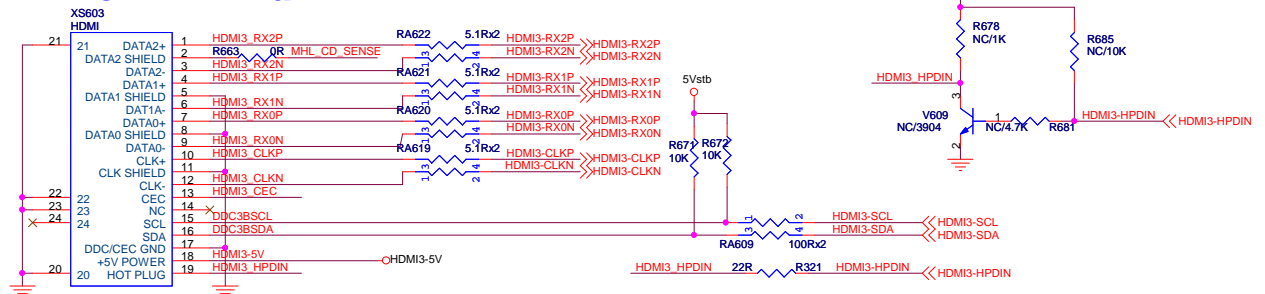
2st HDMI & ARC



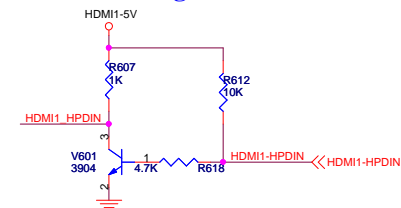
3rd HDMI



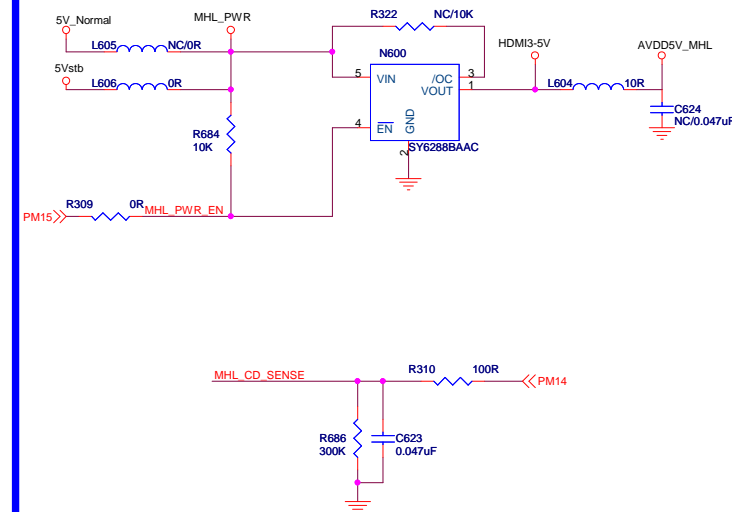
1th HDMI & MHL



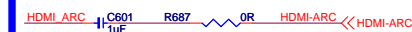
Hot-Plug Control



MHL for MSD6369



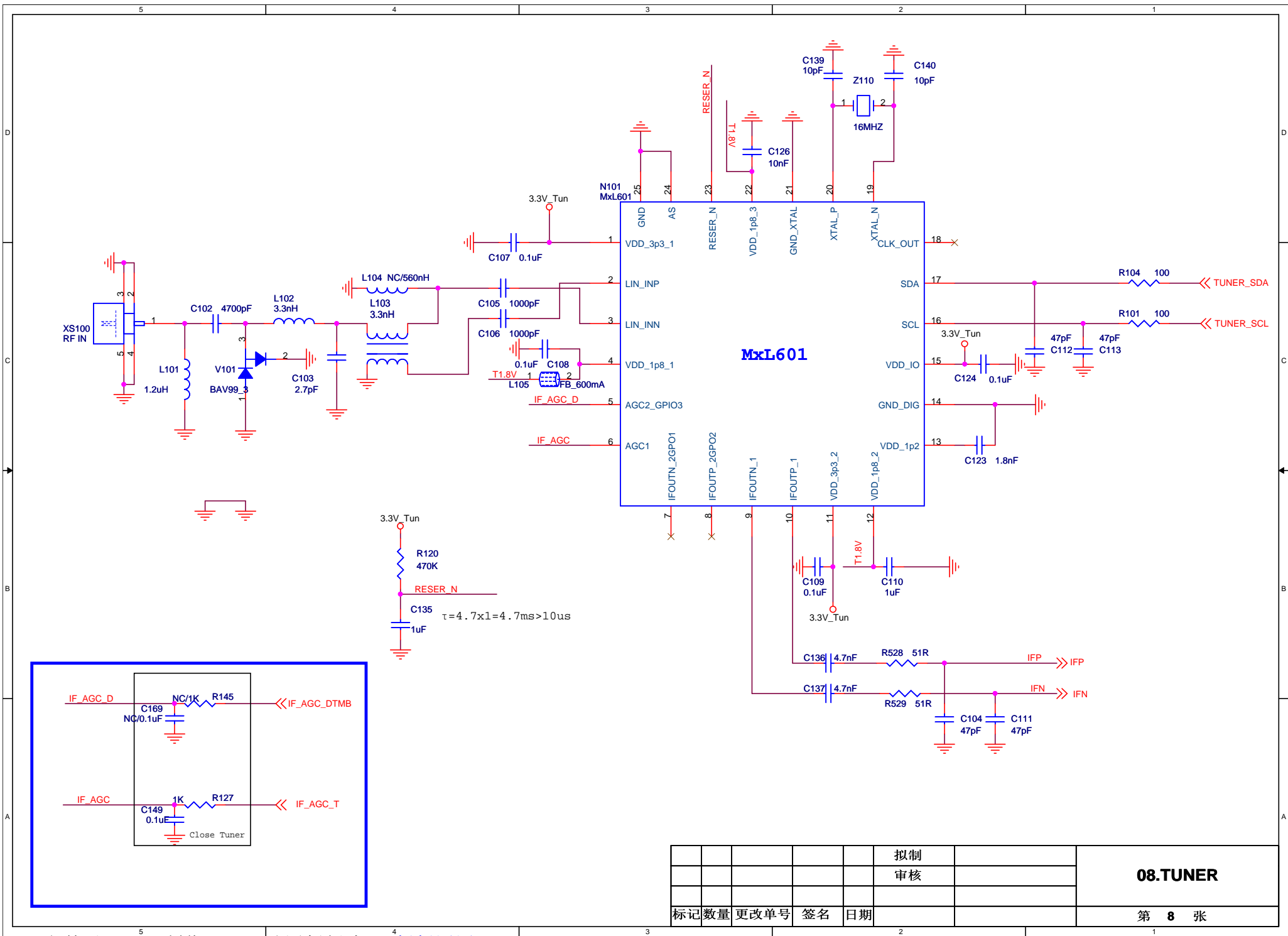
ARC



For CEC



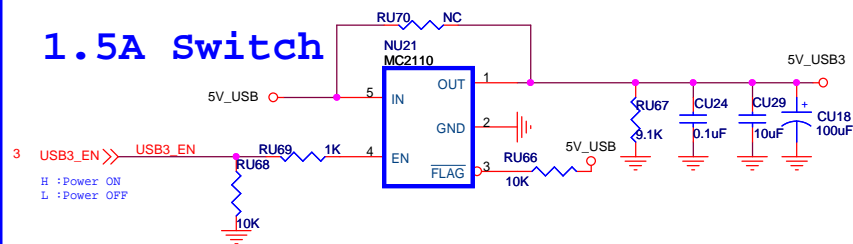
					拟制		06.HDMI
					审核		
标记	数量	更改单号	签名	日期			第 6 张



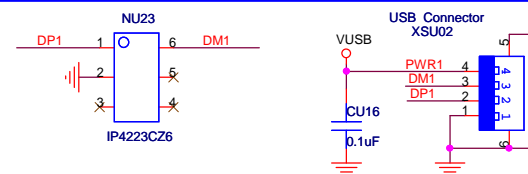
USB POWER



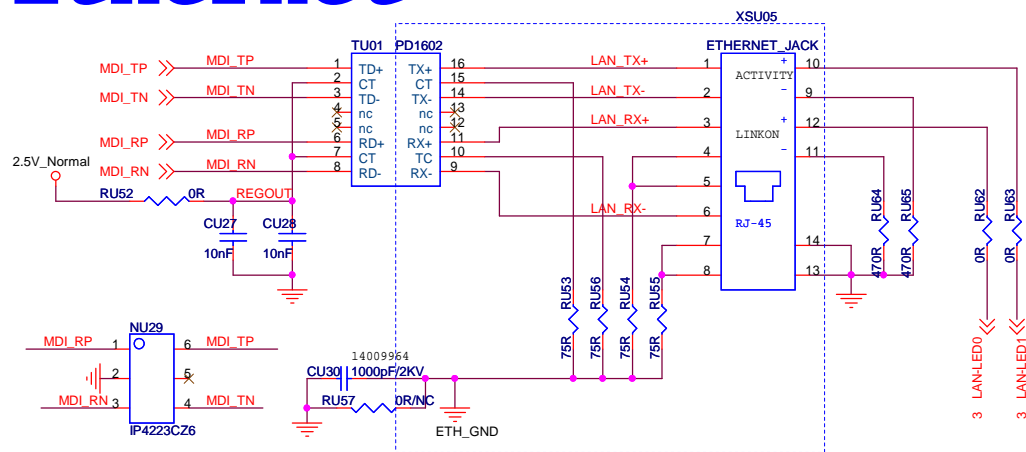
1.5A Switch



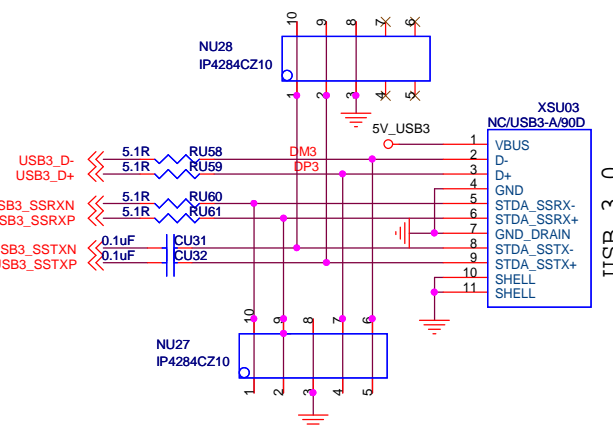
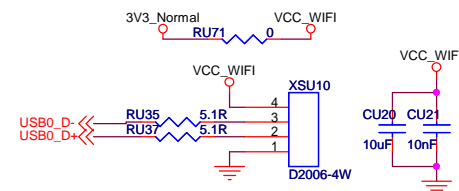
Note: 电容靠近USB(XSU04)端子,提供usb读写稳定性



Ethernet

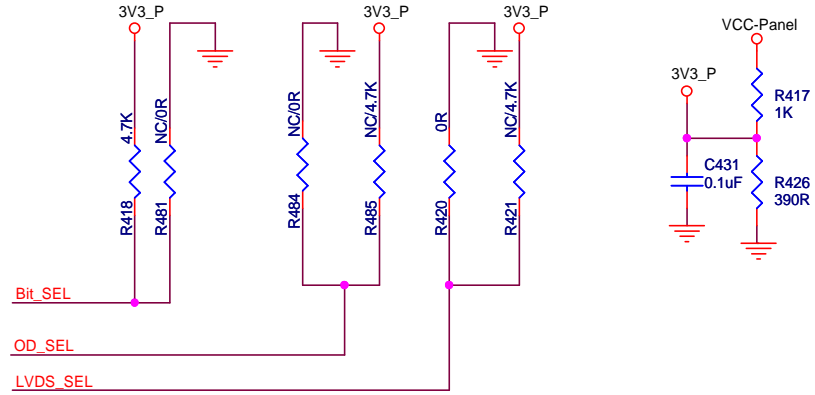
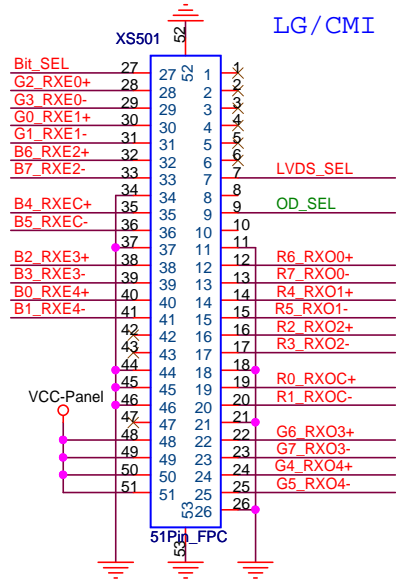


Wifi

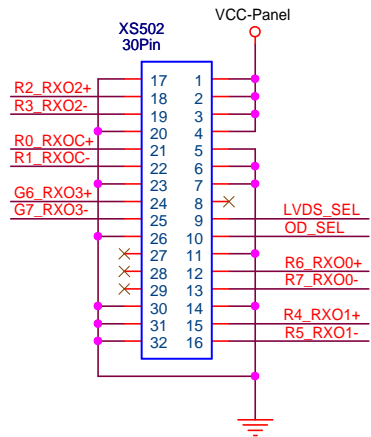
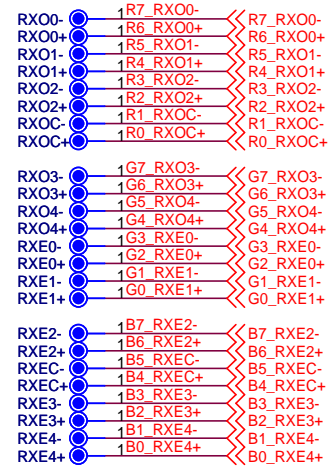


					拟制		09.USB & Ethernet
					审核		
标记	数量	更改单号	签名	日期			第 9 张

FULL HD FFC

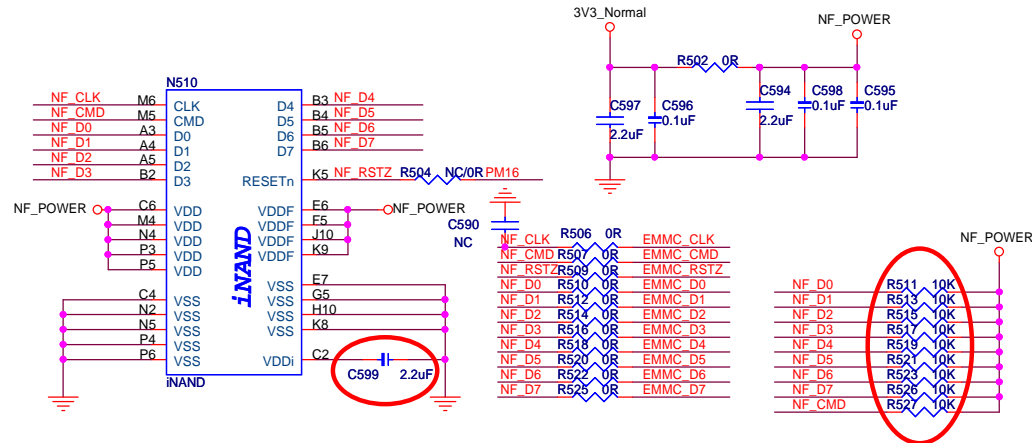


HD FFC

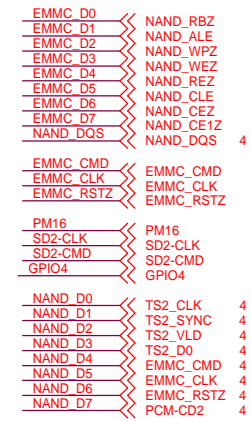
Net to other page

					拟制		10.LVDS
					审核		
标记	数量	更改单号	签名	日期			第 10 张

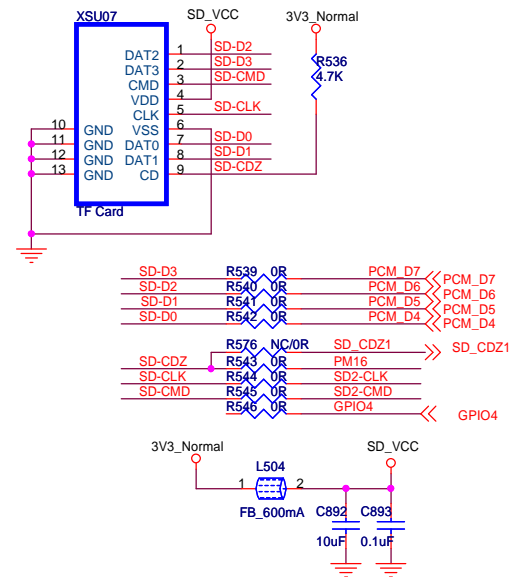
iNAND FLASH



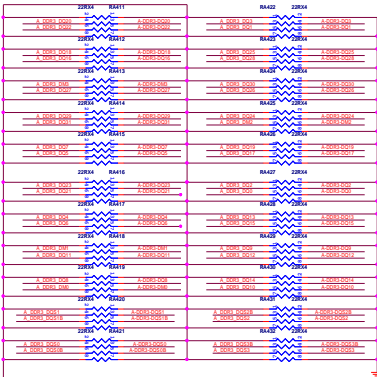
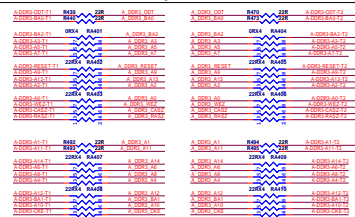
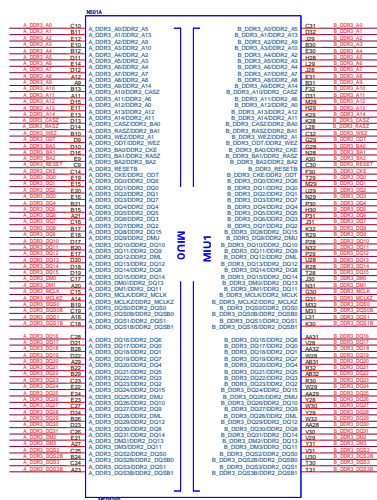
Net



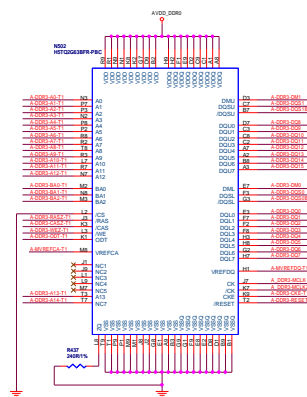
TF Socket



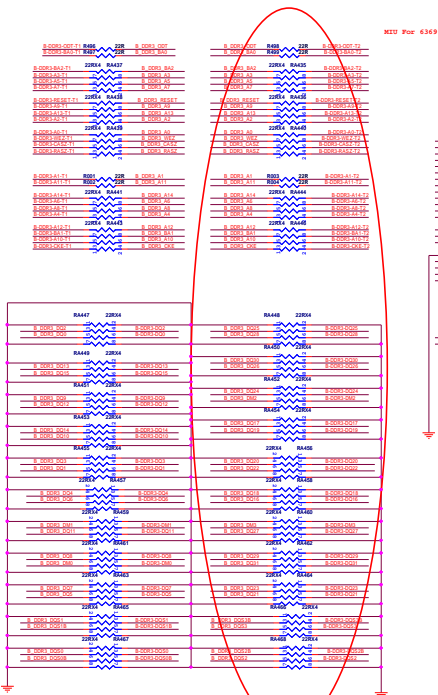
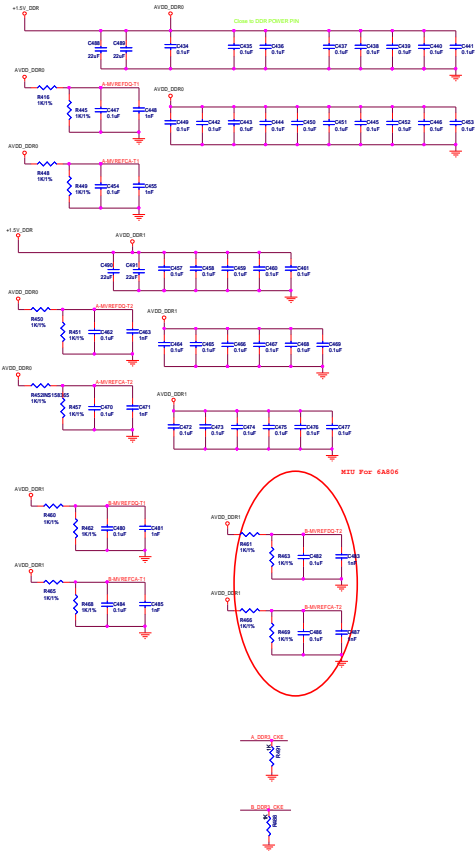
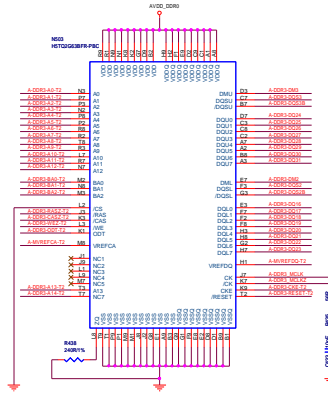
					拟制		13.NAND FLASH/SD
					审核		
标记	数量	更改单号	签名	日期			第 13 张



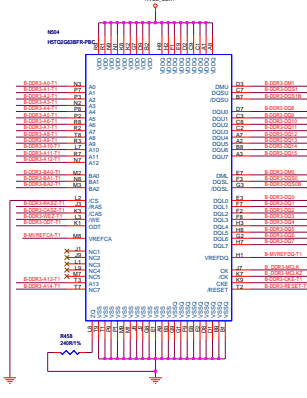
DOR3 2G bit 1600MHz



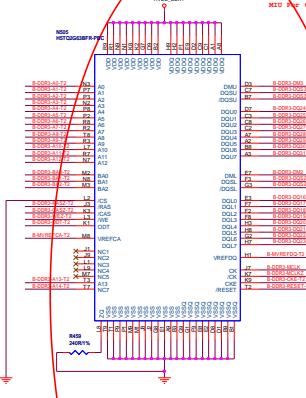
DOR3 2G bit 1600MHz

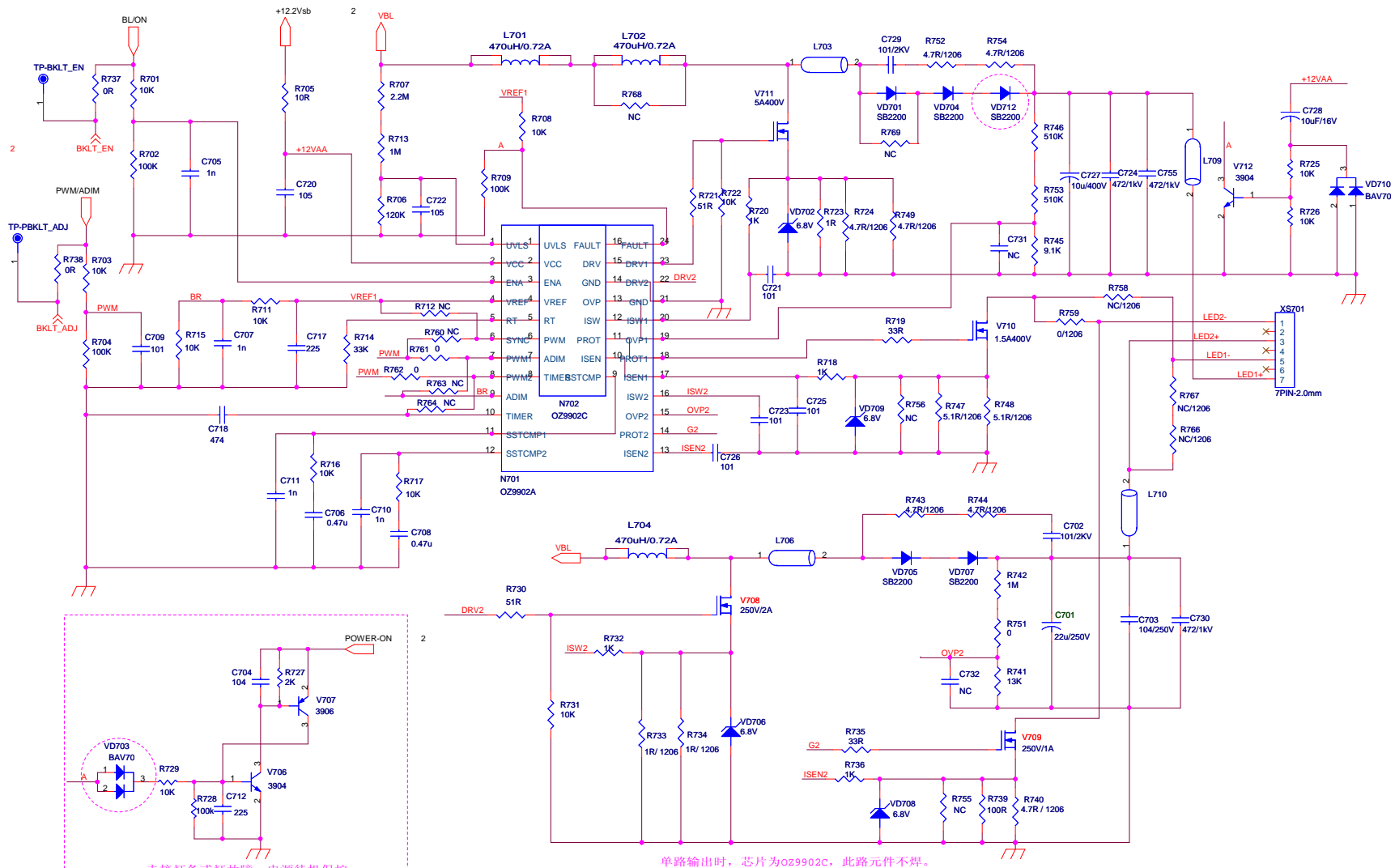


DOR3 2G bit 1600MHz



DOR3 2G bit 1600MHz





未接灯条或灯故障，电源待机保护。

单路输出时，芯片为OZ9902C，此路元件不焊。

					拟制		
					审核		
标记	数量	更改单号	签名	日期			16.Backlight Driver
							第 16 张